POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	:	CDE012
SAMPLE CODE	:	SH800480T032-ZHC15
MASS PRODUCTION CODE	:	PH800480T032-ZHC15
PCAP FIRMWARE VERSION		ZHC12_7_231128_ILITEK_20231130.hex
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	002
DRAWING NO. (Ver.)	:	LMD-PH800480T032-ZHC15 (Ver.001)
PACKAGING NO. (Ver.)	:	PKG-PH800480T032-ZHC15 (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
廖志豪 Rex Liao	陳宗淇 Howard Chen	張慶源 Yuan Chang

POWERTIP
2024.03.14
TW RD APR.

□ Preliminary specification for design input

■ Specification for sample approval

POWERTIP TECH. CORP.

Headquarters:

No.8, 6th Road, Taichung Industrial Park,
Taichung, Taiwan
台中市 407 工業區六路 8 號

TEL: 886-4-2355-8168
FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw
Http://www.powertip.com.tw

History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
11/16/2023	01	001	Preliminary.	-	Yuan
03/14/2024	01	002	For customer Request Modify ADD PCAP FIRMWARE VERSION Modify Color of CIE Coordinate Modify PCAP Firmware Information	- Cover Page 6 10	Yuan

Contents

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Resolution	800 *3 (RGB) * 480 Dots
LCD Type	Full Viewing Angle, Normally Black, Transmissive type
Screen size(inch)	7 inch
Surface treatment	Anti-Glare
Color configuration	R.G.B. Vertical Stripe
Interface	Parallel RGB (Data), SPI (Configuration), DE mode
Driver IC	Himax---HX8249-A02 Himax---HX8678-C
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website: http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	188.0 (W) * 127.04 (L) * 8.9 (H)	mm

LCD panel

Item	Standard Value	Unit
View Area	153.40 (W) * 92.44 (L)	mm
Active Area	152.40 (W) * 91.44 (L)	mm

Note: For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power Supply for TFT Panel	V_{DD}	GND=0V	-0.3	3.96	V	-
Power Supply for Backlight Unit	V_{CC}	GND=0V	-0.3	+20.0	V	
Operating Temperature	T_{OP}	-	-20	+70	°C	
Storage Temperature	T_{ST}	-	-30	+80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply for TFT Panel	VDD	GND=0V	3.0	3.3	3.6	V
Power Supply for Backlight Unit	VCC	GND=0V	5	12	15	V
Input Voltage for TFT Panel	V_{IH}	GND=0V	0.7VDD	-	VDD	V
	V_{IL}	GND=0V	0	-	0.3VDD	
Supply Current for TFT Panel(RMS)	IDD	IDD@VDD=3.3V	-	30	45	mA
Peak Supply Current for TFT Panel	IDD	Full screen white	-	-	450	mA
Supply Current for Backlight Unit	ICC	ICC@VCC=5V	-	0.8	1.2	A
Supply Current for Backlight Unit	ICC	ICC@VCC=12V	-	0.3	0.45	
Input Voltage for PWM Signal	VPH	GND=0V	1.2	-	-	V
	VPL	GND=0V	-	-	0.4	V
Dimming Clock Rate	fP	GND=0V	0.1	-	8	KHz

1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

Item	Symbol		Condition	Min.	Typ.	Max.	unit	
Response time	Tr+Tf		Ta = 25°C θX, θY = 0°	-	30	45	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10	-	80	-	Deg.	Note 4
	Bottom	θY-		-	80	-		
	Left	θX-		-	80	-		
	Right	θX+		-	80	-		
Contrast ratio		CR	Ta = 25°C θX , θY = 0°	650	800	-	-	Note 3
Color of CIE Coordinate (With B/L)	White	X		0.24	0.29	0.34	-	Note1
		Y		0.27	0.32	0.37		
	Red	X		0.58	0.63	0.68		
		Y		0.31	0.36	0.41		
	Green	X		0.25	0.30	0.35		
		Y		0.59	0.64	0.69		
	Blue	X		0.07	0.12	0.17		
		Y		0.01	0.06	0.11		
Average Brightness Pattern=white display (With LCD)*2	IV		VCC=12.0V PWM="High" (Duty=100%)	650	800	-	cd/m^2	Note1
Uniformity (With LCD)*1	△B		VCC=12.0V PWM="High" (Duty=100%)	70	-	-	%	Note1

Note 1:

*1: △B=B(min) / B(max) * 100%

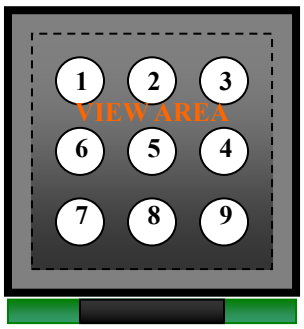
*2: Measurement Condition for Optical Characteristics:

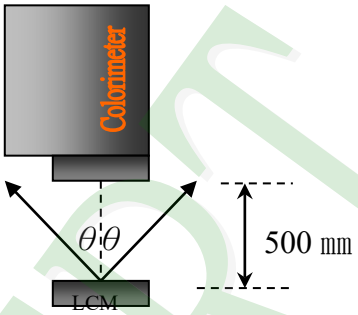
a: Environment: 25℃±5℃ / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 ㎜, (θ= 0°)

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

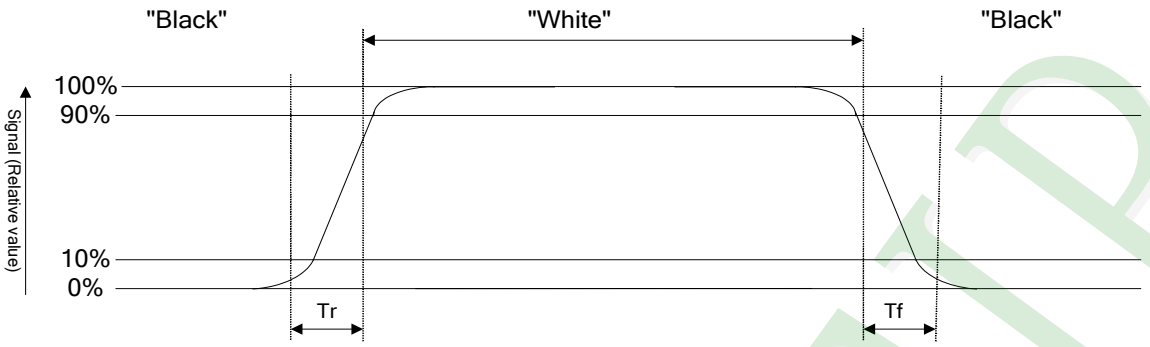
Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white” (falling time) and from “white” to “black” (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White

Normally Black



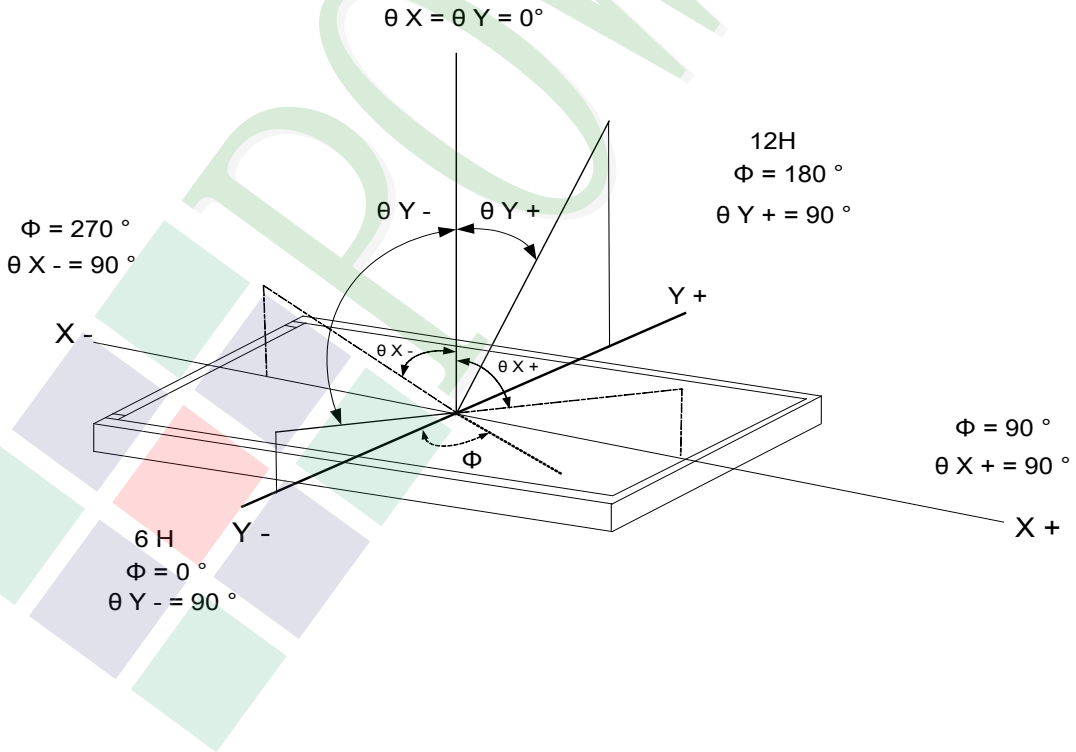
Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at “White” state}}{\text{Photo detector output when LCD is at “Black” state}}$$

Note 4: Definition of viewing angle:

Refer to figure as below:



1.6 Backlight Characteristics

Maximum Ratings

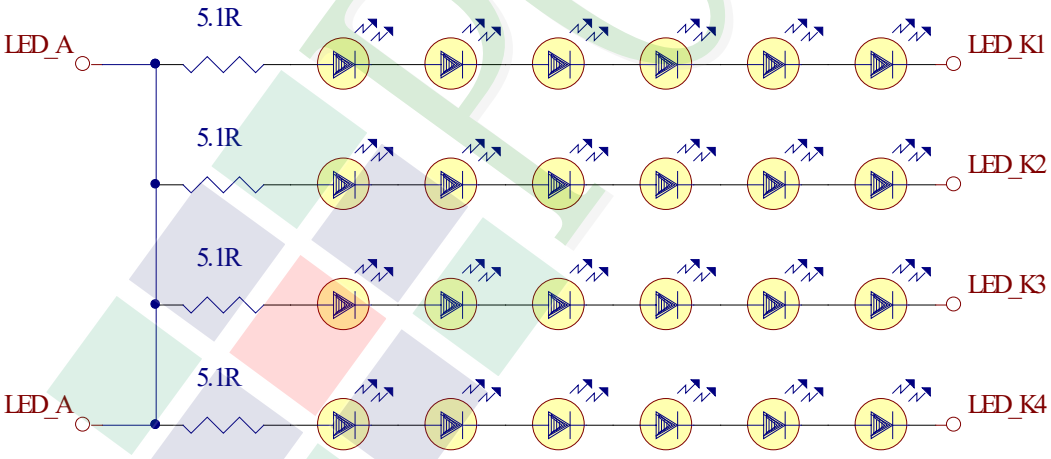
Item	Symbol	Min.	Max.	Unit	Remark
LED Forward Current	I_F	-	600	mA	Per
LED Reverse Voltage	V_R	-	1.2	V	

Electrical / Optical Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
LED Voltage	V_L	14.9	16.6	19.1	V	Note1
LED Current	I_L	-	200	-	mA	-
LED life time	-	50,000	-	-	H_r	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L =200 mA

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and I_L =200 mA. The LED life time could be decreased if operating I_L is larger than 200mA



1.7 Touch Panel Characteristics

1.7.1 Features

Item	Standard Value
Touch Panel Size	7”
Touch type	True Multi-Touch Capacitive Touch Panel
Input Method	True Multi-touch with up to 5 Points of Absolution X and Y Coordinates
Output Interface	I²C
IC	ILITEK--ILI2130

1.7.2 I²C Address

Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
1	0	0	0	0	0	1	**R/W**

1.7.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	unit
Supply voltage	TPVDD	-	-0.3	3.63	V
Operating Temperature	TOP	Non condensing	-20	+70	°C
Storage Temperature	TST	Non condensing	-30	+80	°C

1.7.4 DC Electrical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	unit
Power Supply Voltage	TPVDD	-	-	3.3	-	-

1.7.5 Optical Characteristics

Item	Standard Value	unit
Total light transmittance	85% or more	-
Hardness Of Surface	≥7H	-

PCAP Firmware Information

File: ZHC12_7_231128_ILITEK_20231130.hex

SHA256: 6B962363A15ED36D4A2B5F98C5744D88A268D577CD082BB5F6141E146C573907

Remark: None

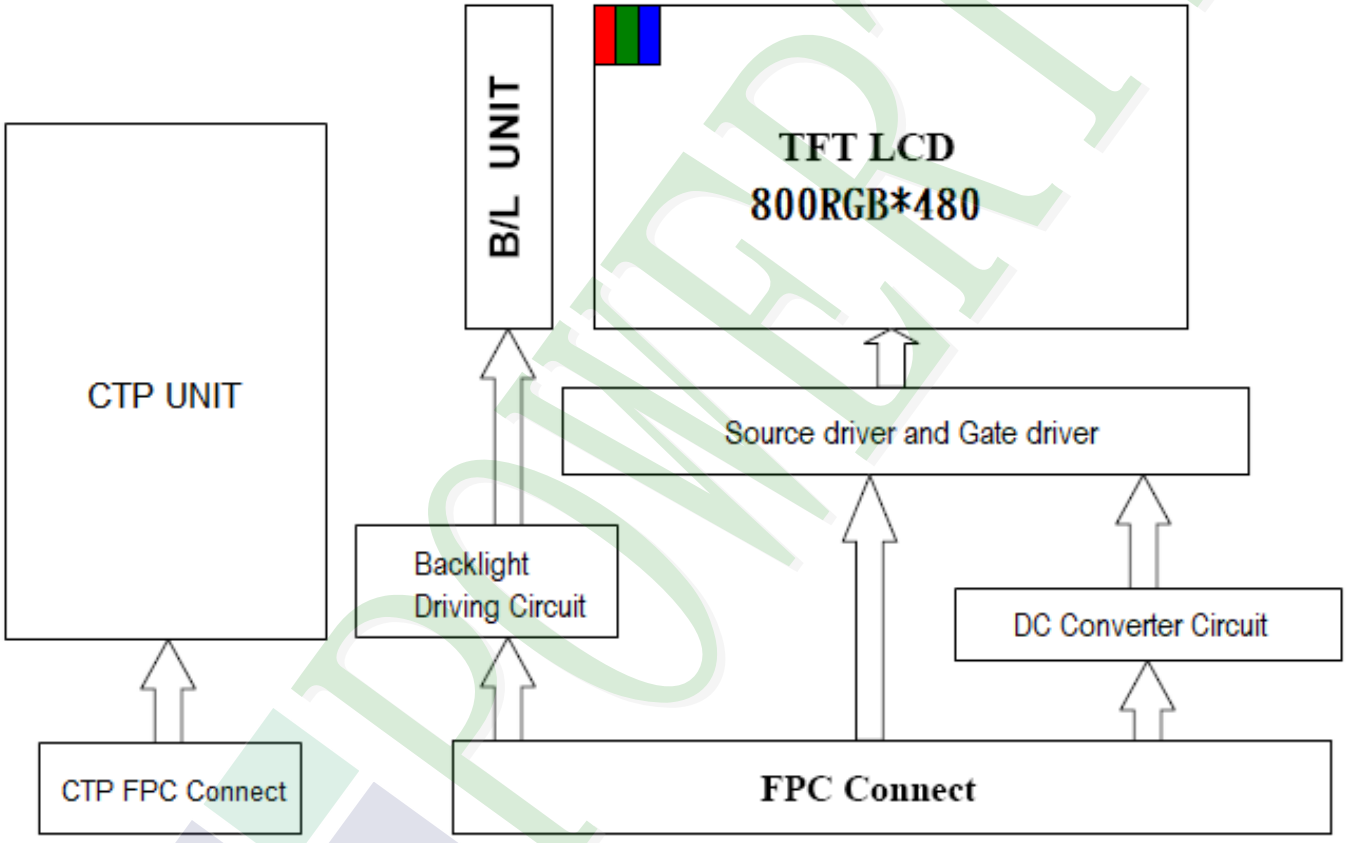
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



2.2 Interface Pin Description

TFT LCM Interface

Pin#	Name	Description
1	GND	Power ground.
2	VDD	Power for Digital Circuit.
3	VDD	Power for Digital Circuit.
4	VCC	Power For LED backlight.
5	VCC	Power For LED backlight.
6	PWM	Shutdown & Dimming control input for backlight. Do not allow this pin to float. “Hi” =100%, “Low” = 0%.
7	GND	Power ground.
8	R0	Red Data.
9	R1	Red Data.
10	R2	Red Data.
11	R3	Red Data.
12	GND	Power ground.
13	R4	Red Data.
14	R5	Red Data.
15	R6	Red Data.
16	R7	Red Data.
17	GND	Power ground.
18	G0	Green Data.
19	G1	Green Data.
20	G2	Green Data.
21	G3	Green Data.
22	GND	Power ground.
23	G4	Green Data.
24	G5	Green Data.
25	G6	Green Data.
26	G7	Green Data.
27	GND	Power ground.
28	B0	Blue Data.
29	B1	Blue Data.

Pin#	Name	Description
30	B2	Blue Data.
31	B3	Blue Data.
32	GND	Power ground.
33	B4	Blue Data.
34	B5	Blue Data.
35	B6	Blue Data.
36	B7	Blue Data.
37	GND	Power ground.
38	HS	Line synchronization signal. Horizontal Sync Input.
39	VS	Frame synchronization signal. Vertical Sync Input.
40	GND	Power ground.
41	DE	Data Enable
42	GND	Power ground.
43	DCLK	Sample clock. Data will be latched at the falling edge of DCLK.
44	GND	Power ground.
45	CS / ID1	Serial communication chip selection/ID[4:1]These pins select LCM type. See NOTE1
46	SDIN / ID2	Serial communication data/ ID[4:1]These pins select LCM type. See NOTE1
47	SCK / ID3	Serial communication clock/ ID[4:1]These pins select LCM type. See NOTE1
48	DISPLAY CONTROL / ID4	Display Enable(Hi Active)./ ID[4:1]These pins select LCM type. See NOTE1
49	/RESET	Global Reset (Low Active).
50	GND	Power ground.

Note1:

ID Pins Definition:

	PIN 45 ID1	PIN 46 ID2	PIN 47 ID3	PIN 48 ID4
3.5” Module	X	0	0	X
4.3” Module	X	1	0	X
5.0” Module	X	0	1	X
7.0” Module	X	1	1	X

1. Resistor = 10k ohm
2. “X” = No use

Touch Panel interface

Pin#	Name	Description
1	GND	Ground.
2	TPVDD	Power Supply Voltage (3.3V)
3	SCL	I2C Clock
4	SDA	I2C Data
5	INT	Active Low
6	RESET	Active low global reset signal input.

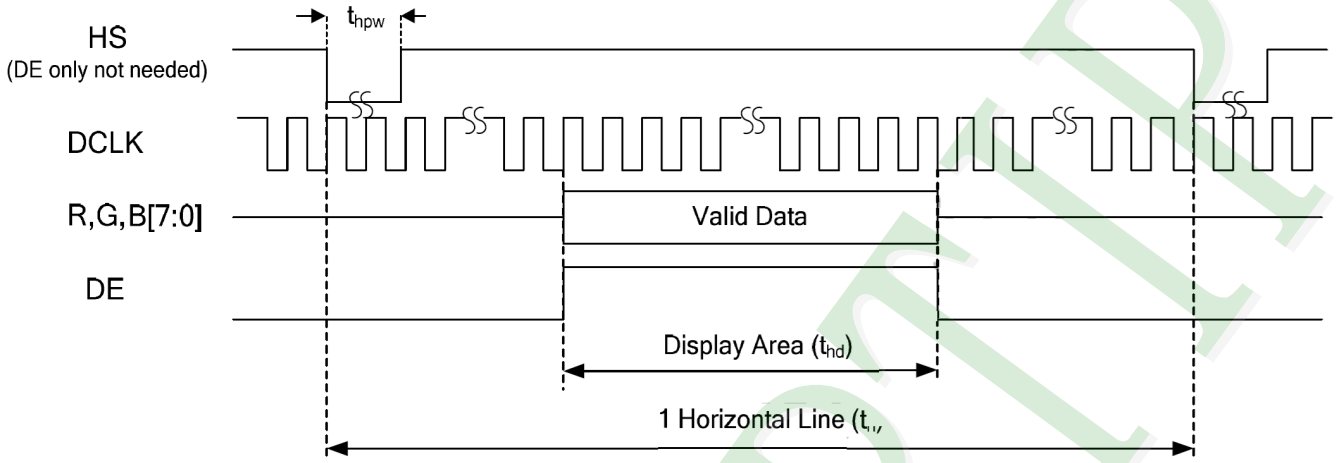
2.3 Timing Characteristics

2.3.1 RGB Mode Selection Table

RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
DE Mode	Input	GND	GND	Input

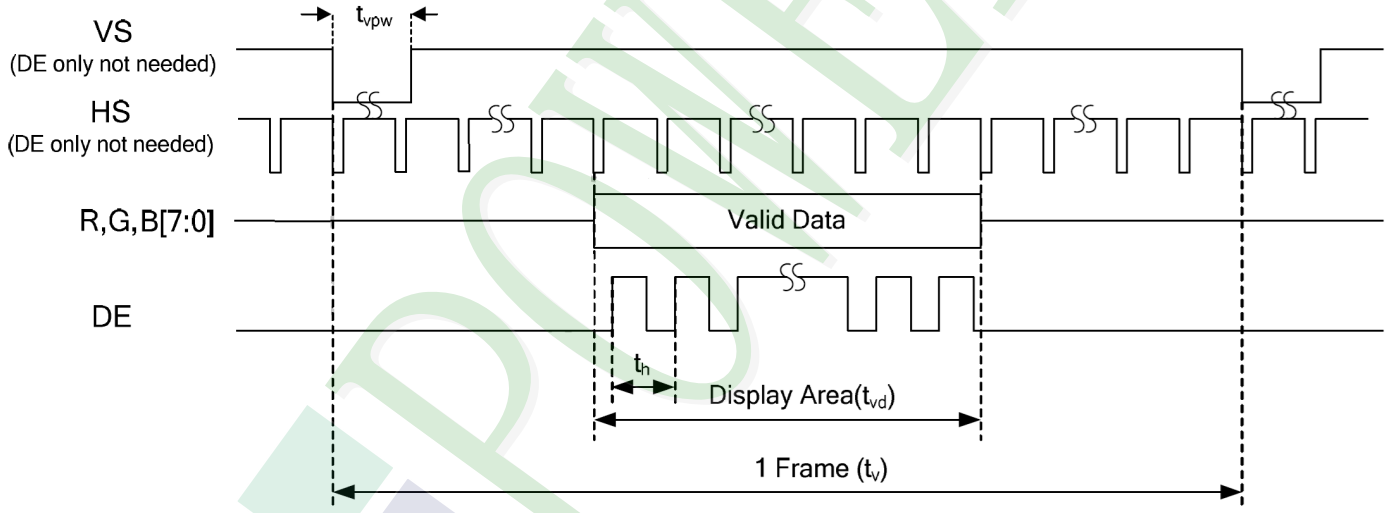
2.3.2 Parallel RGB DE Mode

- **Horizontal**



Horizontal input timing at DE only mode

- Vertical



Vertical input timing at DE only mode

2.3.3 Parallel 24-bit RGB Input Timing

Item	Symbol	Min	Typ.	Max	Unit	Note
DCLK Frequency	FDCLK	25.2	27.2	30.5	MHz	-
Horizontal valid data	thd		800		-	-
1 horizontal line	th	856	860	920	DCLK	-
Vertical valid data	tvd		480		-	-
1 vertical field	tv	490	528	552	H	-

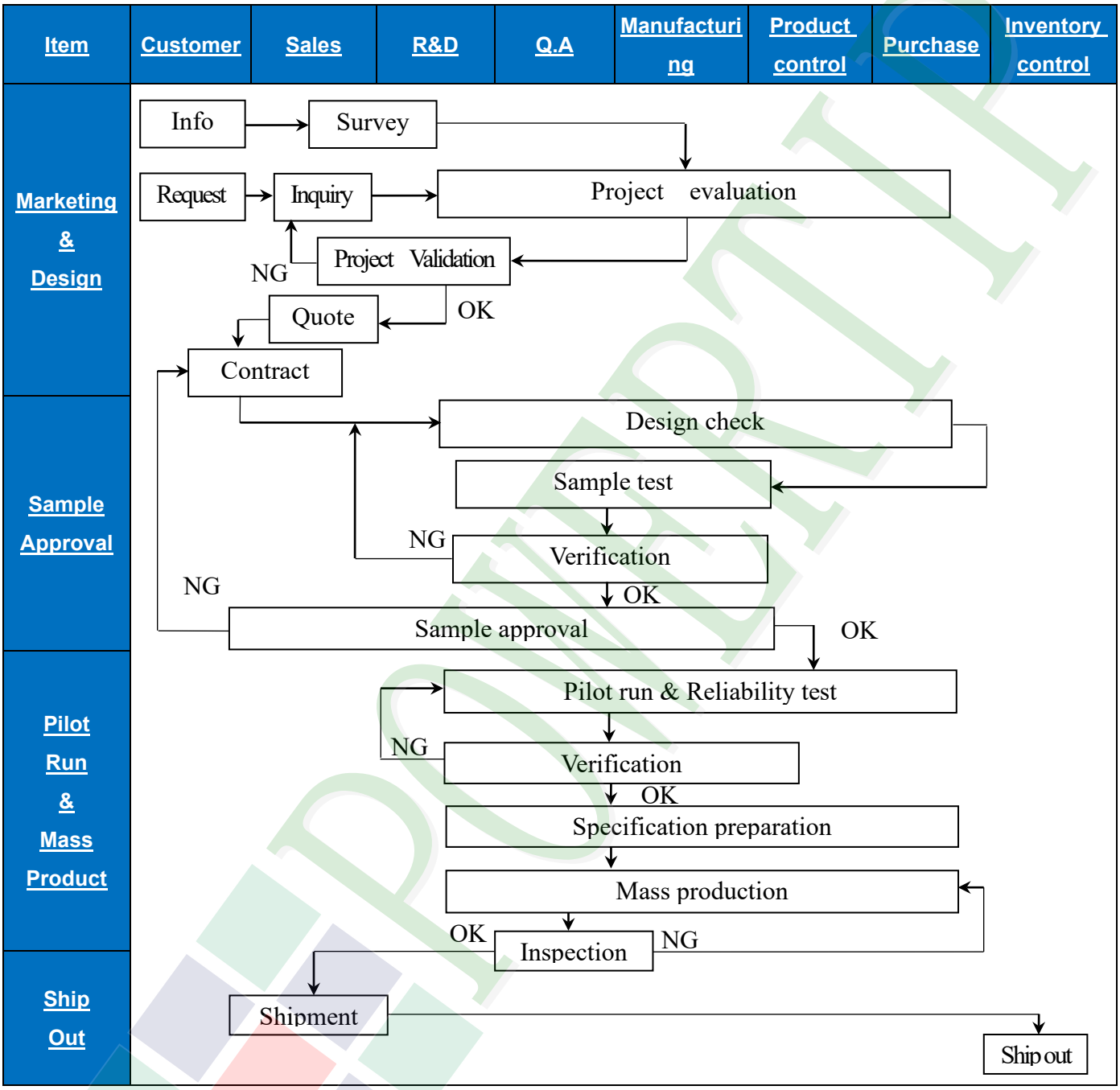
Note:

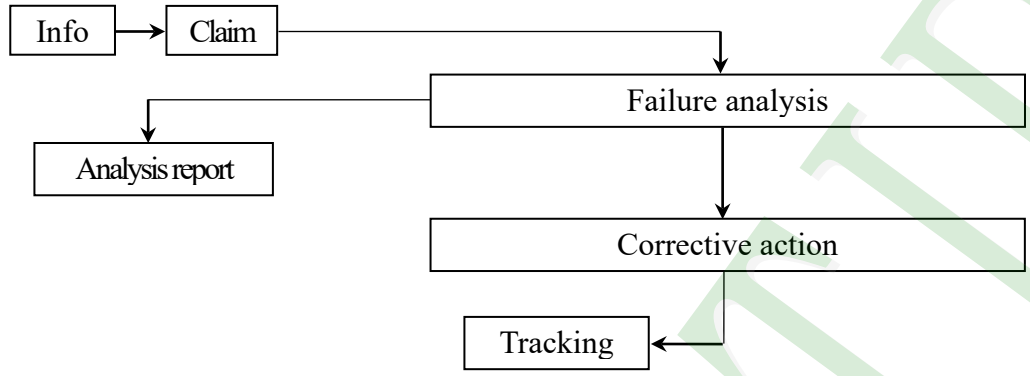
(1) thd is same to Hactive, and tvd is same to Vactive in chapter 5.1.

(2) DCLK frequency min/max value is base on frame rate 60 Hz

3. Quality Assurance System

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3.5"-15″ (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge, MIL-STD, Powertip Tester, Sample

◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

◆OUT Going Defect Level: Sampling

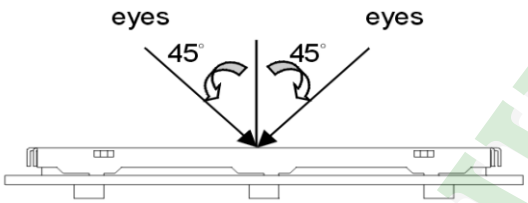
◆Standard of the product appearance test:

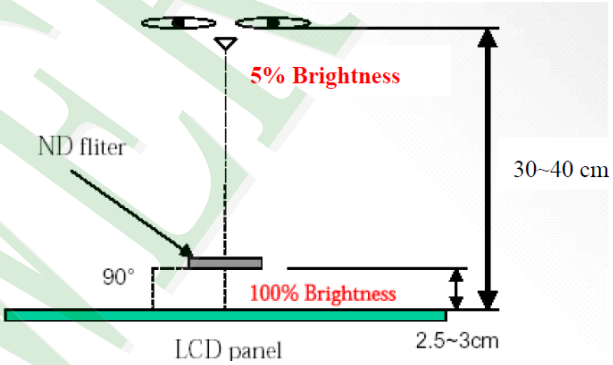
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light(about 300lux ～500lux)

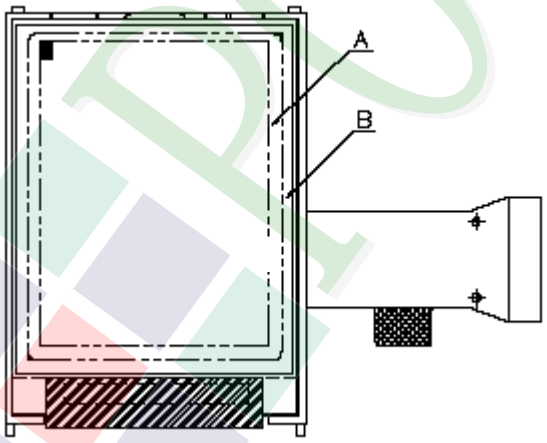
and distance of view must be at 30~40 cm.

(2). The test direction is base on about around 45˚ of vertical line.





(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection： (Unit：mm)

◆Specification For TFT-LCD Module 3.5”~15”: (Ver.B01)

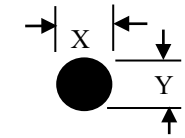
<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="3">01</td><td rowspan="3">Product condition</td><td>1.1 The part number is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>1.2 Mixed product types.</td><td>Major</td></tr>
<tr><td>1.3 Assembled in inverse direction.</td><td>Major</td></tr>
<tr><td>02</td><td>Quantity</td><td>2.1 The quantity is inconsistent with work order of production.</td><td>Major</td></tr>
<tr><td>03</td><td>Outline dimension</td><td>3.1 Product dimension and structure must conform to structure diagram.</td><td>Major</td></tr>
<tr><td rowspan="6">04</td><td rowspan="6">Electrical Testing</td><td>4.1 Missing line character and icon.</td><td>Major</td></tr>
<tr><td>4.2 No function or no display.</td><td>Major</td></tr>
<tr><td>4.3 Display malfunction.</td><td>Major</td></tr>
<tr><td>4.4 LCD viewing angle defect.</td><td>Major</td></tr>
<tr><td>4.5 Current consumption exceeds product specifications.</td><td>Major</td></tr>
<tr><td>4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.</td><td>Minor</td></tr>
<tr><td>05</td><td>Dot defect

(Bright dot, Dark dot)

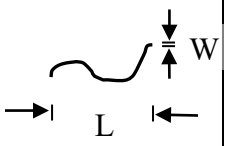
On -display</td><td>
<table>
<tr><th colspan="2">Item</th><th>Acceptance (Q’ty)</th></tr>
<tr><td rowspan="4">Dot Defect</td><td>Bright Dot</td><td>≦ 4</td></tr>
<tr><td>Dark Dot</td><td>≦ 5</td></tr>
<tr><td>Joint Dot</td><td>≦ 3</td></tr>
<tr><td>Total</td><td>≦ 7</td></tr>
</table>
5.1 Inspection pattern: full white, full black, Red, Green and blue screens.
5.2 It is defined as dot defect if defect area ＞1/2 dot.
5.3 The distance between two dot defect ≧5 mm.
5.4 Bright dot : Dots appear bright and unchanged in visible with 5% ND filter is defined.
5.5 Tiny bright dot: bright dot area ≦1/2 dot.
a. Dots appear bright and unchanged in visible with 5% ND filter is defined defect and is judged in accordance with 6.1
b. Dots invisible with 5% ND Filter is Ignored</td><td>Minor</td></tr>
</table>

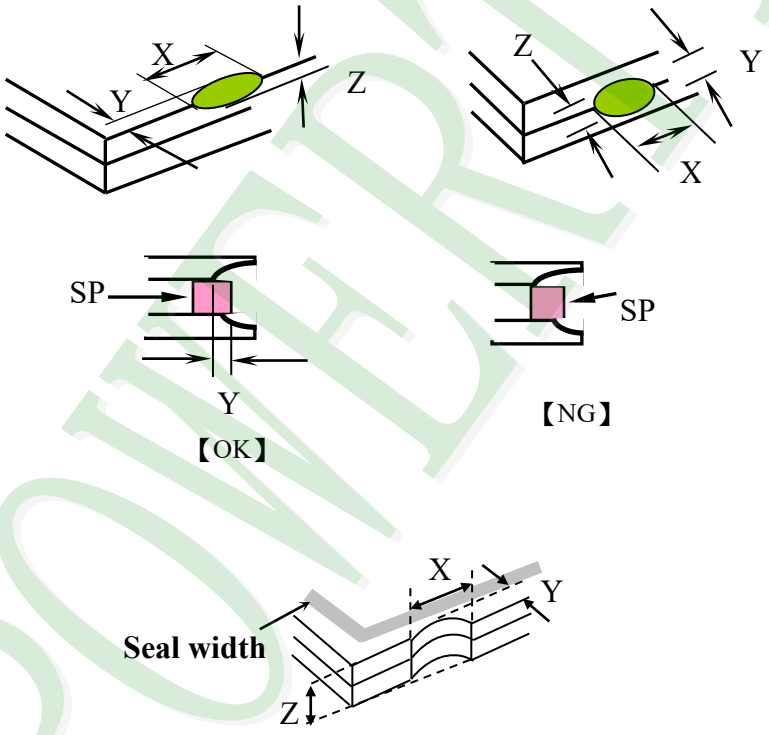
◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>06</td><td>Black or white Dot, scratch, contamination

Round type
 

Φ= (x+y) / 2

Line type
 
</td><td>6.1 Round type (Non-display or display):
<table>
<tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q’ty)</th></tr>
<tr><th>A area</th><th>B area</th></tr>
<tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="4">Ignore</td></tr>
<tr><td>0.25 ＜ Φ ≦ 0.50</td><td>5</td></tr>
<tr><td>Φ ＞ 0.50</td><td>0</td></tr>
<tr><td>Total</td><td>5</td></tr>
</table>
6.2 Line type(Non-display or display):
<table>
<tr><th rowspan="2">module size</th><th rowspan="2">Length (L)</th><th rowspan="2">Width (W)</th><th colspan="2">Acceptance (Q’ty)</th></tr>
<tr><th>A area</th><th>B area</th></tr>
<tr><td rowspan="5">3.5” to less 9”</td><td>---</td><td>W ≦ 0.03</td><td>Ignore</td><td rowspan="5">Ignore</td></tr>
<tr><td>L ≦ 10.0</td><td>0.03 ＜W ≦ 0.05</td><td>4</td></tr>
<tr><td>L ≦5.0</td><td>0.05 ＜W ≦ 0.10</td><td>2</td></tr>
<tr><td>---</td><td>W ＞0.10</td><td>As round type</td></tr>
<tr><td colspan="2">Total</td><td>5</td></tr>
<tr><td rowspan="4">9” to 15”</td><td>---</td><td>W ≦ 0.05</td><td>Ignore</td><td rowspan="4">Ignore</td></tr>
<tr><td>L ≦ 10.0</td><td>0.05 ＜W ≦ 0.10</td><td>5</td></tr>
<tr><td>---</td><td>W ＞0.10</td><td>As round type</td></tr>
<tr><td colspan="2">Total</td><td>5</td></tr>
</table></td><td>Minor</td></tr>
<tr><td>07</td><td>Polarizer Bubble</td><td>
<table>
<tr><th rowspan="2">Dimension (diameter: Φ)</th><th colspan="2">Acceptance (Q’ty)</th></tr>
<tr><th>A area</th><th>B area</th></tr>
<tr><td>Φ ≦ 0.25</td><td>Ignore</td><td rowspan="5">Ignore</td></tr>
<tr><td>0.25 ＜ Φ ≦ 0.50</td><td>4</td></tr>
<tr><td>0.50 ＜ Φ ≦ 0.80</td><td>1</td></tr>
<tr><td>Φ ＞0.80</td><td>0</td></tr>
<tr><td>Total</td><td>5</td></tr>
</table></td><td>Minor</td></tr>
</table>

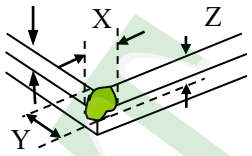
NO	Item	Criterion	Level
08	The crack of glass	Symbols： X: The length of crack　Y: The width of crack. Z: The thickness of crack　W: terminal length T: The thickness of glass　a : LCD side length 8.1 General glass chip: 8.1.1 Chip on panel surface and crack between panels:  	Minor

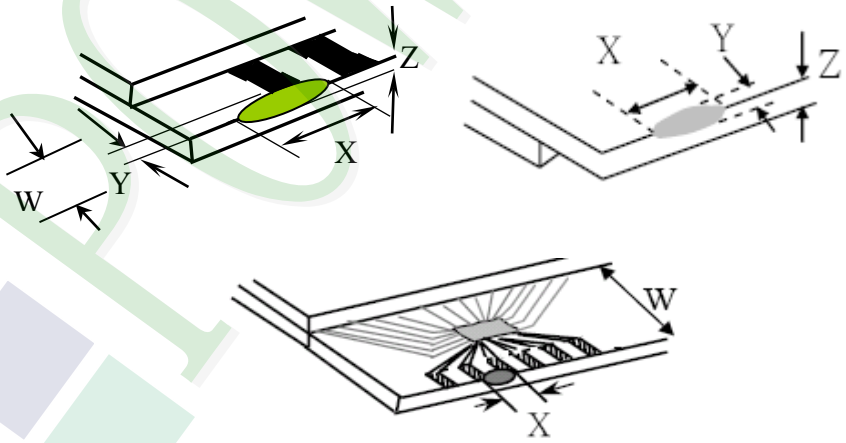
X	Y	Z
≦≦ a	Crack can't enter viewing area	≦1/2 t
≦a	Crack can't exceed the half of SP width.	1/2 t ＜ Z ≦2 t

◆Specification For TFT-LCD Module 3.5″ ~15″ : (Ver.B01)

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td rowspan="2">08</td><td rowspan="2">The crack of glass</td><td>Symbols :

X: The length of crack
Z: The thickness of crack
t: The thickness of glass
Y: The width of crack.
W: terminal length
a: LCD side length

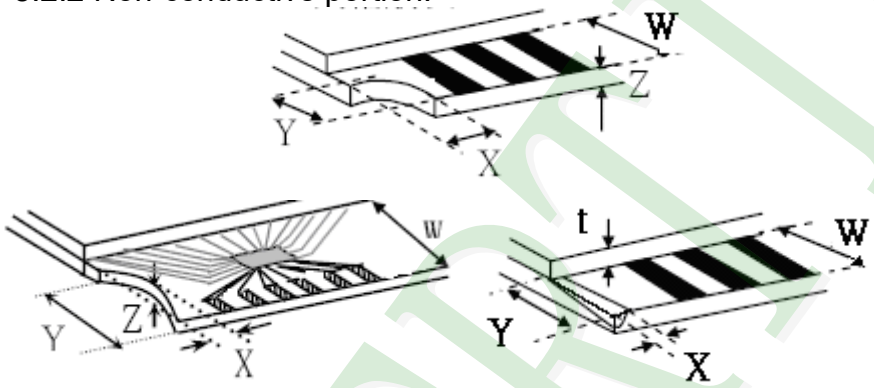
8.1.2 Corner crack:
 

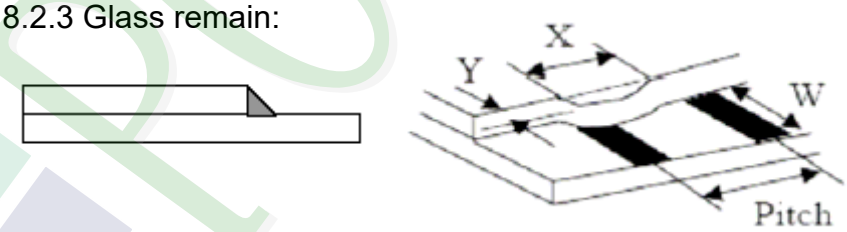
<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5 a</td><td>Crack can’t enter viewing area</td><td>Z ≦ 1/2 t</td></tr>
<tr><td>≦1/5 a</td><td>Crack can’t exceed the half of SP width.</td><td>1/2 t ＜ Z ≦ 2 t</td></tr>
</table></td><td>Minor</td></tr>
<tr><td>8.2 Protrusion over terminal:
8.2.1 Chip on electrode pad:
 

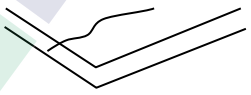
<table>
<tr><th></th><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>Front</td><td>≦ a</td><td>≦ 1/2 W</td><td>≦ t</td></tr>
<tr><td>Back</td><td>≦ a</td><td>≦ W</td><td>≦ 1/2 t</td></tr>
</table></td></tr>
</table>

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>08</td><td>The crack of glass</td><td>
Symbols:

X: The length of crack Y: The width of crack.
Z: The thickness of crack W: terminal length
t: The thickness of glass a: LCD side length

8.2.2 Non-conductive portion:


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ 1/3 a</td><td>≦ W</td><td>≦t</td></tr>
</table>
If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.
8.2.3 Glass remain:


<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦ a</td><td>≦ 1/3 W</td><td>≦t</td></tr>
</table>
8.2.4 Cracking:

Not Allowed
</td><td>Minor</td></tr>
</table>

◆Specification For TFT-LCD Module 3.5″ ~15″ (Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≦1.5 mm.	Minor

4. Reliability Test

4.1 Reliability Test Condition (Ver.B01)

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature Storage Test</td><td>Keep in 80 ±5℃ 240 hrs</td></tr>
<tr><td>2</td><td>Low Temperature Storage Test</td><td>Keep in -30 ±5℃ 240 hrs</td></tr>
<tr><td>3</td><td>High Temperature / High Humidity Storage Test</td><td>Keep in 60 ℃ / 90% R.H duration for 240 hrs
(Excluding the polarizer)</td></tr>
<tr><td>4</td><td>Temperature Cycling Storage Test</td><td>-30℃ → +25℃ → 80℃ → +25℃
(30mins) (5mins) (30mins) (5mins)
20 Cycle</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 2 KV with 5 times Discharge for each polarity +/-
Contact Discharge:
Apply 250 V with 5 times discharge for each polarity +/-</td></tr>
<tr><td>1. Temperature ambiance: 15℃～35℃
2. Humidity relative: 30%～60%
3. Energy Storage Capacitance(Cs+Cd): 150pF±10%
4. Discharge Resistance(Rd): 330Ω±10%
5. Discharge, mode of operation:
Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)</td></tr>
<tr><td>6</td><td>Vibration Test (Packaged)</td><td>1. Sine wave 10～55 Hz frequency (1 min/sweep)
2. The amplitude of vibration: 1.5 mm
3. Each direction (X, Y, Z) duration for 2 hrs</td></tr>
<tr><td>7</td><td>Drop Test (Packaged)</td><td><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop Direction :※1 corner / 3 edges / 6 sides each 1time</td></tr>
</table>

◎Result Evaluation Criteria :
Under the display quality test conditions with normal operations with normal operation state.
Do not change these conditions as such changes may affect practical display function.
(Normal operation state)
Temperature：+20~30°C
Humidity：50~70%
Atmospheric pressure：86~106Kpa

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands, this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320 ± 10°C and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM

5.2.10 Caution!(LCM products with Capacitive Touch Panel)
Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
Therefore, the touch needs to be thoroughly tested inside the target application.

5.2.11 CAUTION: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.

5.2.12 Double-sided tape designed to be attach with the customer's mechanical device, please follow up the rules and regulations published by the original manufacturer of double-sided tape for the attachment operation.

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

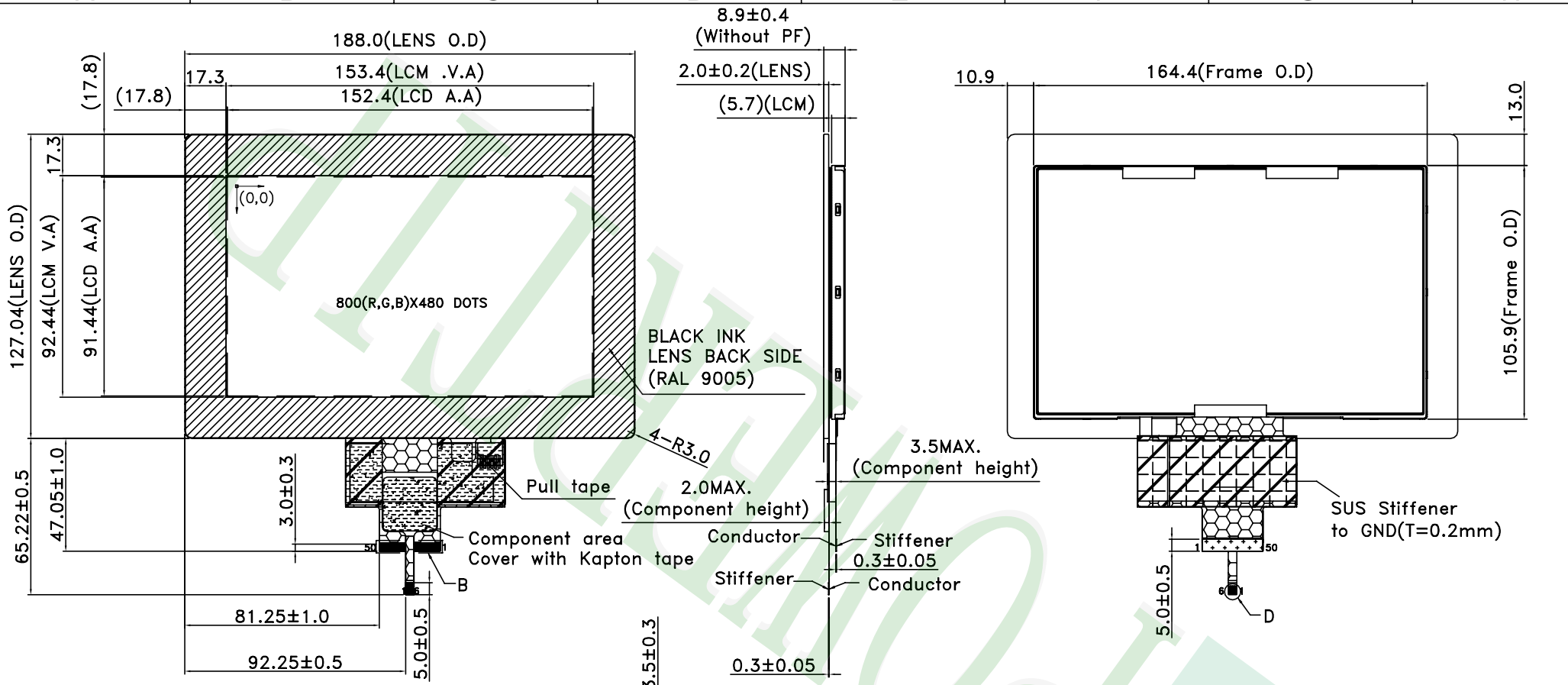
5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



NOTES:

1.LCD TYPE: IPS LCD
2.LCD DISPLAY: Negative/Transmissive
3.The tolerance unless classified ±0.3mm
4.A: P0.5*49=24.5±0.05,W=0.35±0.05
C: P0.5*5=2.5±0.05,W=0.3±0.05
5.T/P suggested connector: "Hirose" FH19SC-6S-0.5SH OR EQUIVALENT
6.TFT suggested connector: "CviLux" CF31501D0R2-05-NH OR EQUIVALENT
7. Kapton tape Stiffener Component area EMI film Aluminum Foil

REV	REV BY	REVISER	DATE
007			
006			
005			
004			
003			
002			
001	NEW DRAWING	Nini	2023/11/16

PART NO: PH800480T032-ZHC15

DRAWING NAME : LMD-PH800480T032-ZHC15

TITLE: LCD MODULE DRAWING

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

Design	Nini	(3)	Surface		
Check	Jason	Unit	MM	Material	
Approve	Bright	Scale	1:1	Thickness	
		Page	1/1	Quantity	

Precision Level (Length(Min) (mm) / Tolerance / Precision)	Precision Level
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

PT-A-054-01

Ver.001	
Documents NO.	PKG-PH800480T032-ZHC15

Packaging Specifications

Approve	Check	Design
Bright	Jason	Nini

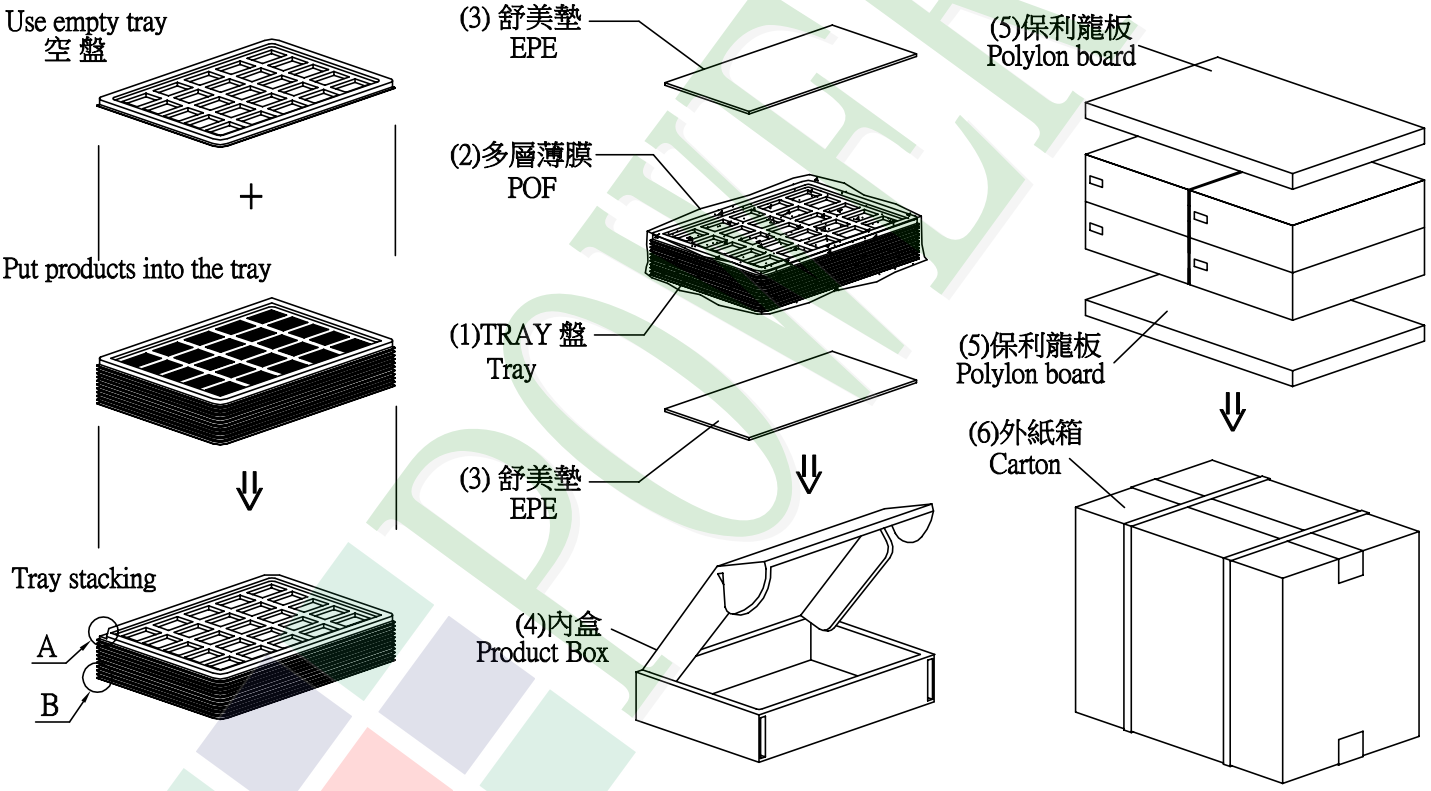
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH800480T032-ZHC15	188X 127.04 X 8.9	0.324	32	10.368
2	TRAY 盤 (1)Tray	TYSG000000696	352 X 260 X 22	0.1	20	2.0
3	多層薄膜(2)POF	OTFILM0BA03ABA	————	——	——	——
4	舒美墊(3)EPE	FOAM000000047	350 X 255 X 5	0.011	8	0.088
5	內盒(4)Product Box	BX00000000022	393 X 274 X 107	0.261	4	1.044
6	保利龍板(5)Polylon board	OTPLB00000008	550 X 393 X 15	0.022	2	0.044
7	外紙箱(6)Carton	BX57041027CCBA	570 X 410 X 265	1.39	1	1.39
8						
9						
10						

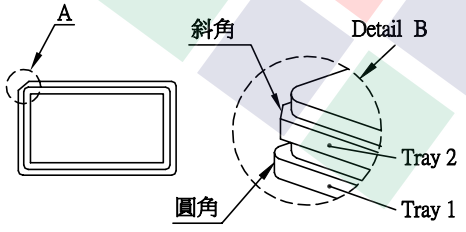
2.一 整箱總重量 (Total LCD Weight in carton) : 14.94 Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)LCD quantity per box : no per tray	2	x no of tray	4	=	8
(2)Total LCD quantity in carton : quantity per box	8	x no of boxes	4	=	32



特 記 事 項 (REMARK)



4. TRAY盤相疊時,需旋轉180度,請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.