POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	:	
SAMPLE CODE	:	SH480272T016-ZHA
MASS PRODUCTION CODE	:	PH480272T016-ZHA
SAMPLE VERSION	:	02
SPECIFICATIONS EDITION	:	009
DRAWING NO. (Ver.)	:	LMD-PH480272T016-ZHA (Ver.005)
PACKAGING NO. (Ver.)	:	PKG-PH480272T016-ZHA (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
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POWERTIP
2020.05.19
TWRD APR

☐ Preliminary specification for design input

■ Specification for sample approval
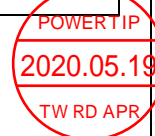
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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
05/14/2019	01	001	New Drawing	-	Yuan
07/26/2019	01	002	For Customer Request Modify Mechanical Specifications Modify Optical Characteristics Modify Block Diagram Modify Timing Characteristics Modify Inspection Specification Modify Reliability Test Condition Modify PRECAUTION RELATING PRODUCT HANDLING Modify FPC design and LCD A.A	4 7~8 10 13,14,16 20~26 27 28 Appendix	Yuan
09/12/2019	01	003	Modify Interface Pin Description	12	Yuan
10/30/2019	01	004	First Sample	-	Yuan
03/23/2020	02	005	Second Sample ADD Kapton Tape	Appendix	Yuan
04/13/2020	02	006	Modify LED Backlight Modify Dimension According to customer request modify Contents	9 Appendix -	Yuan
04/28/2020	02	007	According to customer request modify Contents	-	Yuan
04/29/2020	02	008	Modify LCD Type from Full Viewing Angle to IPS	4	Yuan
05/12/2020	02	009	Modify LCD Type from IPS to Full Viewing Angle Modify FPC suggested connector	4 Appendix	Yuan

Contents

Note: For detailed information please refer to IC data sheet:
Sitronix: SC7283-G4

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Resolution	480 *3 (RGB) * 272 Dots
LCD Type	Full Viewing Angle, Normally Black, Transmissive type
Screen size(inch)	4.3 inch
Surface treatment	Anti-Glare
Color configuration	R.G.B. Vertical Stripe
Weight	39.4 g
Interface	Parallel RGB (Data), SPI (Configuration)
Driver IC	SC7283-G4
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer website: http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5 (W) * 67.2 (L) * 2.6 (H)	mm

LCD panel

Item	Standard Value	Unit
Active Area	95.040 (W) * 53.856 (L)	mm

Note: For detailed information please refer to LCM drawing.

1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power Supply for TFT Panel	VDD	GND=0	-0.3	4.0	V	
Power Supply for Backlight Unit	VCC	GND=0	-0.3	+20.0	V	-
Operating Temperature	T_{OP} (Ts)	Note 1	-20	70	°C	
Storage Temperature	T_{ST} (Ta)	Note 2	-30	80	°C	

The absolute maximum rating values of this product are not allowed to be exceeded at any time. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 1: Ts is the temperature of panel's surface

Note 2: Ta is the ambient temperature of samples

1.4 DC Electrical Characteristics

Module   GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply for TFT Panel	VDD	GND=0V	3.0	3.3	3.6	V
Power Supply for Backlight Unit	VCC	GND=0V	5	12	15	V
Input Voltage for TFT Panel	V_{IH}	GND=0V	0.7VDD	-	VDD	V
	V_{IL}	GND=0V	0	-	0.3VDD	
Supply Current for TFT Panel	IDD	IDD@VDD=3.3V	-	30	45	
Supply Current for Backlight Unit	ICC	ICC@VCC=5V	-	170	225	mA
Supply Current for Backlight Unit	ICC	ICC@VCC=12V	-	70	105	
Input Voltage for PWM Signal	VPH	GND=0V	1.2	-	-	V
	VPL	GND=0V	-	-	0.4	V
Dimming Clock Rate	fP	GND=0V	5	-	100	KHz

1.5 Optical Characteristics

VDD=3.3V, Ta=25°C

Item	Symbol		Condition	Min.	Typ.	Max.	unit	
Response time	Tr+Tf		Ta = 25°C θX, θY = 0°	-	38	57	ms	Note 2
Viewing angle	Top	θY+	CR ≥ 10	-	80	-	Deg.	Note 4
	Bottom	θY-		-	80	-		
	Left	θX-		-	80	-		
	Right	θX+		-	80	-		
Contrast ratio	CR			650	800	-		Note 3
Color of CIE Coordinate (With B/L)	White	X	Ta = 25°C θX , θY = 0°	0.24	0.29	0.34	-	Note1
		Y		0.28	0.33	0.38		
	Red	X		0.55	0.60	0.65		
		Y		0.30	0.35	0.40		
	Green	X		0.30	0.35	0.40		
		Y		0.54	0.59	0.64		
	Blue	X		0.11	0.16	0.21		
		Y		0.09	0.14	0.19		
Average Brightness Pattern=white display (With LCD)*1	IF		VCC=12.0V PWM="High" (Duty=100%)	800	1000	-	cd/m²	Note1
Uniformity (With LCD)*2	△B		VCC=12.0V PWM="High" (Duty=100%)	70	-	-	%	Note1

Note 1:

*1: $\triangle B = B(min) / B(max) * 100\%$

*2: Measurement Condition for Optical Characteristics:

a: Environment: 25℃±5℃ / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency

b: Measurement Distance: 500 ± 50 ㎜, (θ= 0°)

c: Equipment: TOPCON BM-7 fast, (field 1˚), after 10 minutes operation

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%

Colorimeter=BM-7 fast
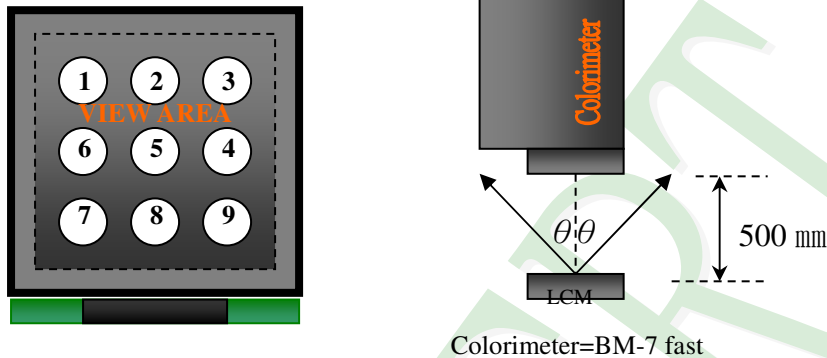
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note 2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

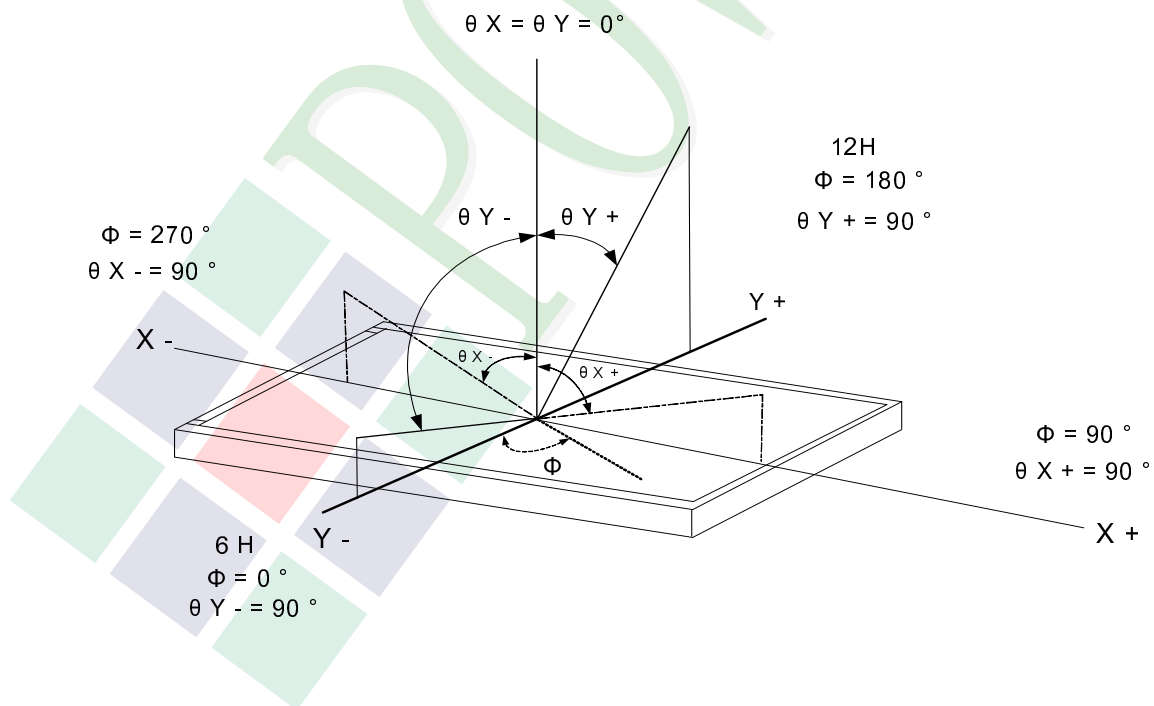Refer to figure as below:

Normally White

Normally Black
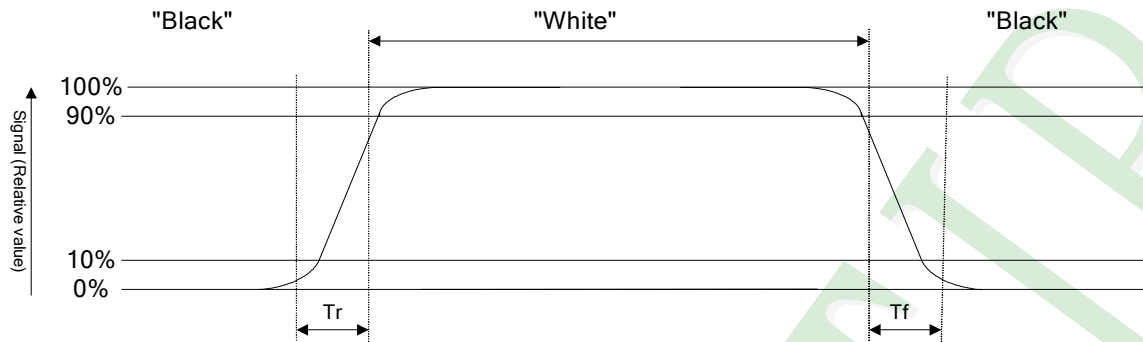
Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note 4: Definition of viewing angle:

Refer to figure as below:

1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
LED Forward Current	I_F	-	30	mA	One LED
LED Reverse Voltage	V_R	-	5.0	V	

Electrical / Optical Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
LED Voltage	V_L	18.0	19.0	-	V	Note1
LED Current	I_L	-	40	-	mA	-
LED life time	-	50,000	-	-	H_r	Note2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L=40 mA

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and I_L=40 mA. The LED life time could be decreased if operating I_L is larger than 40 mA
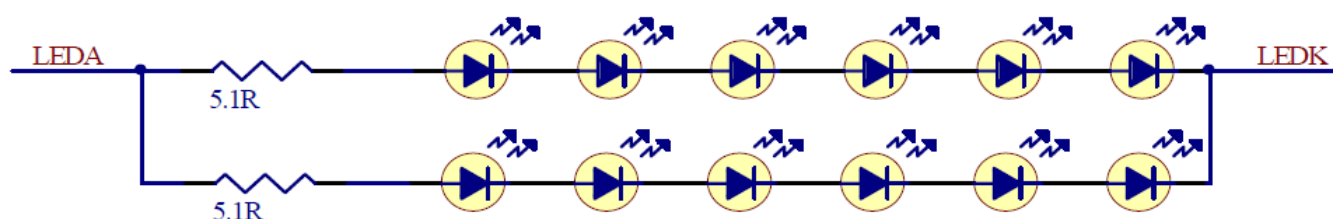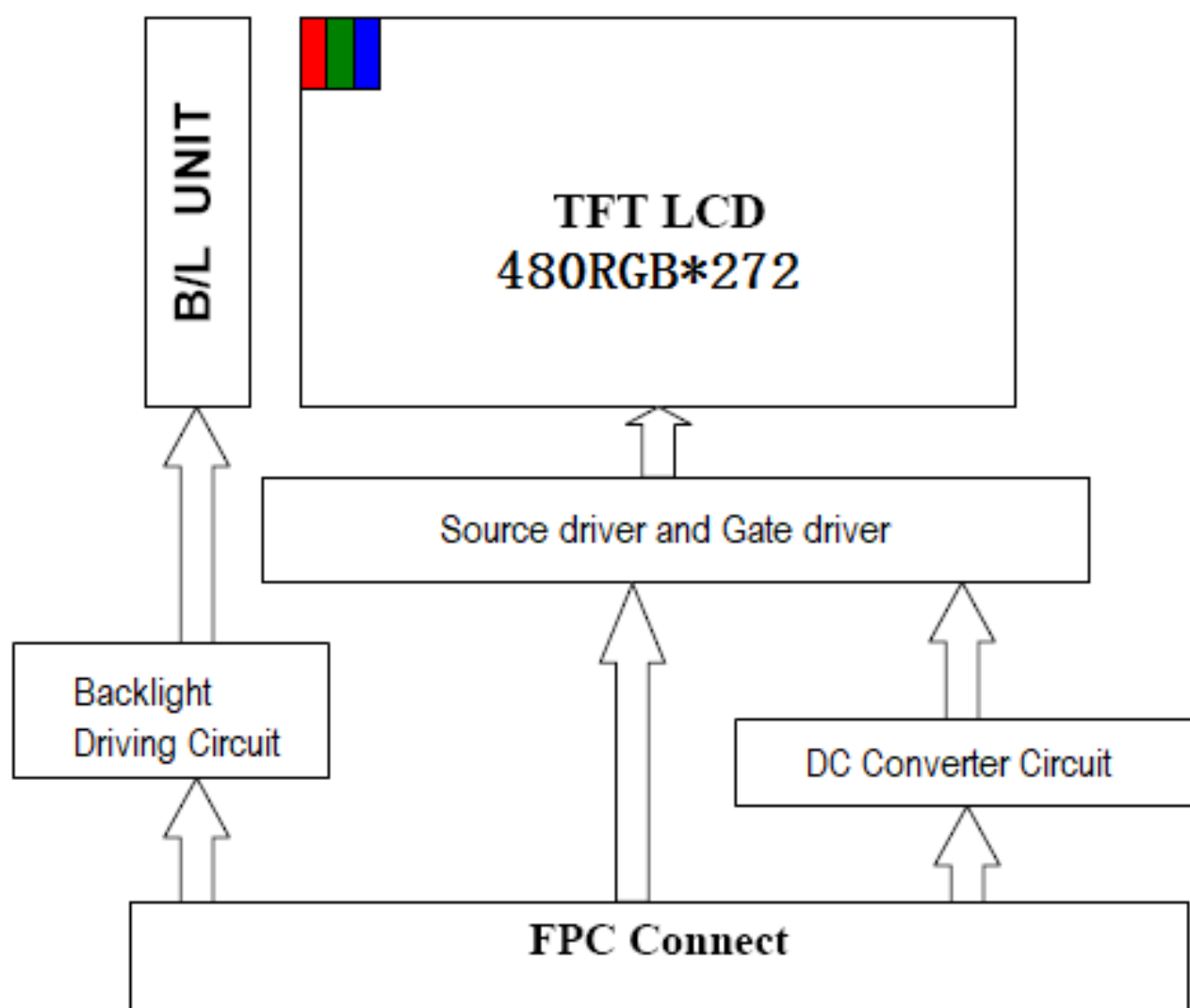
2. Module Structure

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

2.2 Interface Pin Description

TFT LCM Interface

Pin#	Name	Description
1	GND	Power ground.
2	VDD	Power for Digital Circuit.
3	VDD	Power for Digital Circuit.
4	VCC	Power For LED backlight.
5	VCC	Power For LED backlight.
6	PWM	Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi" =100%, "Low" = 0%.
7	GND	Power ground.
8	R0	Red Data.
9	R1	Red Data.
10	R2	Red Data.
11	R3	Red Data.
12	GND	Power ground.
13	R4	Red Data.
14	R5	Red Data.
15	R6	Red Data.
16	R7	Red Data.
17	GND	Power ground.
18	G0	Green Data.
19	G1	Green Data.
20	G2	Green Data.
21	G3	Green Data.
22	GND	Power ground.
23	G4	Green Data.
24	G5	Green Data.
25	G6	Green Data.
26	G7	Green Data.
27	GND	Power ground.
28	B0	Blue Data.
29	B1	Blue Data.

Pin#	Name	Description
30	B2	Blue Data.
31	B3	Blue Data.
32	GND	Power ground.
33	B4	Blue Data.
34	B5	Blue Data.
35	B6	Blue Data.
36	B7	Blue Data.
37	GND	Power ground.
38	HS	Line synchronization signal. Horizontal Sync Input.
39	VS	Frame synchronization signal. Vertical Sync Input.
40	GND	Power ground.
41	DE	Data Enable
42	GND	Power ground.
43	DCLK	Sample clock. Data will be latched at the falling edge of DCLK.
44	GND	Power ground.
45	CS / ID1	Serial communication chip selection/ID[4:1]These pins select LCM type. See NOTE1
46	SDIN / ID2	Serial communication data/ ID[4:1]These pins select LCM type. See NOTE1
47	SCK / ID3	Serial communication clock/ ID[4:1]These pins select LCM type. See NOTE1
48	DISPLAY CONTROL / ID4	Display Enable(Hi Active)./ ID[4:1]These pins select LCM type. See NOTE1
49	/RESET	Global Reset (Low Active).
50	GND	Power ground.

Note1:

ID Pins Definition:

	PIN 45 ID1	PIN 46 ID2	PIN 47 ID3	PIN 48 ID3
3.5" Module	X	0	0	X
4.3" Module	X	1	0	X
5.0" Module	X	0	1	X
7.0" Module	X	1	1	X

1. Resistor = 10k ohm

2. "X" = No use

2.3 Timing Characteristics

2.3.1 RGB Mode Selection Table

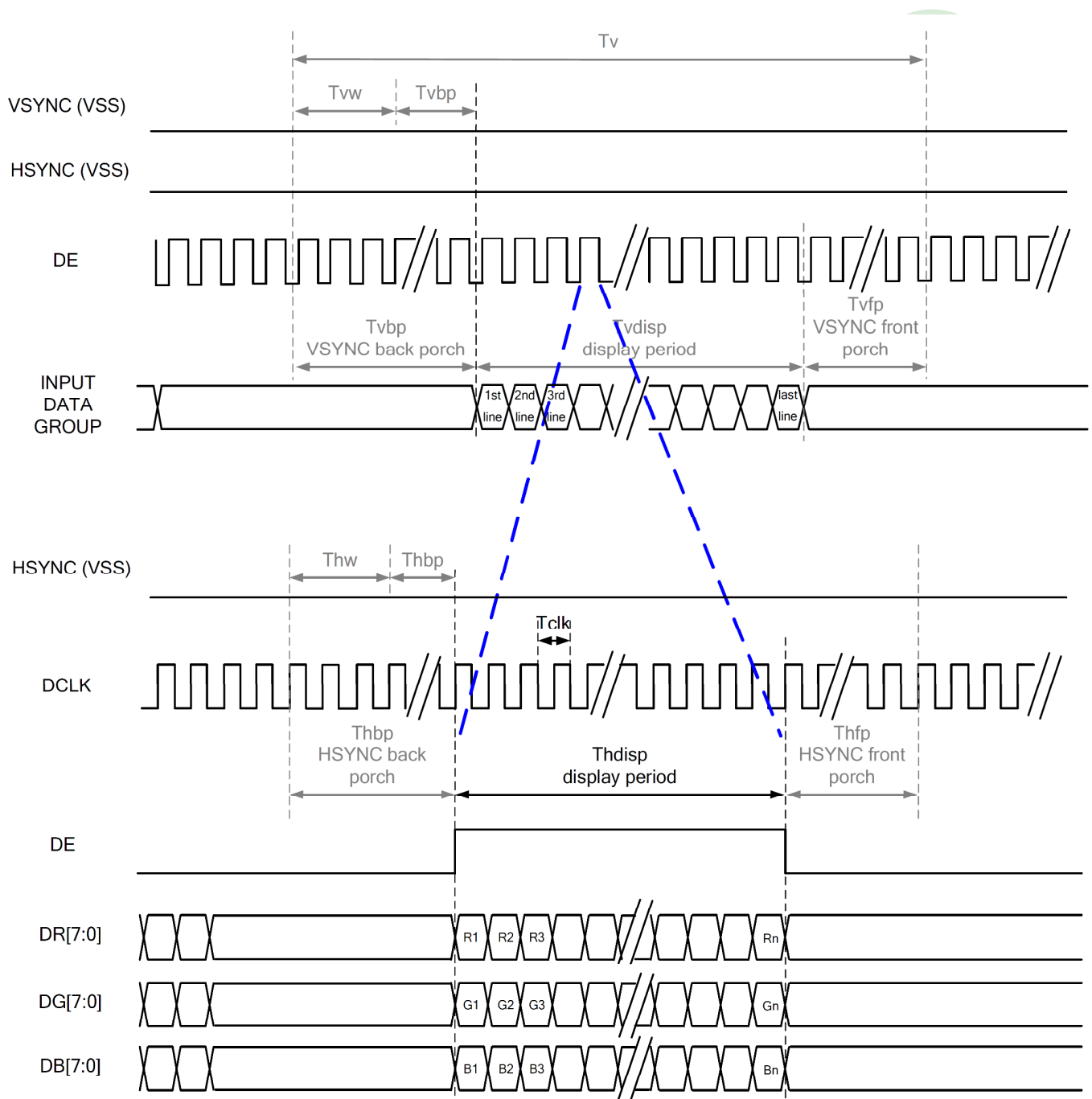| RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

2.3.2 Parallel RGB SYNC Mode
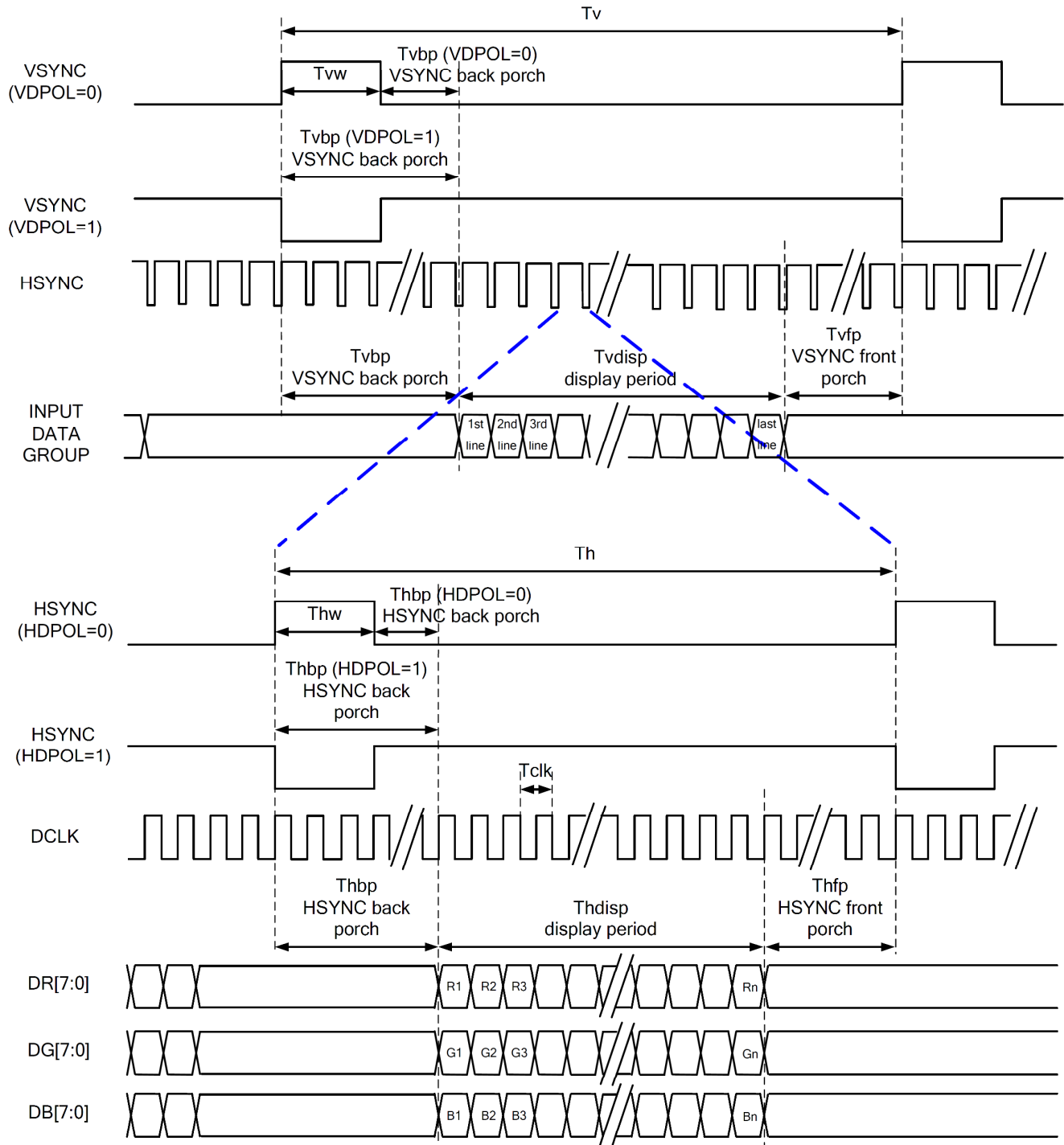
2.3.3 Parallel RGB SYNC-DE Mode
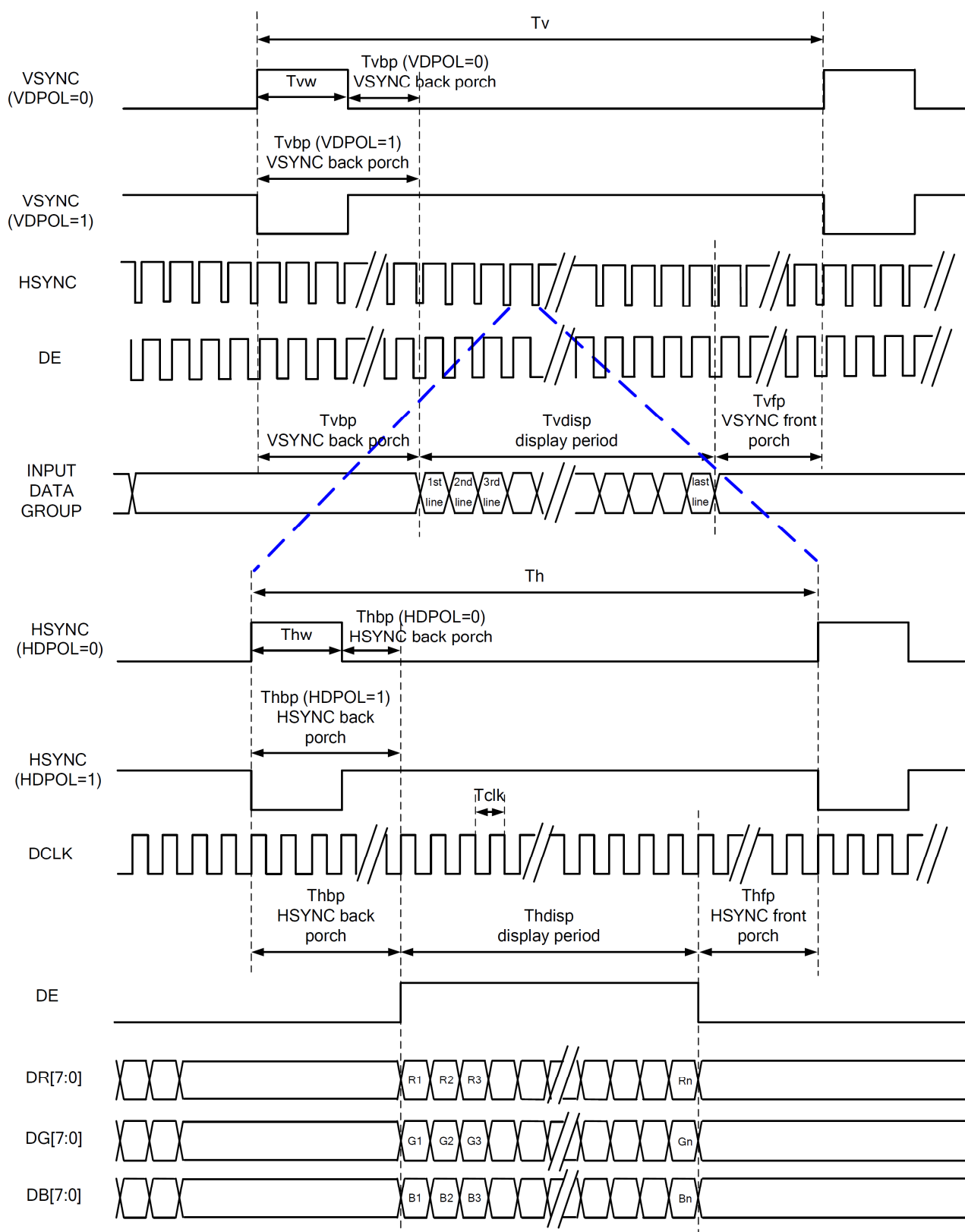
2.3.4 Parallel RGB DE Mode

2.3.5 Parallel 24-bit RGB Input Timing

Item		Symbol	Min	Typ.	Max	Unit	Note
DCLK Frequency		Fclk	8	9	12	MHz	
DCLK Period		Tclk	83	111	125	ns	
HSYNC	Period time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period time	Tv	276	292	321	HSYNC	
	Display Period	Tvdisp		272		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

Note: It is necessary to keep Tvbp =12 and Thbp=43 in sync mode. DE mode is unnecessary to

keep it.

2.3.6 Power ON Sequence
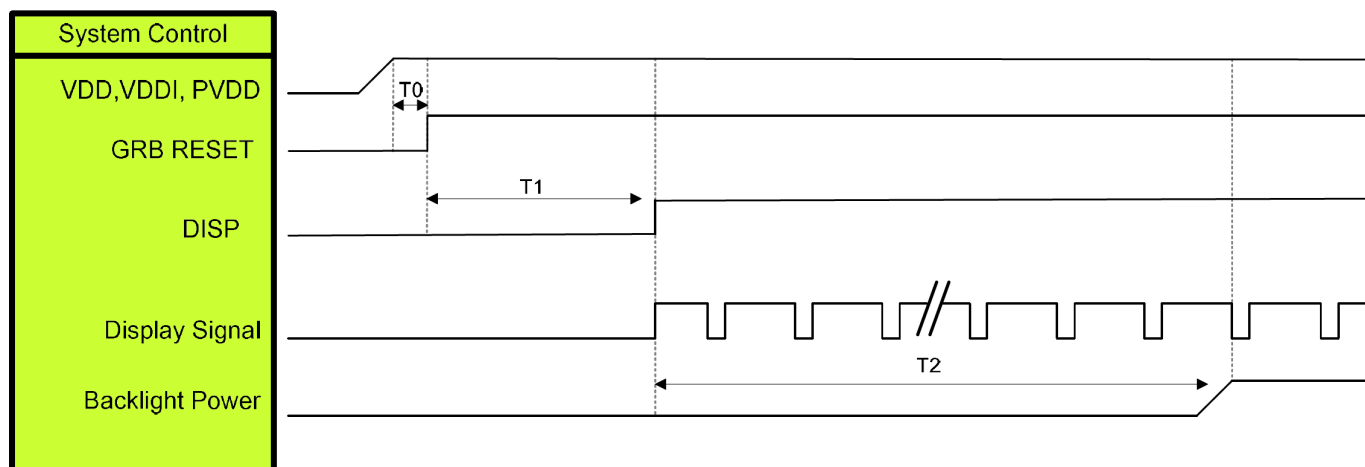
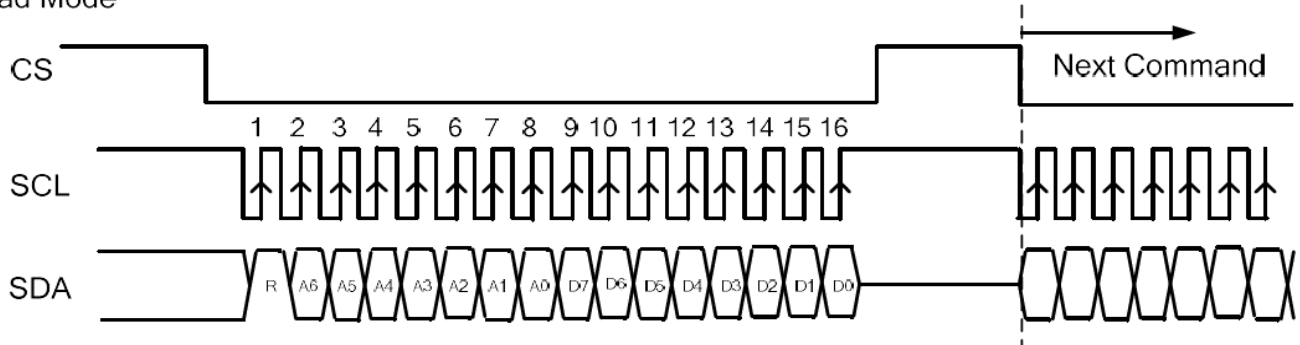| Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP= "High"	10	ms
T2	Display signal output to backlight power on	250	ms

2.3.7 Power Off Sequence
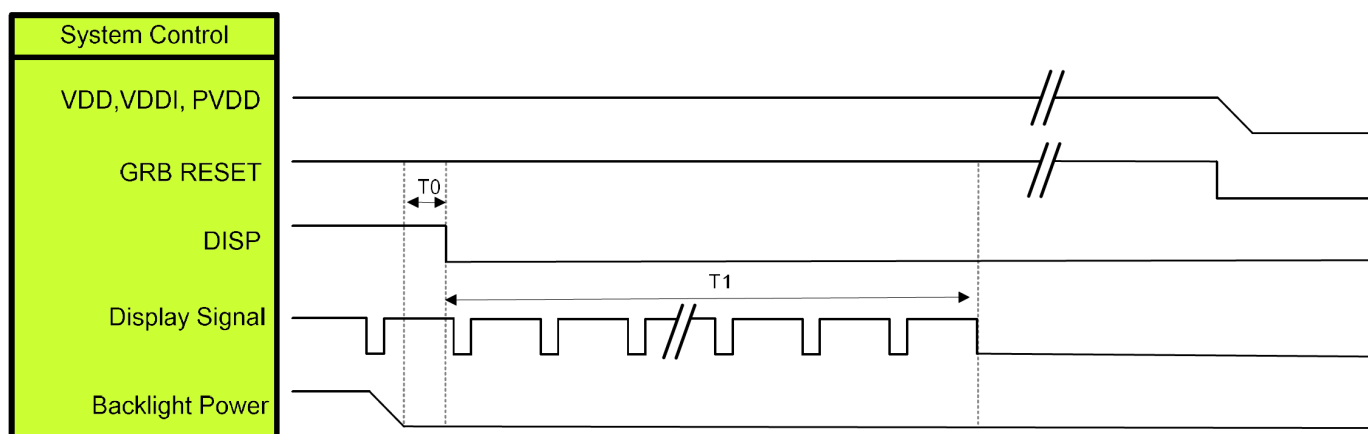
Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP= "Low"	5	ms
T1	DISP= "Low" to IC internal voltage discharge complete	80	ms

2.4 3-wire Serial Interface (SPI)

R/W: Read/Write mode control bit.
R/W=1: Read mode
R/W=0: Write mode
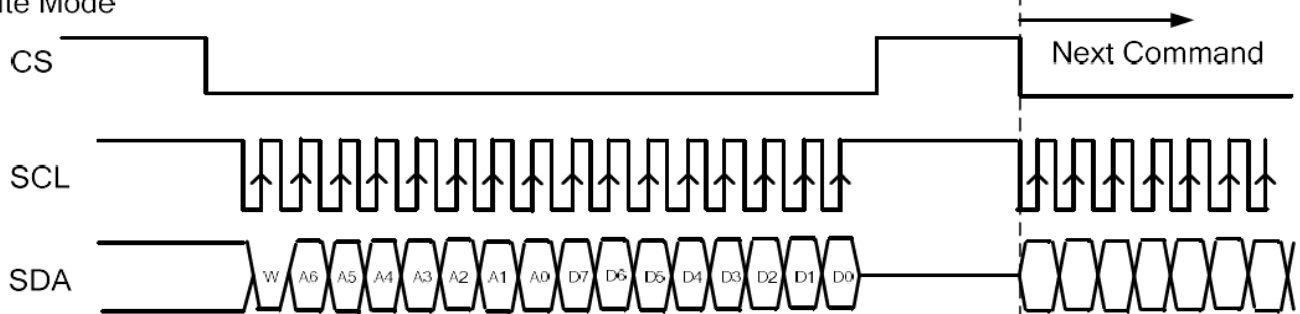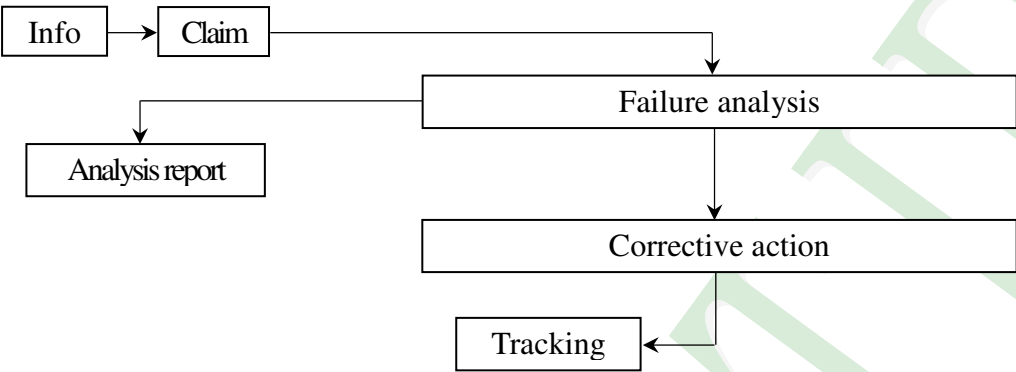
a. Each serial command consists of 16 bits of data which is loaded one bit a time at the rising edge of serial clock SCL.

b. Common loading operation starts from the falling edge of CS and is completed at the next rising edge of CS.

c. The serial control block is operational after power on reset, but commands are established by the VSYNC signal. If command is transferred multiple times for the same register, the last command before the VSYNC signal is valid.

d. If less than 16 bits of SCL are input while CS is low, the transferred data is ignored.

e. If 16 bits or more of SCL are input while CS is low, the previous 16 bits of transferred data before then rising edge of CS pulse are valid data.

f. Serial block operates with SCL clock.

g. Serial data can be accepted in the power save mode.

h. After power on reset or GRB reset, it is required 100ms delay to begin SPI communication.

# 3. Quality Assurance System

3.1 Quality Assurance Flow Chart

Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Marketing & Design	Info → Survey; Request → Inquiry	Inquiry → Project evaluation; Project Validation (NG); Quote; Contract			Project evaluation			
Sample Approval		Contract	Design check; Sample test; Verification (NG/OK); Sample approval					
Pilot Run & Mass Product			Pilot run & Reliability test; Verification (NG/OK); Specification preparation; Mass production; Inspection					
Ship Out	Shipment							Ship out

Flow chart detail:
- Info → Survey
- Request → Inquiry → Project evaluation
- Project evaluation → Project Validation
- Project Validation — NG → Inquiry
- Project Validation — OK → Quote
- Quote → Contract
- Contract → Design check
- Design check → Sample test
- Sample test → Verification
- Verification — NG → Design check
- Verification — OK → Sample approval
- Sample approval — NG → Contract
- Sample approval — OK → Pilot run & Reliability test
- Pilot run & Reliability test → Verification
- Verification — NG → Pilot run & Reliability test
- Verification — OK → Specification preparation
- Specification preparation → Mass production
- Mass production → Inspection
- Inspection — OK → Shipment
- Inspection — NG → Mass production
- Shipment → Ship out

Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	Info → Claim → Failure analysis → Corrective action → Tracking, Analysis report							
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2 Inspection Specification

◆**Scope: The document shall be applied to TFT-LCD Module for 3.5″-15″ (Ver.B01).**

◆**Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.**

◆**Equipment: Gauge, MIL-STD, Powertip Tester, Sample**

◆**Defect Level: Major Defect AQL: 0.4; Minor Defect   AQL: 1.5**

◆**OUT Going Defect Level: Sampling**

◆**Standard of the product appearance test:**

   a. Manner of appearance test:

   (1). The test best be under 20W×2 fluorescent light(about 300lux ～500lux)

     and distance of view must be at 30~40 cm.

   (2). The test direction is base on about around 45° of vertical line.
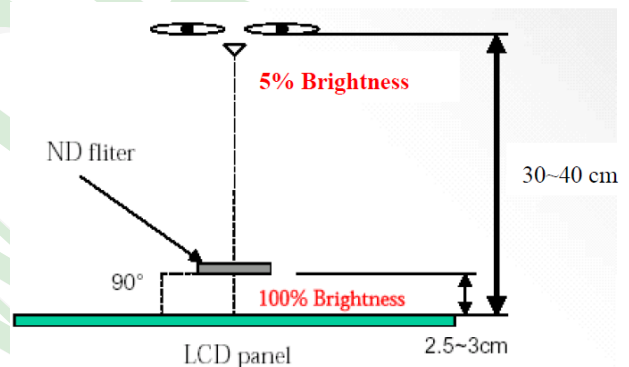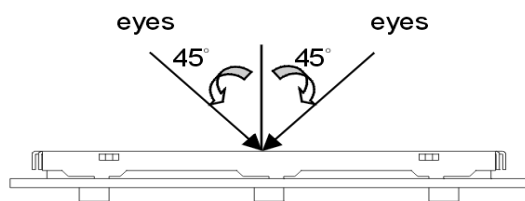
   (3). Definition of area.
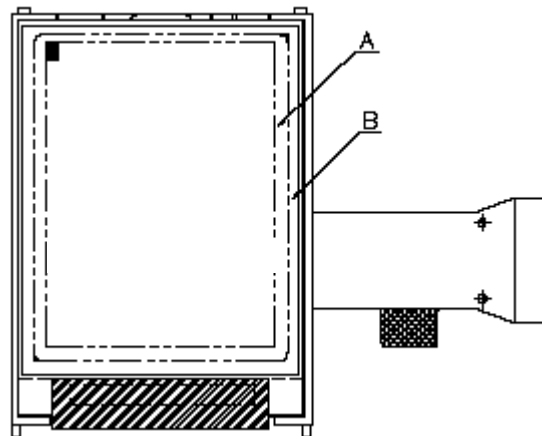
A area: viewing area

B area: Outside of viewing area
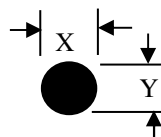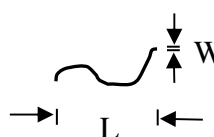
   (4). Standard of inspection： (Unit：mm)

NO	Item	Criterion	Level
01	Product condition	1.1 The part number is inconsistent with work order of production.	Major
		1.2 Mixed product types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to structure diagram.	Major
04	Electrical Testing	4.1 Missing line character and icon.	Major
		4.2 No function or no display.	Major
		4.3 Display malfunction.	Major
		4.4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major
		4.6 Mura cannot be seen through 5% ND filter at 50% Gray, should be judged by the viewing angle of 90 degree.	Minor
05	Dot defect (Bright dot, Dark dot) On -display	<table><tr><th>Item</th><th>Acceptance (Q'ty)</th></tr><tr><td rowspan="4">Dot Defect</td></tr><tr><td>Bright Dot</td><td>≦ 4</td></tr><tr><td>Dark Dot</td><td>≦ 5</td></tr><tr><td>Joint Dot</td><td>≦ 3</td></tr><tr><td>Total</td><td>≦ 7</td></tr></table> 5.1 Inspection pattern: full white, full black, Red, Green and blue screens. 5.2 It is defined as dot defect if defect area ＞1/2 dot. 5.3 The distance between two dot defect ≧5 mm. 5.4 Bright dot that can not be seen through 5% ND filter.	Minor

◆**Specification For TFT-LCD Module 3.5″ ~15″ :** (Ver.B01)
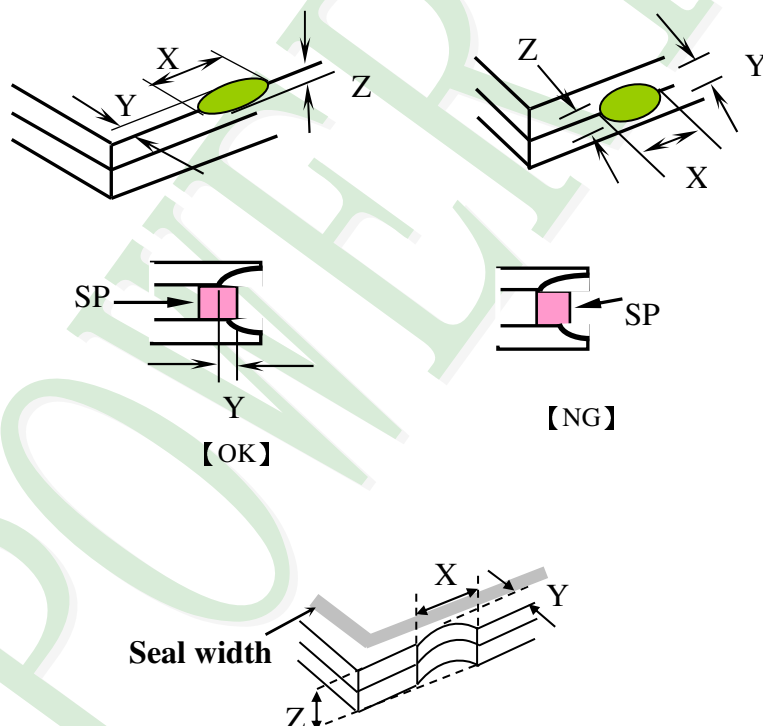
NO	Item	Criterion	Level
06	**Black or white Dot, scratch, contamination** **Round type** $\Phi = (x+y)/2$ **Line type** 	**6.1 Round type (Non-display or display):** Dimension/Acceptance table and Line type table below	**Minor**

6.1 Round type (Non-display or display):

Dimension (diameter: Φ)	Acceptance (Q'ty)	
	A area	**B area**
Φ ≦ 0.25	**Ignore**	**Ignore**
0.25 < Φ ≦ 0.50	5	
Φ > 0.50	0	
Total	**5**	

6.2 Line type(Non-display or display):

module size	Length (L)	Width (W)	Acceptance (Q'ty)	
			A area	**B area**
3.5" to less 9"	---	W ≦ 0.03	**Ignore**	**Ignore**
	L ≦ 10.0	0.03 < W ≦ 0.05	4	
	L ≦ 5.0	0.05 < W ≦ 0.10	2	
	---	W > 0.10	As round type	
		Total	**5**	
9" to 15"	---	W ≦ 0.05	**Ignore**	**Ignore**
	L ≦ 10.0	0.05 < W ≦ 0.10	5	
	---	W > 0.10	As round type	
		Total	**5**	

NO	Item	Criterion	Level
07	**Polarizer Bubble**	table below	**Minor**

Dimension (diameter: Φ)	Acceptance (Q'ty)	
	A area	**B area**
Φ ≦ 0.25	**Ignore**	**Ignore**
0.25 < Φ ≦ 0.50	4	
0.50 < Φ ≦ 0.80	1	
Φ > 0.80	0	
Total	**5**	
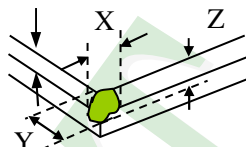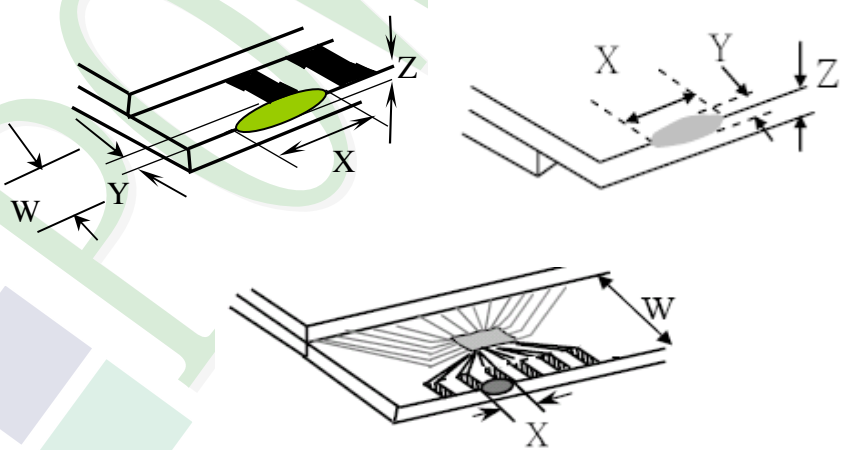
◆**Specification For TFT-LCD Module 3.5″ ~15″ :**

(Ver.B01)

<u>NO</u>	<u>Item</u>	<u>Criterion</u>	<u>Level</u>
08	**The crack of glass**	Symbols： X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length T: The thickness of glass a : LCD side length **8.1 General glass chip:** **8.1.1 Chip on panel surface and crack between panels:** 	**Minor**

<u>X</u>	<u>Y</u>	<u>Z</u>
≦ ≦ a	**Crack can't enter viewing area**	≦1/2 t
≦a	**Crack can't exceed the half of SP width.**	1/2 t ＜ Z ≦2 t
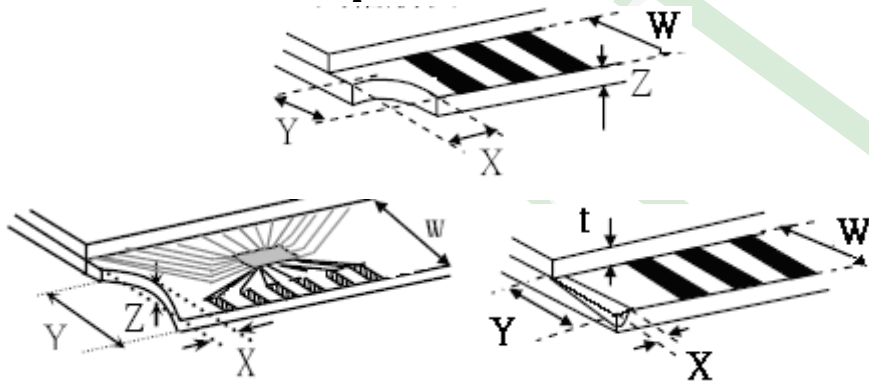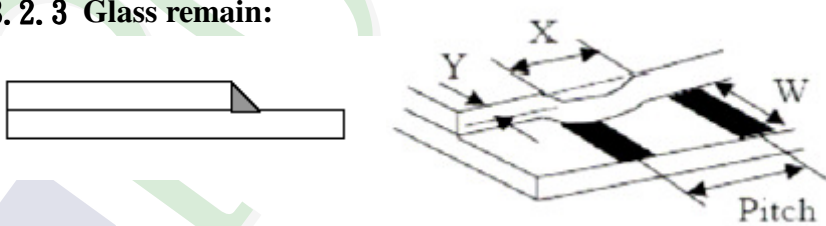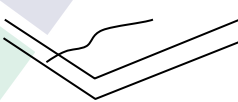
NO	Item	Criterion	Level
08	The crack of glass	Symbols： X: The length of crack　　Y: The width of crack. Z: The thickness of crack　W: terminal length t: The thickness of glass　　a: LCD side length 8.1.2 Corner crack: (see table below) 8.2 Protrusion over terminal: 8.2.1 Chip on electrode pad: (see table below)	Minor

8.1.2 Corner crack table:

X	Y	Z
≦1/5 a	Crack can't enter viewing area	Z ≦ 1/2 t
≦1/5 a	Crack can't exceed the half of SP width.	1/2 t < Z ≦ 2 t

8.2.1 Chip on electrode pad table:

	X	Y	Z
Front	≦ a	≦ 1/2 W	≦ t
Back	≦ a	≦ W	≦ 1/2 t

NO	Item	Criterion	Level
08	The crack of glass	Symbols: X: The length of crack Y: The width of crack. Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 8.2.2 Non-conductive portion: | X	Y	Z
---	---	---
$\leqq 1/3$ a	$\leqq$ W	$\leqq$ t | If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. 8.2.3 Glass remain: | X	Y	Z
---	---	---
$\leqq$ a	$\leqq 1/3$ W	$\leqq$ t | 8.2.4 Cracking: Not Allowed	Minor

◆**Specification For TFT-LCD Module 3.5″ ~15″：**

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type, quantity, dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC.	Major
		10.3 Parts on PCB or FPC must be: no wrong parts, missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC）is ≦1.5 mm.	Minor

4. Reliability Test

4.1 Reliability Test Condition (Ver.B01)

NO.	TEST ITEM	TEST CONDITION	
1	High Temperature Storage Test	Keep in 80 ±5℃ 240 hrs	
2	Low Temperature Storage Test	Keep in −30 ±5℃ 240 hrs	
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs (Excluding the polarizer)	
4	Temperature Cycling Storage Test	−30℃ → +25℃ → 80℃ → +25℃ (30mins)　(5mins)　(30mins)　(5mins) 20 Cycle	
5	ESD Test	**Air Discharge:** Apply 2 KV with 5 times Discharge for each polarity +/-	**Contact Discharge:** Apply 250 V with 5 times discharge for each polarity +/-
5	ESD Test	1. Temperature ambiance: 15℃～35℃ 2. Humidity relative: 30%～60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)	
6	Vibration Test (Packaged)	1. Sine wave 10～55 Hz frequency (1 min/sweep) 2. The amplitude of vibration: 1.5 mm 3. Each direction (X, Y, Z) duration for 2 hrs	
7	Drop Test (Packaged)	<table><tr><td>Packing Weight (Kg)</td><td>Drop Height (cm)</td></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table> Drop Direction：※1 corner / 3 edges / 6 sides each 1time	

◎Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state.
Do not change these conditions as such changes may affect practical display function.
(Normal operation state)
Temperature：+20~30℃
Humidity：50~70%
Atmospheric pressure：86~106Kpa

5. Precaution Relating Product Handling

5.1 Safety
    5.1.1   If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
    5.1.2   If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 Handling
    5.2.1   Avoid any strong mechanical shock which can break the glass.
    5.2.2   Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
    5.2.3   Do not remove the panel or frame from the module.
    5.2.4   The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers ,etc.)
    5.2.5   Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
    5.2.6   Do not touch the display area with bare hands, this will stain the display area.
    5.2.7   Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
    5.2.8   To control temperature and time of soldering is 320 ± 10°C and 3-5 sec.
    5.2.9   To avoid liquid (include organic solvent) stained on LCM
    5.2.10   Caution! (LCM products with Capacitive Touch Panel)

               Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).

               Therefore, the touch needs to be thoroughly tested inside the target application.
    5.2.11   Caution: Continuously displaying same static image will result in high possibility of image sticking/image burn-in effect due to TFT panel characteristic.

5.3 Storage
    5.3.1   Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
    5.3.2   Do not place the module near organics solvents or corrosive gases.
    5.3.3   Do not crush, shake, or jolt the module.

5.4 Terms of Warranty
    5.4.1   Applicable warrant period
               The period is within thirteen months since the date of shipping out under normal using and storage conditions.
    5.4.2   Unaccepted responsibility
               This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

NOTES:
1.LCD TYPE:TFT LCD
2.LCD DISPLAY:Normally Black/TRANSMISSIVE
3.The tolerance unless classified ±0.3mm
4.a :P0.5*49=24.5±0.05 ; W=0.35±0.05
4.a :P0.5*49=24.5±0.05 ; W=0.35±0.05
5. ▦ :Kapton tape;
   ▨ :Component area; ▨ :Stiffener(SUS t=0.2mm to GND)
   ▧ :Aluminum Foil Conductive Adhesive Tape; ▤ :EMI Film;
6.FPC suggested connector : FH12A-50S-0.5SH(Hirose) or compatible △5

Dimension labels (top view):
42.0±0.5
67.2(O.D)
56.5(Frame V.A)
54.9(LCM V.A)
53.856(LCD A.A)
2.9
3.7
4.23
105.5(O.D)
97.9(Frame V.A)
96.3(LCM V.A)
95.040(LCD A.A)
480(R,G,B)x272 DOTS
40.0±0.5
0.5±0.1
25.5±0.1
a
50
1
3.0±0.3
3.8
4.6
5.23
Pull tape

Component Area
Component A rea A.A

Side view:
2.1 Max
Component Height
Conductor
Stiffener
0.3±0.05
2.6
5.0±0.5
11
50

Detail Dots Scale 100X:
0.198
0.198
R
G
B
Detail Dots
Scale 100X

007						
006						
005	Modify FPC suggested connector		Clare	2020/5/13		
004	Modify Dimension		Clare	2020/4/8		
003	Add Kapton tape		Clare	2020/3/20		
002	Modify FPC design and LCD A.A		Clare	2019/7/26		
001	NEW DRAWING		Clare	2019/5/13		
REV	REV BY		REVISER	DATE		

PART NO: PH480272T016-ZHA
DRAWING NAME: LMD-PH480272T016-ZHA
TITLE: LCD MODULE DRAWING

久正光電股份有限公司
POWERTIP TECHNOLOGY CORPORATION

	Design	Clare Chen	Surface	
	Check	Tina Chen	Material	
	Approve	Jimmy Chen	Quantity	
	Unit	MM	Scale	FIT	Page	1/1

	Length(Min)	Tolerance (mm)	Precision Level
Length(mm)	Precision		
1 ~ 4	4 ~ 16	-	-
4 ~ 16	16 ~ 63	-	-
16 ~ 63	63 ~ 250	-	-
63 ~ 250	250 ~ 1000	-	-

1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PH480272T016-ZHA	105.5 X 67.2	0.04	216	8.64
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"X350X0.015	——	6	——
3	TRAY 盤 (2)Tray	TY00000000392	352 X 260 X 10.8	0.1	60	6.0
4	內盒(3)Product Box	BX36627063ABBA	383 X 270 X 66	0.182	6	1.092
5	保利龍板(4)Polylon board	OTPLB00PL08ABA	550 X 393 X 20	0.0284	2	0.0568
6	外紙箱(5)Carton	BX57041027CCBA	570 X 410 X 265	1.0	1	1.0
7						
8						
9						

2.一 整箱總重量 (Total LCD Weight in carton) :   16.79   Kg±10%

3.單箱數量規格表 (Packaging Specifications and Quantity) :

(1)LCM quantity per box : no per tray   4   x no of tray   9   =   36

(2)Total LCM quantity in carton : quantity per box   36   x no of boxes   6   =   216
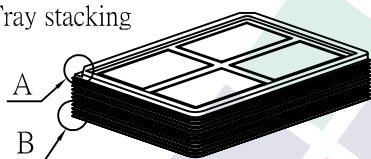
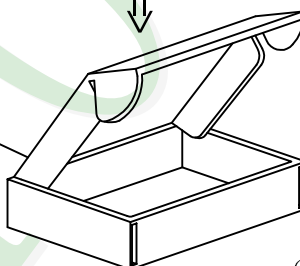Use empty tray
空 盤

+
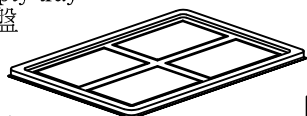
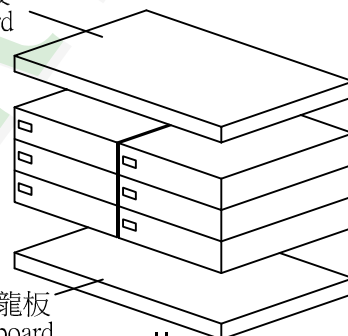Put products into the tray

⇩

Tray stacking

A

B
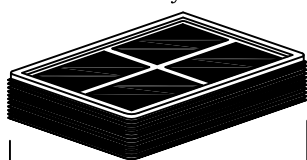
(1)多層薄膜
POF

(2)TRAY 盤
Tray

(4)保利龍板
Polylon board

(4)保利龍板
Polylon board

⇩

(3)內盒
Product Box

⇩

(5)外紙箱
Carton

特 記 事 項 (REMARK)

A

斜角   Detail B

Tray 2

圓角   Tray 1
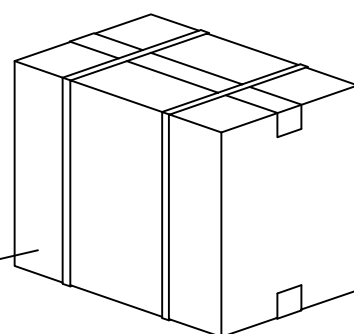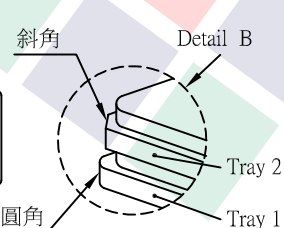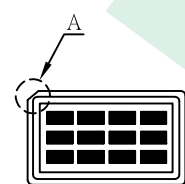
5.外購tray:TY00000000392
自製tray:TYSG000000227

4.TRAY盤相疊時,需旋轉180度,請詳見B視圖
Rotate tray 180 degrees and place on top of stack.
Check the tray stack using Fig. B.