



**5.0 inch TFT LCD
Without Touch Panel
SPECIFICATION**

MODEL NAME: LM MDF050CXN1

Date: 2019/04/18

Customer Signature		
Customer		
Approved Date	Approved By	Reviewed By

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1 Document revision history :

DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY	APPROVED BY
A	2019.04.18	First Release.		



2. General Description

- 5.0" (diagonal), 720 x 3 RGB x 1280 dots, 16.7M colors, TFT LCD module.
- Viewing Direction: ALL.
- Driving IC: ILI9881D
- MIPI interface
- Logic voltage: 2.8V (typ.).
- Without touch panel.

3. Mechanical Specifications

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Table 1

Parameter		Specifications	Unit
Outline dimensions		67.16*120.60	mm
Color TFT 240xRGBx320	TP view area	-	mm
	TP active area	-	mm
	LCD active area	62.10*(W) x 110.40(H)	mm
	Display format	720 x 3 RGB x 1280	dots
	Color configuration	RGB Vertical stripe	-
	Dot pitch	0.02875*3(W) (RGB) x 0.08625(H)	mm
Weight		TBD	grams



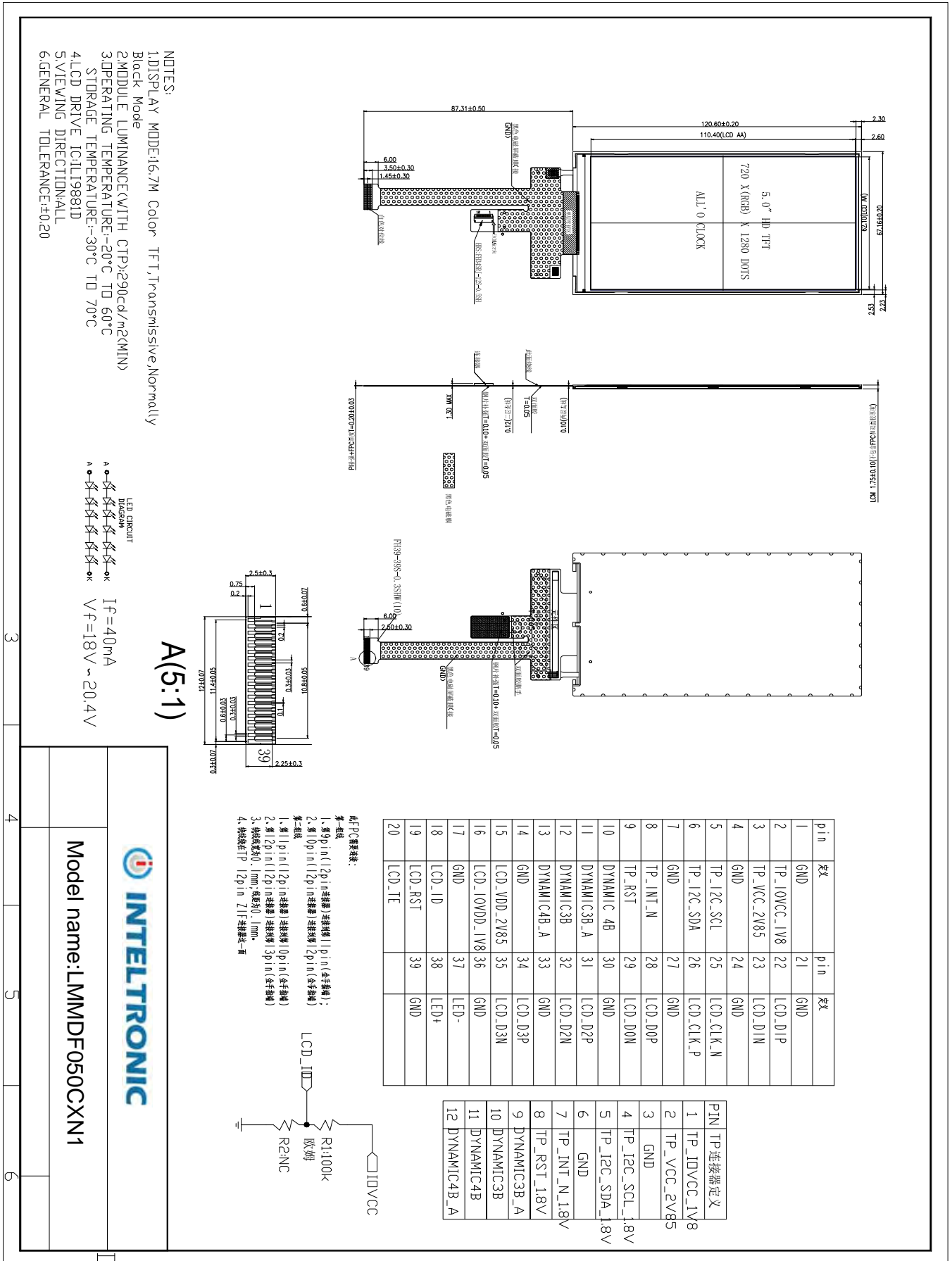


Figure 1: Outline Drawing

4. Interface signals

Table 2: Pin assignment

Pin No.	Symbol	Description
1	GND	Ground
2	TP_IOVCC_1V8	Power supply for TP I/O block
3	TP_VCC_2V85	Power supply for TP system
4	GND	Ground
5	TP_I2C_SCL	I2C interface system clock pin
6	TP_I2C_SDA	I2C interface data input pin
7	GND	Ground
8	TP_INT_N	I2C interface interrupt pin
9	TP_RST	I2C interface system reset pin
10	DYMAIC_4B	
11	DYMAIC_3B_A	
12	DYMAIC_3B	
13	DYMAIC_4B_A	
14	GND	Ground
15	LCD_VDD_2V85	Power supply for system
16	LCD_IOVCC_1V8	Power supply for I/O block
17	GND	Ground
18	LCD_ID	Hardware distinguishes between different screens
19	LCD_RST	Reset pin. This signal will reset the device and must be applied to properly initialize the chip
20	LCD_TE	Tearing effect output
21	GND	Ground
22	LCD_D1P	Positive polarity of low voltage differential data signal
23	LCD_D1N	Negative polarity of low voltage differential data signal
24	GND	Ground
25	LCD_CLK_N	Negative polarity of low voltage differential clock signal
26	LCD_CLK_P	Positive polarity of low voltage differential clock signal
27	GND	Ground
28	LCD_D0P	Positive polarity of low voltage differential data signal
29	LCD_D0N	Negative polarity of low voltage differential data signal
30	GND	Ground
31	LCD_D2P	Positive polarity of low voltage differential data signal
32	LCD_D2N	Negative polarity of low voltage differential data signal
33	GND	Ground
34	LCD_D3P	Positive polarity of low voltage differential data signal
35	LCD_D3N	Negative polarity of low voltage differential data signal
36	GND	Ground
37	LED-	Negative power supply for back light
38	LED+	Positive power supply for back light
39	GND	Ground



5. Absolute Maximum Ratings

5.1 Electrical Maximum Ratings – for IC Only

Table 3: Electrical Maximum Ratings – for IC

Parameter	Symbol	Min.	Max.	Unit	Note
Power supply voltage (VCC)	VCC	-0.3	+3.6	V	1
Power supply voltage (IOVCC)	IOVCC	-0.3	+3.6	V	1

Note:

- 1.VCC, GND must be maintained.
- 2.The modules may be destroyed if they are used beyond the absolute maximum ratings.

5.2 Environmental Condition

Table 4

Item	Operating temperature (Topr)		Storage temperature (Tstg) (Note 1)		Remark
	Min.	Max.	Min.	Max.	
Ambient temperature	-20°C	+60°C	-30°C	+70°C	Dry
Humidity (Note 1)	使箱内温度达到 (60±2°C)，相对湿度达到 90% ~95%，温湿度稳定后持续 96h;				No condensation

Note 1: Product cannot sustain at extreme storage conditions for long time.

6. Electrical Specifications

Typical Electrical Characteristics

At Ta = 25 °C, VCC=2.5V to 3.3V , IOVCC= 1.65V to 3.3V, GND=0V.

Table 5

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (analog)	VCC-GND		2.5	2.8	3.3	V
Supply voltage (logic)	IOVCC-GND		1.65	1.8/2.8	3.3	V
Supply current (Logic & LCD)	ICC	VCC=2.8V	-	-	10	mA
Supply voltage of white LED backlight	VLED =V(BL+)- V(BL-)	Forward current =40 mA Number of LED dies = 12	-	19.6	-	V
Luminance (on the module surface)			-	350	-	cd/m ²



7. Optical Characteristics

Table 7: Optical specifications

Items		Symbol	Condition	Specifications			Unit	Note
				Min.	Typ.	Max.		
Contrast Ratio		CR		-	800	-	-	
Response Time		T _R		-	10	-	ms	
		T _F		-	20	-	ms	
Chromaticity	Red	X _R	-0.02	0.654	+0.02	-		
		Y _R				-		
	Green	X _G				0.259	-	
		Y _G				0.574	-	
	Blue	X _B				0.140	-	
		Y _B				0.084	-	
	White	X _W				0.303	-	
		Y _W				0.323	-	
Viewing angle	Hor.	ϕ1(3 o'clock)	Center CR≥10	-	80	-	deg.	
		ϕ2(9 o'clock)		-	80	-		
	Ver.	θ2(12 o'clock)		-	80	-		
		θ1(6 o'clock)		-	80	-		
NTSC ratio				-			%	

Note 1: Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

L63: Luminance of gray level 63

L0: Luminance of gray level 0

$$CR = CR(10)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note 5.

Note 2: Definition of Response Time (T_R , T_F):

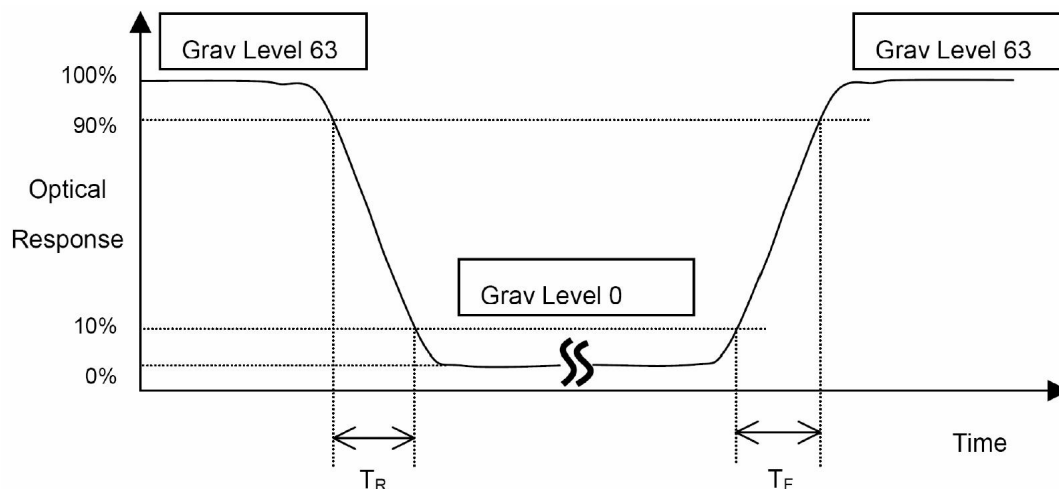


Figure 3



Note 3: Viewing Angle

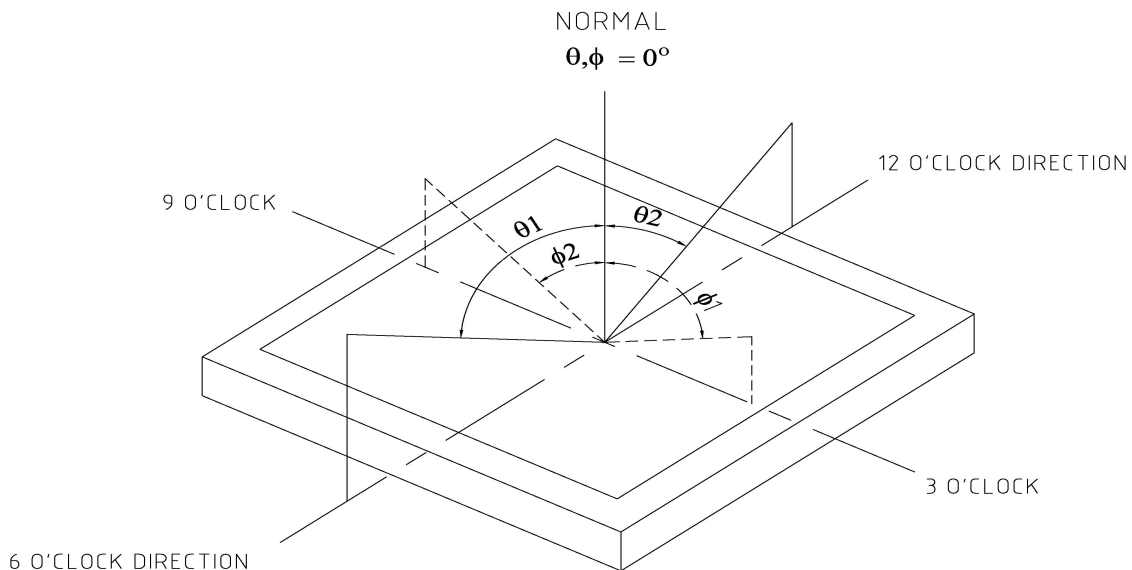


Figure 4

The above “Viewing Angle” is the measuring position with Largest Contrast Ratio; not for good image quality. View Direction for good image quality is 6 O’clock. Module maker can increase the “Viewing Angle” by applying Wide View Film.

Note 4: Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.

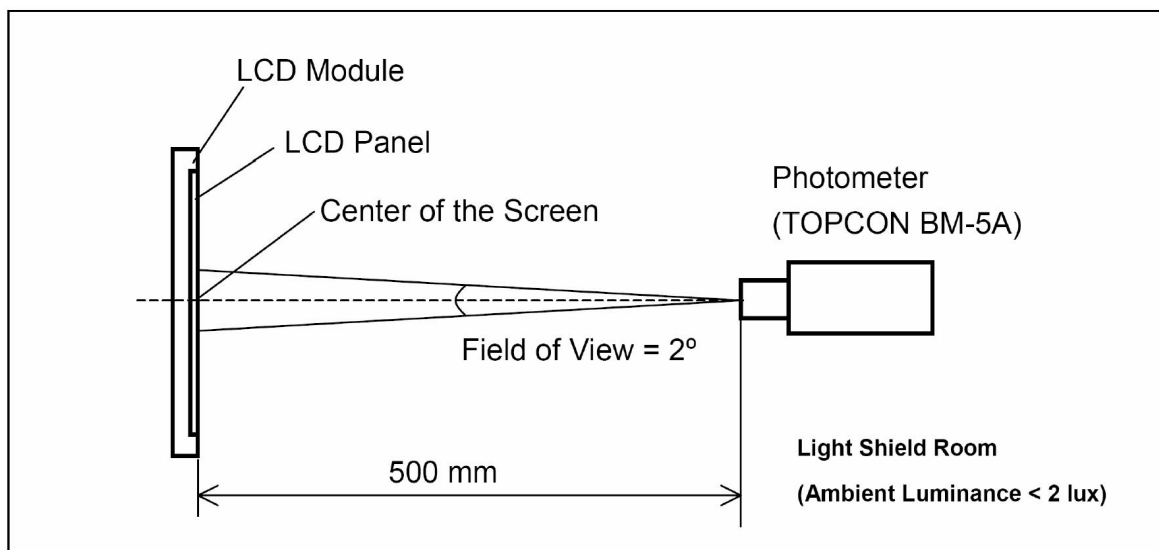


Figure 5



8. Timing Characteristics

8.1 80-system Bus Interface Timing Characteristics of IC

Table 8: Normal Write Mode (IOVCC=1.65~3.3V)

Symbol	Parameter	Min	Typ	Max	Unit
t_{cycle}	Clock Cycle Time (write cycle)	100	-	-	ns
t_{cycle}	Clock Cycle Time (read cycle)	1000	-	-	ns
t_{AS}	Address Setup Time	0	-	-	ns
t_{AH}	Address Hold Time	0	-	-	ns
t_{DSW}	Data Setup Time	5	-	-	ns
t_{DHW}	Data Hold Time	5	-	-	ns
t_{ACC}	Data Access Time	250	-	-	ns
t_{OH}	Output Hold time	100	-	-	ns
PW_{CSL}	Pulse Width /CS low (write cycle)	50	-	-	ns
PW_{CSH}	Pulse Width /CS high (write cycle)	50	-	-	ns
PW_{CSL}	Pulse Width /CS low (read cycle)	500	-	-	ns
PW_{CSH}	Pulse Width /CS high (read cycle)	500	-	-	ns
t_R	Rise time	-	-	4	ns
t_F	Fall time	-	-	4	ns

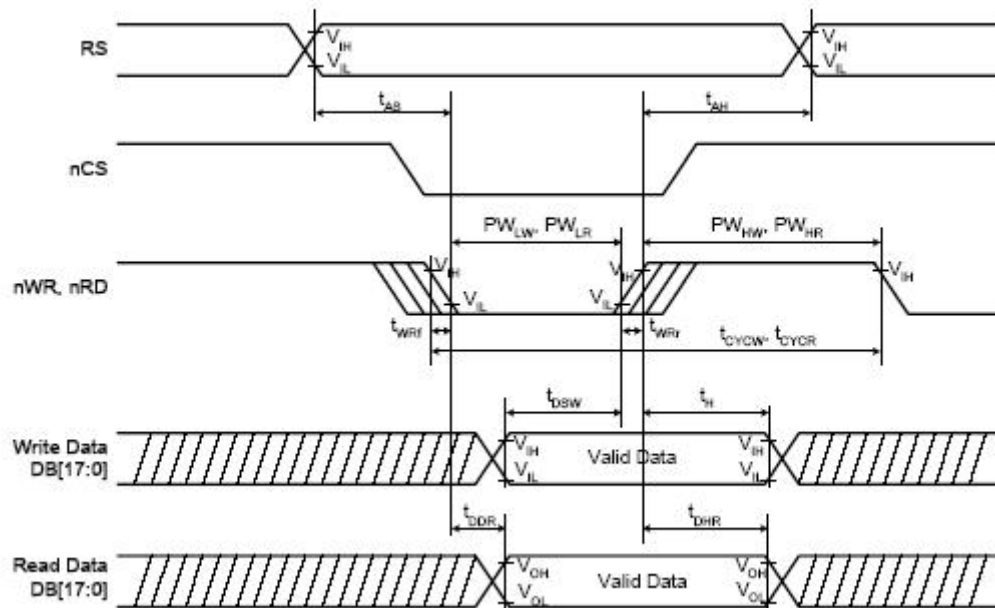


Figure 7. 80-system Bus Timing



8.2 Reset Operation of IC

Table 9: Reset Timing Characteristics (IOVCC=1.65~3.3V)

Item	Symbol	Unit	Min.	Typ.	Max.
Reset low-level width	tRES	ms	1	-	-
Reset rise time	trRES	μs	-	-	10

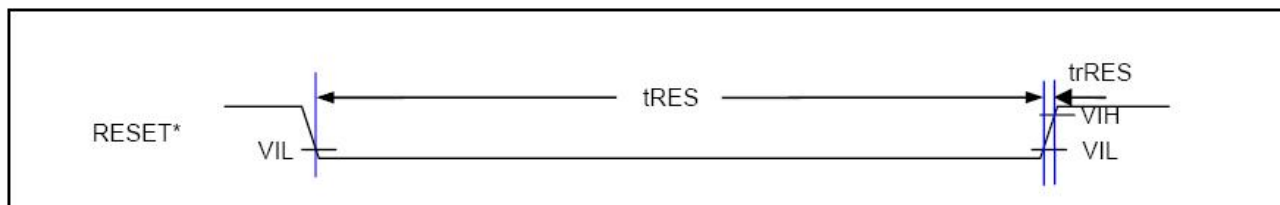


Figure 8: Reset Timing

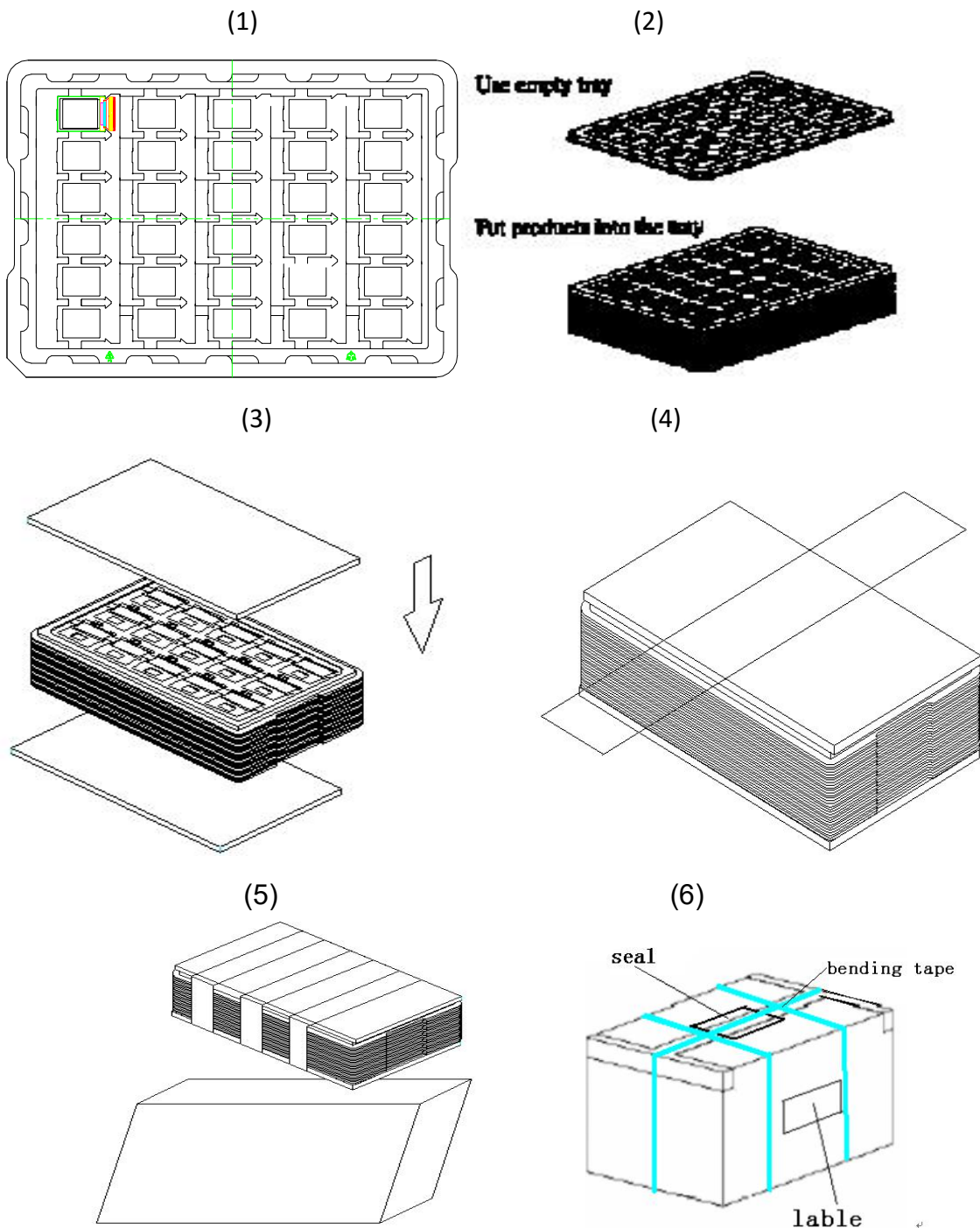
9. Reliability Test Item

Test Item	Sample Type	Test Condition	Test result determinant gist
High temperature storage	正常温度	60±3℃;96H	the inspection of appearance and function character.
Low temperature storage	正常温度	-20±3℃;120H	
High temperature /humidity storage	正常温度	50℃±3℃,90%±3%RH;96H	
High temperature operation	正常温度	60±3℃;96H	no objection of the function character; no fatal objection of the appearance.
Low temperature operation	正常温度	-20±3℃;96H	
High temperature /humidity operation	正常温度	40℃±3℃,90%±3%RH;96H	
Temperature Shock	正常温度	-20±3℃,30min→60±3℃,30 min;32cycle	inspect the objections appearance、function & the whole structure



Packing (Reference only)

Packing Method



1. Put module into tray cavity :
2. Tray stacking
3. Put 1 cardboard under the tray stack and 1 cardboard above:
4. Fix the cardboard to the tray stack with adhesive tape:
5. Put the tray stack into carton.
6. Carton sealing with adhesive tape.

- END -



■ Inspection Specifications

The buyer (customer) shall inspect the modules within twenty calendar days since the delivery date (the "inspection period") at its own cost. The results of the inspection (acceptance or rejection) shall be recorded in writing, and a copy of this writing will be promptly sent to the seller.

The buyer may, under commercially reasonable reject procedures, reject an entire lot in the delivery involved if, within the inspection period, such samples of modules within such lot show an unacceptable number of defects in accordance with this incoming inspection standards, provided however that the buyer must notify the seller in writing of any such rejection promptly, and not later than within three business days of the end of the inspection period.

Should the buyer fail to notify the seller within the inspection period, the buyer's right to reject the modules shall be lapsed and the modules shall be deemed to have been accepted by the buyer.

■ Warranty

Inteltronic Inc. warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for one year from the date of purchase.

Inteltronic Inc. will be limited to replace or repair any of its module which is found and confirmed defective electrically or visually when inspected in accordance with Inteltronic Inc. general module inspection standard.

This warranty does not apply to any products which have been on customer's production line, repaired or altered by persons other than repair personnel authorized by Inteltronic Inc., or which have been subject to misuse, abuse, accident or improper installation. Inteltronic Inc. assumes no liability under the terms of this warranty as a consequence of such events.

If an Inteltronic Inc. product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time and freight. In returning the modules, they must be properly packaged with original package; there should be detailed description of the failures or defect.

■ RMA

Products purchased through Inteltronic Inc. and under warranty may be returned for replacement. Contact support@inteltronicinc.com for RMA number and procedures



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