AMINED BY:		FILE NO . CAS-0008513
C C Chen	EMERGING DISPLAY	ISSUE : DEC.15, 2017
PROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 23
Justin Horng		VERSION: 2
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
FOR	DEL NO.:  ET039000DMA (GP)  MESSRS:	Station only

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RECORD	S OF R REVISED	EVISION		3	EP.04, 2017
DATE	PAGE		SUMMARY		
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#### 1. GENERAL SPECIFICATIONS

1.1 DATA SHEETS NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO:

#### ILITEK ILI6480G6

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EDT GREEN PRODUCT (GP)
REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS
CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM,
POLYBROMINATED BIPHENYLS (PBB), POLYBROMINATED
DIPHENYL ETHERS (PBDE), POLYCHLORINATED BIPHENYLS (PCB)
CATEGORY, POLYCHLORINATED NAPHTHALENE (PCN) CATEGORY,
POLYCHLORINATED TERPHENYLS (PCT) CATEGORY, CHLORINATED
PARAFFINS (CP) CATEGORY, TRIBUTHYL TIN CATEGORY / TRIPHENYL TIN
CATEGORY, ASBESTOS, SPECIFIC AZO COMPOUNDS, FORMALDEHYDE,
POLYVINYL CHLORIDE (PVC) AND PVC BLENDS, OTHER BROMINATED
ORGANIC COMPOUNDS AND OTHER CHLORINATED ORGANIC COMPOUNDS.

#### 2. MECHANICAL SPECIFICATIONS

(1) DIAGONALS	3.9 inch
(2) NUMBER OF DOTS	- 480W * (BGR) * 128H DOTS
(3) MODULE SIZE	- 105.5W * 40.64H *2.95D mm
	(WITHOUT FPC)
(4) EFFECTIVE AREA	- 98.04W * 28.34H mm
(5) ACTIVE AREA	- 95.04W * 25.34H mm
(6) DOT SIZE	. 0.066W * 0.198H mm
(7) PIXEL PITCH	. 0.198W * 0.198H mm
(8) LCD TYPE	. TFT, TRANSMISSIVE, NORMALLY BLACK,
	ANTE-GLARE
(9) COLOR	- 16.7M
(10) VIEWING DIRECTION	. SUPER WIDE VIEW
(11) BACK LIGHT	LED , COLOR : WHITE
( 12 ) INTERFACE MODE	- RGB(24 BIT ) PARALLEL
	(DE/SYNC MODE)

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#### 3. ABSOLUTE MAXIMUM RATINGS

#### 3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER VOLTAGE	VCC-VSS	-0.3	4.0	V	VSS=0
LED BACKLIGHT POWER DISSIPATION	РО	_	714	mW	
LED BACKLIGHT PEAK FORWARD CURRENT	IF	_	30	mA	33.

#### 3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
I I EIVI	MIN.	MAX.	MIN.	MAX.	KEMAKK
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE(1),(2)
HUMIDITY	NOT	E(3)	NOTI	E(3)	WITHOUT CONDENSATION
VIBRATION	9-	3.92 m/s <sup>2</sup> ( 0.4 G)	-5	19.6 m/s <sup>2</sup> ( 2.0 G )	10~55Hz X, Y, Z, EACH 2HRS
SHOCK	-01	58.8 m/s <sup>2</sup> ( 6 G)	0	$980 \text{ m/s}^2$	6 ms XYZ DIRECTIONS 3 TIMES EACH
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACC	EPTABLE	

NOTE (1): Ta AT  $-30^{\circ}$ C: 48HRS MAX.

80°C: 48HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT

TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3):  $Ta \le 60^{\circ}C$ : 90% RH MAX. (48HRS MAX).

Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C (48HRS MAX). Ellerighte Color diet

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#### 4. ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$ 

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY	VCC-VSS	_	3.0	3.3	3.6	V	
OPERATING CURRENT	ICC	_	_	22	27	mA	
INPUT LOW VOLTAGE	$V_{ m IL}$	_	VSS		0.3*VCC	V	
INPUT HIGH VOLTAGE	$V_{IH}$	_	0.7*VCC		VCC	V	
POWER SUPPLY FOR	VF	IF =20mA	18.9	21	23.8	V	NOTE (1)
LED BACKLIGHT							
LED LIFE TIME	_	I <sub>LED</sub> =20mA	30K	_	_	hrs	NOTE (2)
		(PER. LED)					NOTE (3)

NOTE (1): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



VLED-K

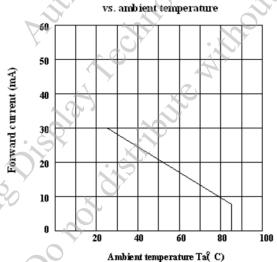
NOTE (2): CONDITIONS; TA=25°C, CONTINUOUS LIGHTING

NOTE (3): DEFINITIONS OF FAILURE

A. LCM LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

NOTE (4): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT (PER LED)

Forward current derating curve

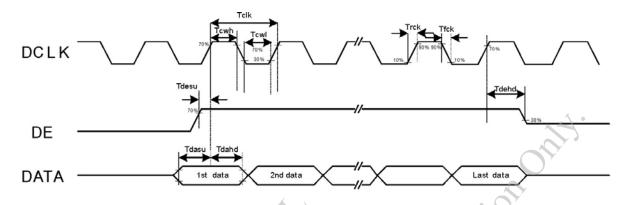


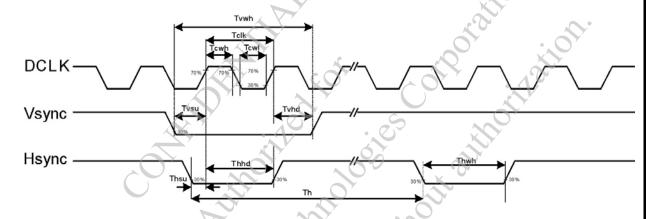
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#### 5. TIMING CHART

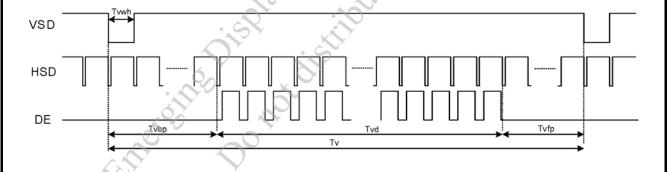
### 5.1 CLOCK AND DATA INPUT WAVEFORMS





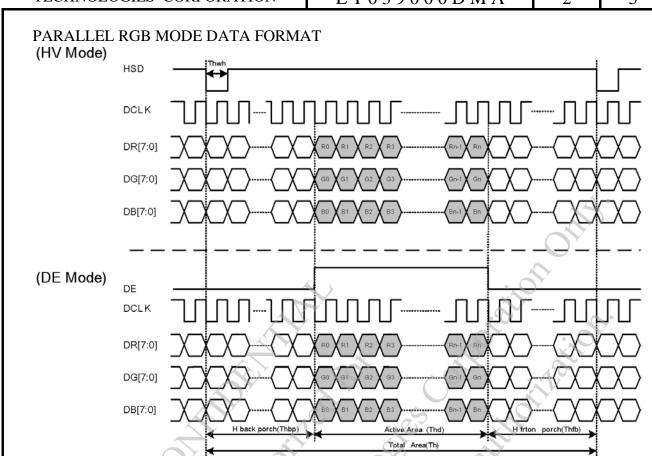
#### 5.2 DATA INPUT FORMAT

#### **VERTICAL INPUT TIMING**



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#### PARALLEL RGB INPUT TIMING TABLE

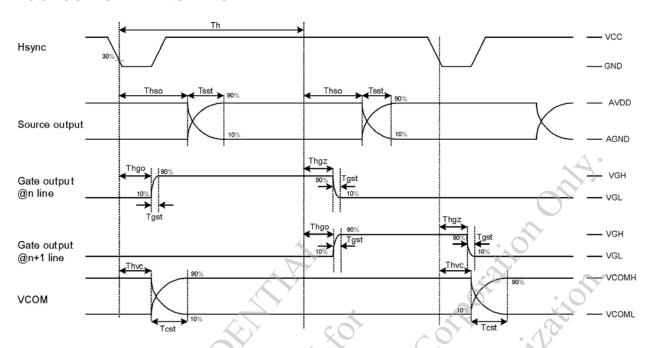
PARAMETER	SYMBOL	VALUE			UNIT
FARAWETER	SIMBOL	MIN.	TYP.	MAX.	UNII
DCLK FREQUENCY	fclk	5	9	12	MHz
VSD PERIOD TIME	Tv	277	288	400	Н
VSD DISPLAY AREA	Tvd	3	272		Н
VSD BACK PORCH	Tvb	3	8	31	Н
VSD FRONT PORCH	Tvfp	2	8	93	Н
HSD PERIOD TIME	Th	520	525	800	DCLK
HSD DISPLAY AREA	Thd		480		DCLK
HSD BACK PORCH	Thbp	36	40	255	DCLK
HSD FRONT PORCH	Thfp	4	5	65	DCLK

NOTE: SYNC MODE: VSYNC BACK PORCH=8 H
HSYNC BACK PORCH=40 DCLK

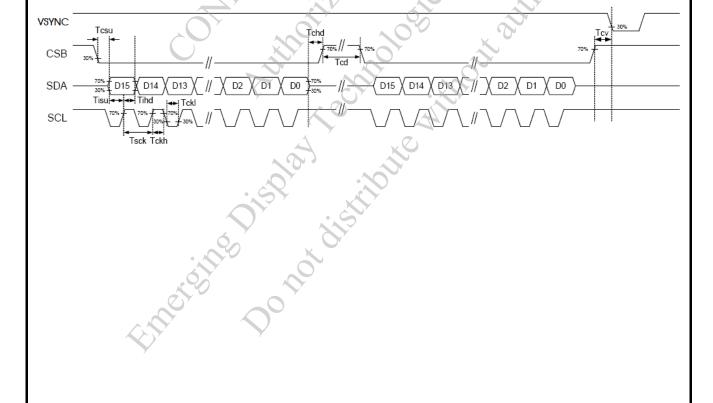
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#### 5.3 OUTPUT TIMING DIAGRAM



#### 5.4 3-WIR TIMING DIAGRAM



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#### 6. OPTICAL CHARACTERISTICS (NOTE 1)

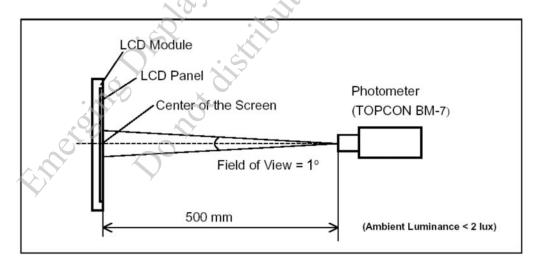
#### 6.1 OPTICAL CHARACTERISTICS

Ta = 2.5°C

ITEM		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK
				0 00	70	75			
VIEWING ANGL	E	$\theta_{ ext{y-}}$	CR ≥ 10	$\theta_x=0^{\circ}$	70	75		deg.	NOTE (2)
VIEWING ANGL	Æ	$\theta_{x+}$		θ <sub>y</sub> =0°	70	75			NOTE (3)
		$\theta_{x}$			70	75			4 .
CONTRAST RAT	Oľ	CR	θx=0°,	θу=0°	400	500		_ ^	NOTE(3)
RESPONSE TIME	Ξ	TR(t r+ t f)	θx=0°,	θy=0°		30	50	msec	NOTE (4)
THE BRIGHTNE	SS	В	θx=0°,		500	550		cd/m <sup>2</sup>	NOTE (5)
OF MODULE	1		IF =2	0mA			. (	SY.	` '
	WHITE	Wx			0.28	0.31	0.34		
	WIIIIE	Wy			0.33	0.36	0.39		<b>~</b> .
COLOR OF	RED -	Rx	θx=0°,		0.585	0.615	0.645	· \ C	O
COLOR OF CIE	KED	Ry		θy=0° 0mA	0.33	0.36	0.39		NOTE (6)
COORDINATE	CDEEN	Gx	NTSC		0.31	0.34	0.37	.1	NOIE (6)
COORDINATE	GREEN -	Gy	1(150	0.00%	0.595	0.625	0.655	<u> </u>	
	BLUE	Bx	1/2		0.135	0.165	0.195	)	
	BLUE	Ву			0.07	0.1	0.13		
THE BRIGHTNE UNIFORMITY	SS OF	- 8	θx=0°, IF =2	θy=0° 0mA	75	80	6	%	NOTE (5)

#### NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.

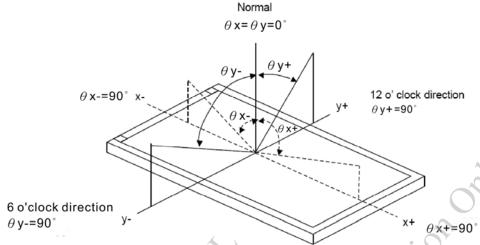


### EMERGING DISPLAY

TECHNOLOGIES CORPORATION

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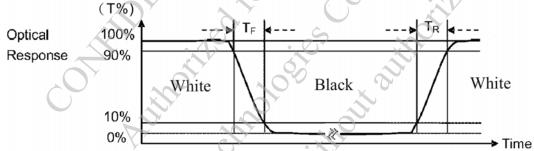
NOTE (2): DEFINITION OF VIEWING ANGLE:



NOTE (3): DEFINITION OF CONTRAST RATIO:

CONTRAST RATIO(CR) =  $\frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$ 

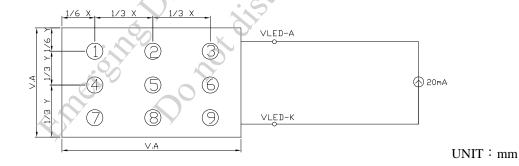
NOTE (4): DEFINITION OF RESPONSE TIME: TR AND TF
THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE ( 5 ) : MEASURED AT THE CENTER AREA OF THE PANEL WHEN ALL THE INPUT TERMINALS OF LCD PANEL IS ELECTRICALLY OPENED.

NOTE ( 6 ) : THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

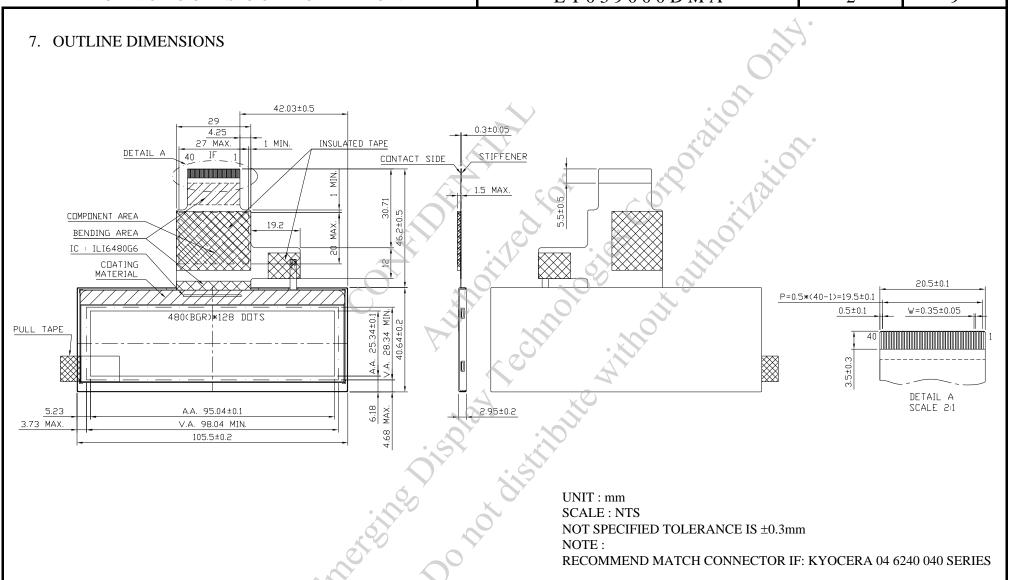
#### 6.2 THE BRIGHTNESS TEST METHOD



#### 6.3 THE BRIGHTNESS UNIFORMITY CALCULATE METHOD

### E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

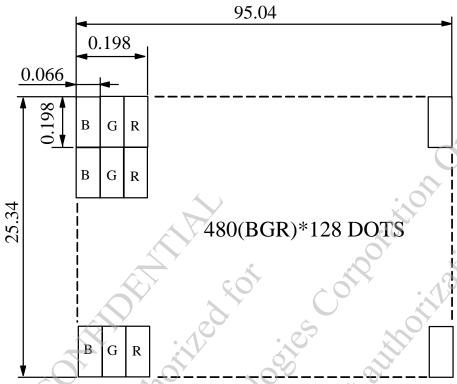
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UNIT: mm
SCALE: NTS
NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

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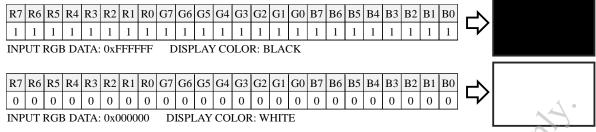
#### 10. INTERFACE SIGNALS

#### 10.1 LCM INTERFACE

PIN NO.	SYMBOL	I/O/P	FUNCTION
1	VLED-K	P	POWER SUPPLY FOR LED (CATHODE)
2	VLED-A	P	POWER SUPPLY FOR LED (ANODE)
3	VSS	P	POWER GROUND
4	VCC	P	POWER VOLTAGE
5	R0	P	RED DATA (LSB)
6	R1	I	RED DATA
7	R2	I	RED DATA
8	R3	I	RED DATA
9	R4	I	RED DATA
10	R5	I	RED DATA
11	R6	I	RED DATA
12	R7	I	RED DATA (MSB)
13	G0	I	GREEN DATA (LSB)
14	G1	I	GREEN DATA
15	G2	I	GREEN DATA
16	G3	(I)	GREEN DATA
17	G3 G4	I	GREEN DATA
18	G5 <	I	GREEN DATA
19	G6	I	GREEN DATA
20	G7	I	GREEN DATA (MSB)
21	B0	I	BLUE DATA (USB)
22	B1	I	BLUE DATA (ESB)
23	B2	I	BLUE DATA
24	B3	I	BLUE DATA
25	B4	I	BLUE DATA
26	B5	I	BLUE DATA
27	B6	I	BLUE DATA
28	B7	I	BLUE DATA (MSB)
29	VSS	P	POWER GROUND
30	DCLK	I	DOT DATA CLOCK
30	DCLK	1	STANDBY MODE
31	STB	T	WHEN STB: "H", NORMAL OPERATION
31	SID	5 1	WHEN STB: "L", STANDBY MODE
22	HGMMG	190 <u>.                                    </u>	
32	HSYNC	I	HORIZONTAL SYNC SIGNAL
33	VSYNC	1	VERTICAL SYNC SIGNAL
34	DE	I	DATA ENABLE
			HV MODE OR DE MODE CONTROL SIGNAL
25	Impor	<b>T</b>	HVDSL="H": SET UNDER HV MODE, VSYNC AND HSYNC
35	HVDSL	I	SIGNALS HAVE TO PROVIDE BY SYSTEM.
			HVDSL="L": SET UNDER DE MODE, DE SIGNAL HAVE TO
27	TIGG	D	PROVIDE BY SYSTEM.
36	VSS	P	POWER GROUND
37	CSB	I	THIS PIN ACT AS 3-WIRE "CSB" PIN
38	SDA	I	THIS PIN ACT AS 3-WIRE "SDA" PIN
39	SCL	I	THIS PIN ACT AS 3-WIRE "SCL" PIN
40	NC		NO CONNECT

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#### 10.2 RGB INPUT DATA AND THE DISPLAY COLOR

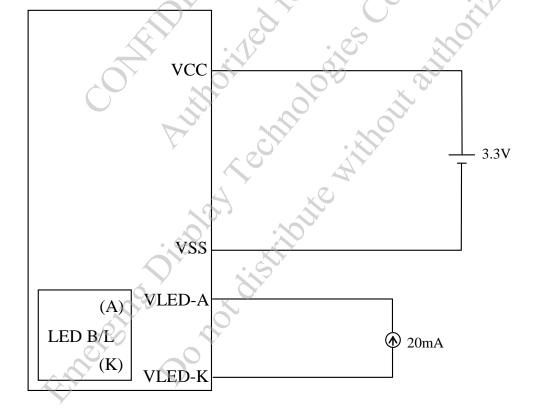


DISPLAY COLOR

PLEASE NOTE THAT THE DISPLAY COLOR IS INVERTED COMPARED TO NORMAL RGB DATA, THUS THE SOFTWARE OR GRAPHIC CONTROLLER IN THE FRONT END HAVE TO CORRECT IT TO MAKE THE DISPLAY IMAGE COLOR CORRECT.

#### 11. POWER SUPPLY

#### 11.1 POWER SUPPLY FOR LCM



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#### 12. INSPECTION CRITERION

#### 12.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.( E.D.T ) TO CUSTOMERS

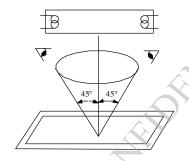
#### 12.2 INSPECTION CONDITIONS

#### 12.2.1 (1)OBSERVATION DISTANCE: 35±5cm

(2) VIEW ANGLE: ±45°

#### PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 45°



LINE OF SIGHT FOR INSPECTION SHALL BE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT 45° WITH RESPECTS TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE CONE AXIS MUST BE PERPENDICULAR NORMAL TO LCD SURFACE AND PASSES THROUGH THE FLUORESCENT LAMP.

#### 12.2.2 ENVIRONMENT CONDITIONS:

AMBIEN	25±5°C	
AMBII	$65 \pm 20\%$ RH	
AMBIENT	COSMETIC INSPECTION	600~800 lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 lux
INSP	15 secs (MAX.)	

### 12.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

#### 12.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD: MIL-STD-105E

NORMAL INSPECTION, SINGLE SAMPLING

LEVEL II

(b)AQL: MAJOR DEFECT: AQL 0.65 MINOR DEFECT: AQL 1.0

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#### 12.3 INSPECTION STANDARDS

#### 12.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED     DISPLAY FUNCTION, FOR ALL     AND SPECIFIED DOTS     EX: DISCONNECTION, SHORT     CIRCUIT ETC	•
MAJOR DEFECT	2.BACKLIGHT	NO LIGHT     FLICKERING AND OTHER ABNORMAL ILLUMINATION     SUBJECT TO INDIVIDUAL	0.65
	3.DIMENSIONS	ACCEPTANCE SPECIFICATIONS	>*
MINOR DEFECT	1.DISPLAY ZONE  2.BEZEL ZONE  3.PCB	<ul> <li>BLACK/WHITE SPOT</li> <li>BUBBLES ON POLARIZER</li> <li>BLACK/WHITE LINE</li> <li>SCRATCH</li> <li>CONTAMINATION</li> <li>LEVER COLOR SPREAD</li> <li>STAINS</li> <li>SCRATCHES</li> <li>FOREIGN MATTER</li> <li>CRACKS</li> <li>SCRATCHES</li> <li>SCRATCHES</li> <li>SCRATCHES</li> <li>SCRATCHES</li> </ul>	1.0
	4.SOLDERING  5.DISPLAY ON (ALL ON)	<ul> <li>INSUFFICIENT SOLDER</li> <li>SOLDERED IN INCORRECT POSITION</li> <li>CONVEX SOLDERING SPOT</li> <li>SOLDER BALLS</li> <li>SOLDER SCRAPS</li> <li>LIGHT LINE</li> </ul>	

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#### 12.3.2 MODULE DEFECTS CLASSIFICATION

NO.	ITEM	CRIT	ΓERIA	
1	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND	SPEC	
2	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC		
3	DOT DEFECT	PANEL IS DISPLAYING UNDER B 3. DARK DOT : DOTS APPEAR DARK AND UNCH	ACCEPTABLE COUNT $ \begin{array}{c c} N \leq 2 \\ N \leq 3 \\ N \leq 4 \end{array} $ OVER 1/2 OF WHOLE DOT IS DOT.  CHANGED IN SIZE IN WHICH LCD LACK PATTERN.	
4	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA		PERMISSIBLE NO. IGNORE 3 NONE	
5	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	AVERAGE DIAMETER (mm): D $D \le 0.15$ $0.15 < D \le 0.4$ $0.4 < D$ NOTE : DIAMETER D=(a+b)/2	NUMBER OF PIECES PERMITTED  IGNORE  3  NONE	

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110	YERRA 6	Γ	CD ITED I			
NO.	ITEM		CRITERIA			
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED		
		DAME OF THE	D ≤ 0.25	IGNORE		
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 4		
		FOLARIZER	0.5 < D	NONE		
		CLIDEA CE CTAINC	D < 0.1	IGNORE		
		SURFACE STAINS	$0.1 < D \le 0.3$	N ≤ 3		
		CF FAIL / SPOT	D < 0.1 $0.1 < D \le 0.3$	IGNORE N < 3		
	DUDDI EC OE	NOTE · (1)POLARIZ		AS THE BUBBLE APPEARS		
	BUBBLES OF POLARIZER	` /	VE DISPLAY AREA. THE I			
6	/DIRT/CF FAIL			HE POLARIZER BUBBLE		
	/SURFACE STAINS		ON THE OUTSIDE OF AC			
	Joera Mez Simily			S DEFINED AS IT CAN BE		
		OBSERVI	ED WHEN THE MODULE	IS POWER ON.		
		(3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED				
		AS FOLL		·		
		AVERAG	E DIAMETER (D)= $(a+b)/2$			
				1,0		
		$\mathcal{O}_{\lambda}$	) (b	A5-7-1		
		<b>V</b>				
		K	a			
7	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED				
8	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	GH 2% ND FILTER		
	UNEVEN COLOR					
9	SPREAD,	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.				
	COLORATION	, , , ,				
	BEZEL		HAVE RUST, BE DEFORM			
10	APPEARANCE	PRINTS STAINS HAVE OTHER CONTAMINATION.				
		(2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS. (1)THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE				
		THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE				
		THAN THREE PLACES.				
	A	(2)NO OXIDATION OR CONTAMINATION PCB TERMINALS (3)PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION				
	6	CHARACTERISTIC		N THE PRODUCTION		
11	PCB			ISSING PARTS OR EXCESS		
		PARTS.	LIVO WRONG PARTS, MI	ISSII VO I TIKTIS OK EMELSS		
	200		THE PCB SHOULD CONFO	ORM TO THE PRODUCT		
		CHARACTERISTIC		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
		(5)IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR				
	~ Y		O; MAKE SURE IT IS SMO			

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NO.	ITEM	CRITERIA
NO.	TTEM	(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE
		(2)INSUFFICIENT SOLDER
		(a)LSI, IC
		A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR
		"HEEL" OF LEAD AND PAD
		SOLDER FILLET
		(b)CHIP COMPONENT
		· SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE
		WETTING
		SOLDER FILLET
		1/2
		· SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF
12	SOLDERING	SIDES AND FRONT SURFACE AREA ARE COVERED
		X .0
		SOLDER
		(2) DADTE ALIGNACIAT
		(3)PARTS ALIGNMENT (a)LSI, IC
	Ó	LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE
		LEAD WIDTH IS MORE THAN 30% BETOND LAD OUTLINE
	.62	
	S.Meiding	
	7	
		<b>*</b>

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NO	ITEM	CDITEDIA
NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12	SOLDERING	
		<ul> <li>(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB.</li> <li>(5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE.</li> <li>(6)NO RESIDUE OR SOLDER BALLS ON PCB.</li> <li>(7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.</li> </ul>
13	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14	GENERAL APPEARANCE	<ul> <li>(1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP.</li> <li>(2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP.</li> <li>(3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT.</li> <li>(4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS.</li> <li>(5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER.</li> <li>(6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR.</li> <li>(7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED.</li> <li>(8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET.</li> <li>(9)LCD PIN LOOSE OR MISSING PINS.</li> <li>(10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET.</li> <li>(11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET.</li> <li>(12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.</li> </ul>

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NO.	ITEM	CRITERIA			
		THE LCD WITH EXTENSIVE (	CRACK IS NOT ACCEPTABLE		
		GENERAL GLASS CHIP:	a b c ≤t/2 <viewing 2="" 8x="" area="" t="" ≤1="">,≤2t ≤W/2 ≤1/8X  *W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS</viewing>		
15	CRACKED GLASS	CORNER PART :  b  c  a  CHIP ON ELECTRODE PAD	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		
	Cinciple 1	c a	a b c ≤t ≤1/8X ≤L  *X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH ①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS ②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DAMAGED		

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#### 13. RELIABILITY TEST

#### 13.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION		
1	HIGH TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR		
	OPERATION	240 HRS		
2	LOW TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR		
_	OPERATION	240 HRS		
3	HIGH TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR		
	STORAGE	240 HRS		
4	LOW TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR		
4	STORAGE	240 HRS		
5	HIGH TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH		
	/HUMIDITY TEST	240 HRS		
	STORAGE	240 11K3		
		THE SAMPLE SHOULD BE ALLOWED TO STAND THE		
		FOLLOWING 10 CYCLES OF OPERATION:		
		+80°C		
	THERMAL SHOCK			
6	(NOT OPERATED)	-30°C		
	(Iver or Elarize)	30 min 30 min 30 min 30 min		
	Y Y	- 50 mm   50		
		1 CYCLE		
	Fan	<b>-</b>		
7	ESD	AIR DISCHARGE ± 12KV		
	(ELECTROSTATIC	CONTACT DISCHARGE ± 8KV		
	DISCHARGE)	ACCORDING TO IEC-61000-4-2		
	(NOT OPERATED)	ACCORDING TO IEC-01000-4-2		

NOTE ( 1 ): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION FAILURE ISSUE OCCURRED.

# 13.2 TESTING CONDITIONS AND INSPECTION CRITERIA FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 14.1, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT REFER TO SPECIFICATION		THE CURRENT CONSUMPTION SHOULD
1	CONSUMPTION	REFER TO SPECIFICATION	CONFORM TO THE PRODUCT SPECIFICATION.
			AFTER THE TESTS HAVE BEEN EXECUTED,
2	CONTRAST		THE CONTRAST MUST BE LARGER THAN HALF
			OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

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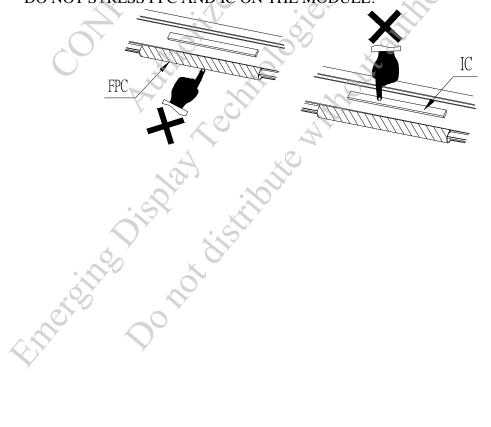
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#### 13.3 OPERATION

- 13.3.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.3.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 13.3.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 13.3.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

  IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 13.3.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

  DO NOT STRESS FPC AND IC ON THE MODULE!



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#### 13.4 NOTICE

- 13.4.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS. FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD.
- 13.4.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED.
- 13.4.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 13.4.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 13.4.5 DON'T GIVE EXTERNAL SHOCK.
- 13.4.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.4.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
  WHEN THE LIQUID IS ATTACHED TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.4.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.4.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS AND SOLVENT.
- 13.4.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.4.11 REWIRING: NO MORE THAN 3 TIMES.