

EXAMINED BY :	EMERGING DISPLAY TECHNOLOGIES CORPORATION	FILE NO . CAS-0008804
Sam Chou		ISSUE : JUN.10, 2020
APPROVED BY:		TOTAL PAGE : 26
<i>Chris Wu</i>		VERSION : 4
CUSTOMER ACCEPTANCE SPECIFICATIONS		
MODEL NO. : ET043011DMA (RoHS) FOR MESSRS :		
CUSTOMER'S APPROVAL DATE : BY :		

RECORDS OF REVISION

DOC . FIRST ISSUE

APR.22, 2019

DATE

REVISED
PAGE
NO.

SUMMARY

SEP.09, 2019

2

3.1 LCD MODULE ELECTRICAL ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VCC-VSS	-0.3	4.6	V	—
LED BACKLIGHT FORWARD CURRENT	IF	—	25	mA	FOR EACH LED

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	4.6	V	—
POWER DISSIPATION FOR LED BACKLIGHT	PD	—	1224	mW	—
FORWARD CURRENT FOR LED BACKLIGHT	ILED	—	60	mA	—

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	REMARK	ITEM	REMARK
VIBRATION	5~20Hz, 1HR 20~500Hz(20Hz), 1HR 20~500Hz(500Hz), 1HR X,Y,Z,TOTAL 3HRS	VIBRATION	10~100 Hz XYZ DIRECTIONS 1 HR EACH

ADD NOTE (1)

3

4. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY	VCC-VSS	—	3.15	3.3	3.45	V	—
OPERATING CURRENT	ICC	VCC-VSS =3.3V	—	190	250	mA	NOTE (1)
INPUT LOW VOLTAGE	V _{IL}	—	VSS	—	0.3*VDD	V	—
INPUT HIGH VOLTAGE	V _{IH}	—	0.7*VDD	—	VDD	V	—
OUTPUT LOW VOLTAGE	V _{OL}	I _{OL} = +1.0mA	VSS	—	VSS+0.4	V	—
OUTPUT HIGH VOLTAGE	V _{OH}	I _{OH} = -1.0mA	VDD-0.4	—	VDD	V	—
POWER SUPPLY FOR LED BACKLIGHT	VLED	ILED=40mA	16.8	19.2	20.4	V	NOTE (2)
LED LIFE TIME	—	IF=20mA (PER LED)	20K	—	—	hrs	NOTE (3) NOTE (4)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	—	3.15	3.3	3.45	V	—
POWER SUPPLY CURRENT	IDD	VDD-VSS =3.3V	—	190	250	mA	NOTE (1)
LOGIC HIGH INPUT VOLTAGE	V _{IH}	—	0.7*VDD	—	VDD	V	NOTE (2)
LOGIC LOW INPUT VOLTAGE	V _{IL}	—	VSS	—	0.3*VDD	V	NOTE (2)
LOGIC HIGH OUTPUT VOLTAGE	V _{OH}	I _{OH} = -1.0mA	VDD-0.4	—	VDD	V	—
LOGIC LOW OUTPUT VOLTAGE	V _{OL}	I _{OL} = +1.0mA	VSS	—	VSS+0.4	V	—
POWER SUPPLY VOLTAGE FOR LED BACKLIGHT	VLED-VLSS	ILED=40mA	16.8	19.2	20.4	V	NOTE (3)
LED LIFE TIME	—	IF=20mA (PER LED)	30K	—	—	HRS	NOTE (4) NOTE (5)

ADD NOTE (2) & NOTE (6)

4,7

5.1.1 AC ELECTRICAL CHARACTERISTICS &
5.1.2 PARALLEL 24-BIT RGB MODE & 5.1.3 TIMING CHARACTERISTIC
PARAMETER→ITEM

10

6.1 OPTICAL CHARACTERISTICS

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
COLOR CHROMATICITY (CENTER)	WHITE	Wx	$\theta_x=0^\circ$, $\theta_y=0^\circ$ ILED = 40mA (NTSC : 50%)	0.29	0.34	0.39	—	NOTE (5)
		Wy		0.30	0.35	0.40		
	RED	Rx		0.56	0.61	0.66		
	GREEN	Gx		0.34	0.39	0.44		
	BLUE	Bx		0.10	0.15	0.20		
		Bv		0.05	0.10	0.15		

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
COLOR CHROMATICITY (CENTER)	WHITE	Wx	$\theta_x=0^\circ, \theta_y=0^\circ$ VDD-VSS=3.3V ILED = 40mA (NTSC : 50%)	0.25	0.30	0.35	—	NOTE (5)
		Wy		0.26	0.31	0.36		
	RED	Rx		0.54	0.59	0.64		
	GREEN	Gx		0.31	0.36	0.41		
	BLUE	Bx		0.14	0.19	0.24		
		Bv		0.04	0.09	0.14		

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TABLE OF CONTENTS

NO.	ITEM	PAGE
1.	GENERAL SPECIFICATIONS -----	1
2.	MECHANICAL SPECIFICATIONS -----	1
3.	ABSOLUTE MAXIMUM RATINGS -----	2
4.	ELECTRICAL CHARACTERISTICS -----	3
5.	TIMING CHARACTERISTICS -----	4 ~ 9
6.	OPTICAL CHARACTERISTICS -----	10 , 11
7.	OUTLINE DIMENSIONS -----	12
8.	BLOCK DIAGRAM -----	13
9.	DETAIL DRAWING OF DOT MATRIX -----	14
10.	INTERFACE SIGNALS -----	15 , 16
11.	POWER SUPPLY -----	16
12.	INSPECTION CRITERIA -----	17 ~ 23
13.	RELIABILITY TEST -----	24 , 25
14.	CAUTION -----	25 , 26

1. GENERAL SPECIFICATIONS

1.1 DATA SHEETS FOR CONTROLLER/DRIVER

PLEASE REFER TO :

SITRONIX ST5625CA
SITRONIX ST5091CA

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE), BIS(2-ETHYLHEXYL) PHTHALATE (DEHP), BUTYL BENZYL PHTHALATE (BBP), DIBUTYL PHTHALATE (DBP), DIISOBUTYL PHTHALATE (DIBP).

2. MECHANICAL SPECIFICATIONS

(1) DIAGONALS	4.3 inch
(2) NUMBER OF DOTS	800W * (RGB) * 480H DOTS
(3) MODULE SIZE	105.5W * 67.2H * 2.6D mm (NOT INCLUDED FPC)
(4) VIEWING AREA	96.9W * 55.5H mm
(5) ACTIVE AREA	95.04W * 53.856H mm
(6) DOT SIZE	0.0396W * 0.1122H mm
(7) PIXEL PITCH	0.1188W * 0.1122H mm
(8) LCD TYPE	TFT , TRANSMISSIVE , NORMALLY BLACK , ANTI-GLARE
(9) COLOR	16.2M
(10) VIEWING DIRECTION	SUPER WIDE VIEW
(11) BACK LIGHT	LED , COLOR : WHITE
(12) INTERFACE MODE	RGB(24 BIT) PARALLEL (DE MODE)

3. ABSOLUTE MAXIMUM RATINGS

3.1 LCD MODULE ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	4.6	V	—
POWER DISSIPATION FOR LED BACKLIGHT	PD	—	1224	mW	—
FORWARD CURRENT FOR LED BACKLIGHT	ILED	—	60	mA	—

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE (1) , (2)
HUMIDITY	NOTE (2)		NOTE (2)		WITHOUT CONDENSATION
VIBRATION	—	2.45 m/s ² (0.25 G)	—	11.76 m/s ² (1.2 G)	10~100 Hz XYZ DIRECTIONS 1 HR EACH
SHOCK	—	29.4 m/s ² (3 G)	—	490 m/s ² (50 G)	10 ms XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE (1) : Ta AT -30°C : WILL BE 48HRS MAX.

80°C : WILL BE 168HRS MAX.

NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3) : Ta ≤ 60°C : 90%RH MAX (96HRS MAX).

Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C (96HRS MAX).

4. ELECTRICAL CHARACTERISTICS

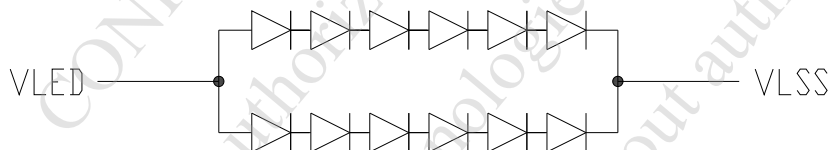
Ta = 25 °C

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	—	3.15	3.3	3.45	V	
POWER SUPPLY CURRENT	IDD	VDD-VSS =3.3V	—	190	250	mA	NOTE (1)
LOGIC HIGH INPUT VOLTAGE	VIH	—	0.7*VDD	—	VDD	V	NOTE (2)
LOGIC LOW INPUT VOLTAGE	VIL	—	VSS	—	0.3*VDD	V	NOTE (2)
LOGIC HIGH OUTPUT VOLTAGE	VOH	IOH= -1.0mA	VDD-0.4	—	VDD	V	
LOGIC LOW OUTPUT VOLTAGE	VOL	IOL= +1.0mA	VSS	—	VSS+0.4	V	
POWER SUPPLY VOLTAGE FOR LED BACKLIGHT	VLED-VLSS	ILED=40mA	16.8	19.2	20.4	V	NOTE (3)
LED LIFE TIME	—	IF=20mA (PER LED)	30K	—	—	HRS	NOTE (4) NOTE (5)

NOTE (1) : THE DISPLAY PATTERN IS ALL “WHITE”.

NOTE (2) : APPLIED TO TERMINALS, R0~R7, G0~G7, B0~B7, CLK, DISP, HSD, VSD, DE.

NOTE (3) : INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT

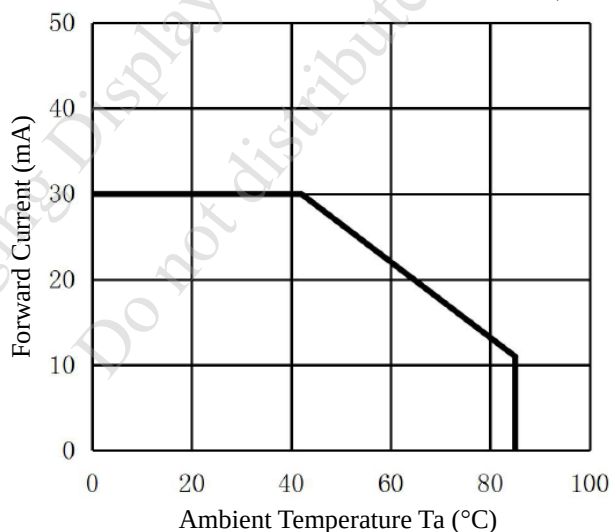


NOTE (4) : CONDITIONS; Ta=25 °C, CONTINUOUS LIGHTING

NOTE (5) : DEFINITIONS OF LIFE TIME :

LCM LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

NOTE (6) : AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)



5. TIMING CHARACTERISTICS

5.1 LCD MODULE AC ELECTRICAL CHARACTERISTICS

5.1.1 AC ELECTRICAL CHARACTERISTICS

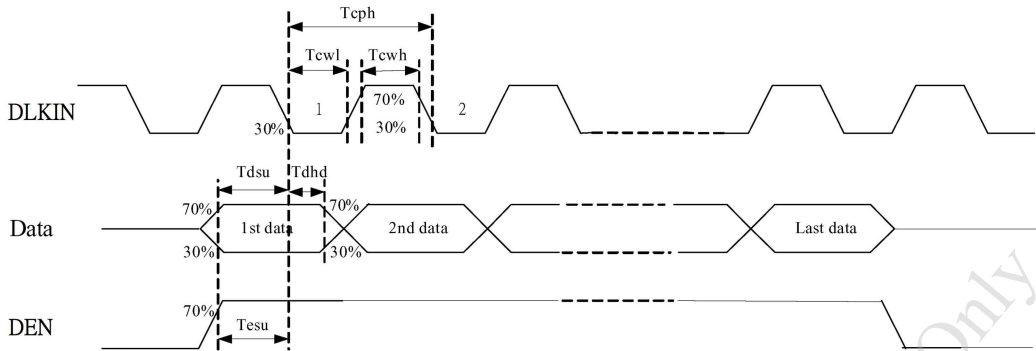
Ta = 25 °C

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
VDD POWER ON SLEW RATE	T _{POR}	—	—	20	ms	FROM 0V TO 90% VDD
RSTB PULSE WIDTH	T _{RST}	50	—	—	us	CLKIN=50MHz
CLKIN CYCLE TIME	T _{cph}	20	—	—	ns	
CLKIN PULSE DUTY	T _{cwh}	40	50	60	%	
VSD SETUP TIME	T _{vst}	8	—	—	ns	
VSD HOLD TIME	T _{vhd}	8	—	—	ns	
HSD SETUP TIME	T _{hst}	8	—	—	ns	
HSD HOLD TIME	T _{hhd}	8	—	—	ns	
DATA SETUP TIME	T _{dsu}	8	—	—	ns	D[7:0], D1[7:0], D2[7:0] TO CLKIN
DATE HOLD TIME	T _{dhd}	8	—	—	ns	D[7:0], D1[7:0], D2[7:0] TO CLKIN
DE SETUP TIME	T _{esu}	8	—	—	ns	
DE HOLD TIME	T _{ehd}	8	—	—	ns	
OUTPUT STABLE TIME	T _{sst}	—	—	6	us	10% TO 90% TARGET VOLTAGE. CL=120pF, R=10Kohm

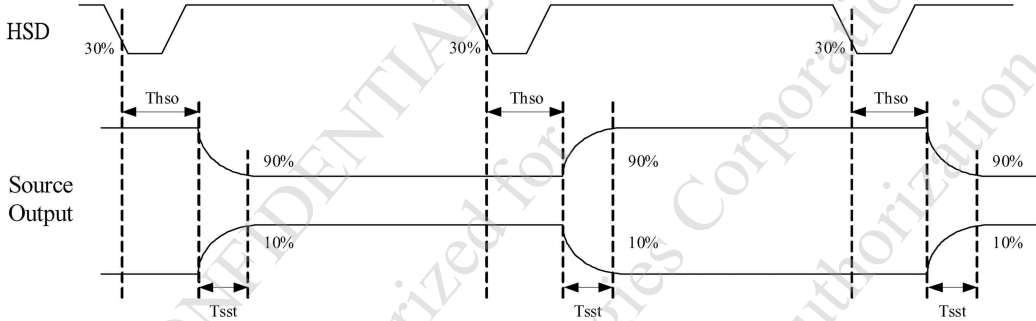
5.1.2 PARALLEL 24-BIT RGB MODE

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
DCLK FREQUENCY	F _{clk}	—	40	50	MHz	
DCLK CYCLE TIME	T _{clk}	20	25	—	ns	
DCLK PULSE DUTY	T _{cwh}	40	50	60	%	T _{clk}
TIME FROM HSYNC TO SOURCE OUTPUT	T _{hso}	—	64	—	DCLK	
TIME FROM HSD TO SOURCE OUTPUT	T _{hso}	—	64	—	CLKIN	
TIME FROM HSD TO LD	T _{hld}	—	64	—	CLKIN	
TIME FROM HSD TO STV	T _{hstv}	—	2	—	CLKIN	
TIME FROM HSD TO CKV	T _{hckv}	—	20	—	CLKIN	
TIME FROM HSD TO OEV	T _{hoev}	—	4	—	CLKIN	
LD PULSE WIDTH	T _{wld}	—	10	—	CLKIN	
CKV PULSE WIDTH	T _{wckv}	—	66	—	CLKIN	
OEV PULSE WIDTH	T _{woev}	—	74	—	CLKIN	

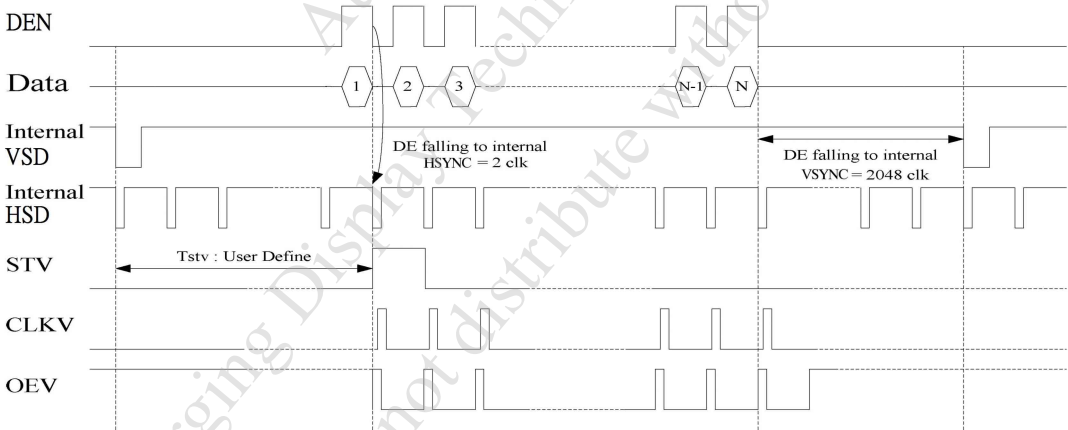
INPUT CLOCK AND DATA TIMING DIAGRAM



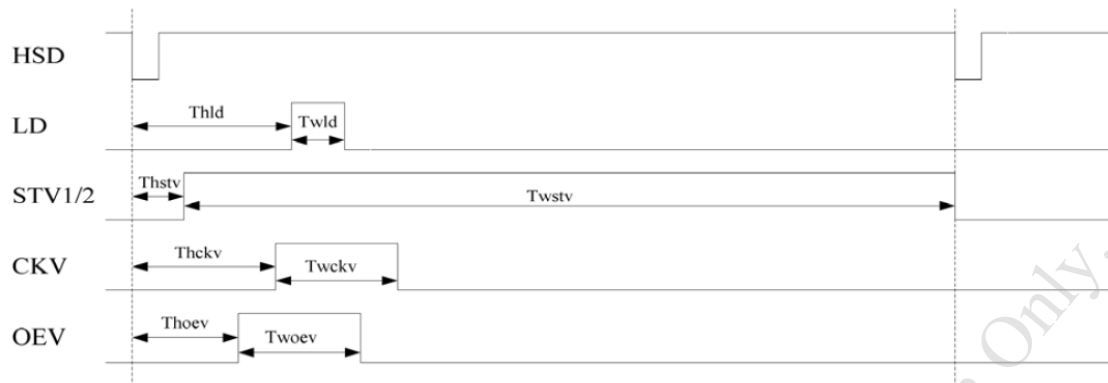
SOURCE OUTPUT TIMING DIAGRAM



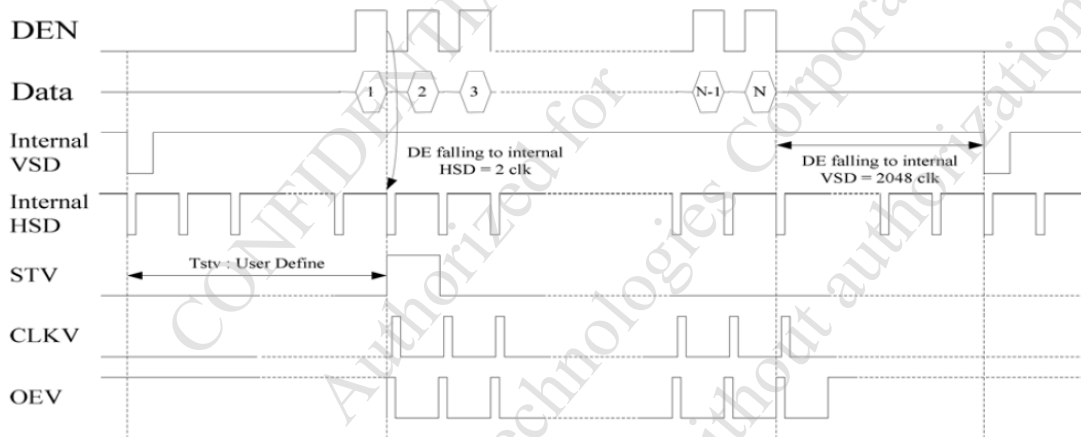
VERTICAL TIMING DIAGRAM DE (TCON + SOURCE MODE)



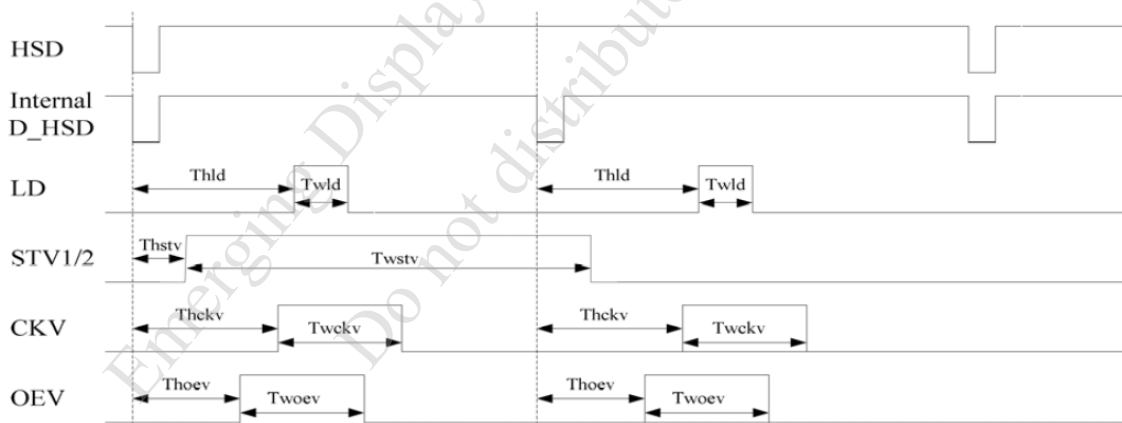
GATE OUTPUT TIMING DIAGRAM (TCON + SOURCE MODE)



VERTICAL TIMING DIAGRAM DE (DUAL GATE MODE)



GATE OUTPUT TIMING DIAGRAM (DUAL GATE MODE)



5.1.3 TIMING CHARACTERISTIC

HORIZONTAL INPUT TIMING

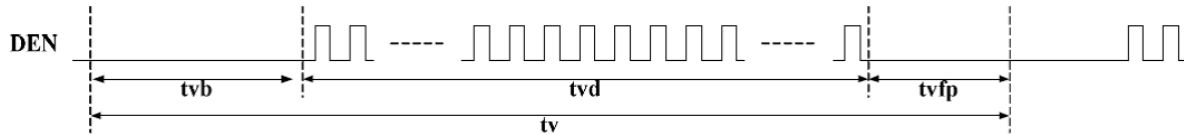
ITEM	SYMBOL	VALUE			UNIT
		MIN.	TYP.	MAX.	
HORIZONTAL DISPLAY AREA	thd	800			DCLK
DCLK FREQUENCY	fclk	—	30	50	MHZ
ONE HORIZONTAL LINE	th	889	928	1143	DCLK
HS PULSE WIDTH	thpw	1	48	255	DCLK
HS BACK PORCH (BLANKING)	thb	88			DCLK
HS FRONT PORCH	thfb	1	40	255	DCLK
DE MODE BLANKING	th-thd	85	128	512	DCLK

VERTICAL INPUT TIMING

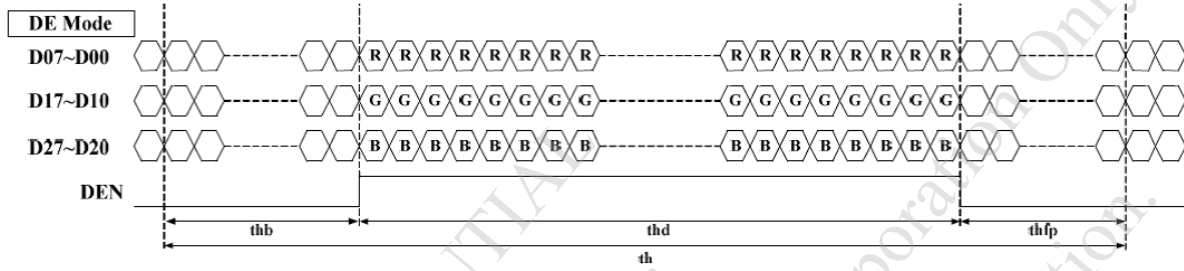
ITEM	SYMBOL	VALUE			UNIT
		MIN.	TYP.	MAX.	
VERTICAL DISPLAY AREA	tvd	480			H
VS PERIOD TIME	tv	513	525	767	MHZ
VS PULSE WIDTH	tvpw	3	3	255	DCLK
VS BACK PORCH (BLANKING)	tvb	32			DCLK
VS FRONT PORCH	tvfb	1	13	255	DCLK
DE MODE BLANKING	tv-tvd	4	45	255	DCLK

5.2 DATA INPUT FORMAT

VERTICAL INPUT TIMING



HORIZONTAL INPUT TIMING

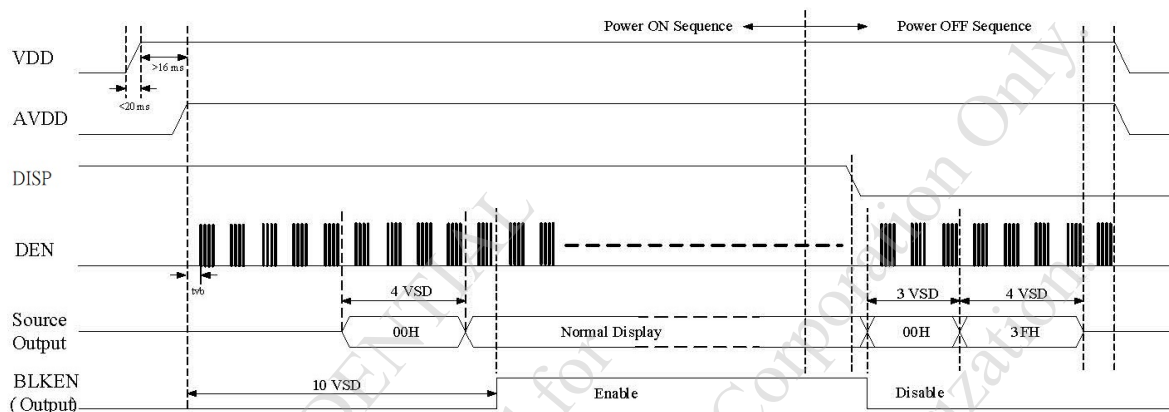


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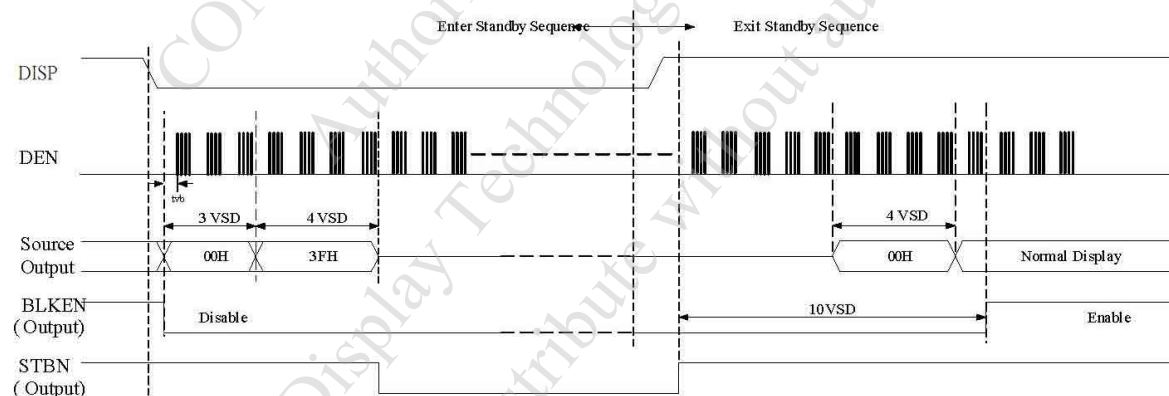
5.3 POWER ON/OFF SEQUENCE

IN ORDER TO PREVENT IC FROM POWER ON RESET FAIL, THE RISING TIME (TPOR) OF THE DIGITAL POWER SUPPLY VDD SHOULD BE MAINTAINED WITHIN THE GIVEN SPECIFICATIONS. REFER TO “AC CHARACTERISTICS” FOR MORE DETAIL ON TIMING.

POWER-ON/OFF TIMING SEQUENCE



STANDBY MODE SEQUENCE



6. OPTICAL CHARACTERISTICS (NOTE 1)

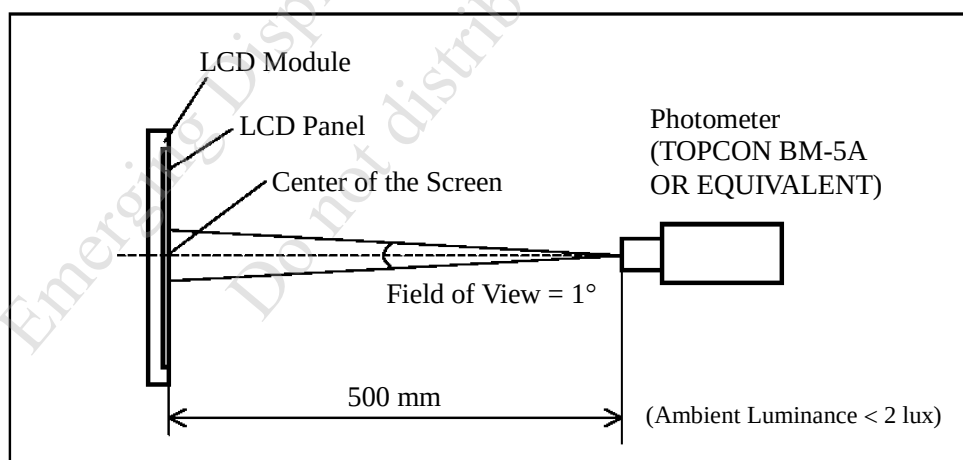
6.1 OPTICAL CHARACTERISTICS

Ta = 25°C

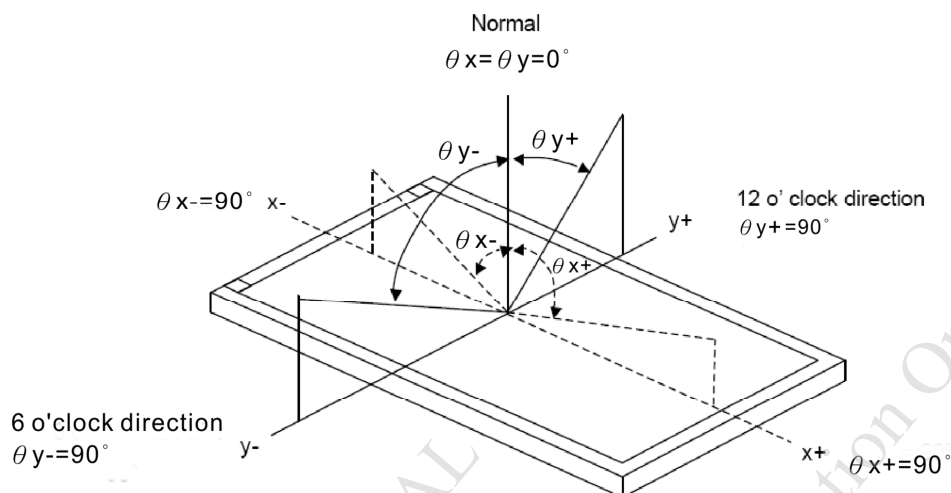
ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT	REMARK
VIEWING ANGLE		θ_{y+}	CR ≥ 10	$\theta_x=0^\circ$	70	80	—	deg.	NOTE (2) NOTE (3)
		θ_{y-}			70	80	—		
		θ_{x+}		$\theta_y=0^\circ$	70	80	—		
		θ_{x-}			70	80	—		
CONTRAST RATIO (CENTER)		CR	$\theta_x=0^\circ$, $\theta_y=0^\circ$		640	800	—	—	NOTE (3)
RESPONSE TIME		tr (rise)+ tf (fall)	$\theta_x=0^\circ$, $\theta_y=0^\circ$		—	30	40	msec	NOTE (4)
COLOR CHROMATICITY (CENTER)	WHITE	Wx	$\theta_x=0^\circ$, $\theta_y=0^\circ$ VDD-VSS=3.3V ILED = 40mA (NTSC : 50%)	0.25	0.30	0.35	—	NOTE (5)	
		Wy		0.26	0.31	0.36			
	RED	Rx		0.54	0.59	0.64	—		
		Ry		0.30	0.35	0.40			
	GREEN	Gx		0.31	0.36	0.41	—		
		Gy		0.52	0.57	0.62			
	BLUE	Bx		0.14	0.19	0.24	—		
		By		0.04	0.09	0.14			
THE BRIGHTNESS OF MODULE (CENTER)		B		450	500	—	cd/m ²	NOTE (6)	
THE BRIGHTNESS OF UNIFORMITY		—		70	—	—	%	NOTE (7)	

NOTE (1) : TEST CONDITION :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.



NOTE (2) : DEFINITION OF VIEWING ANGLE :



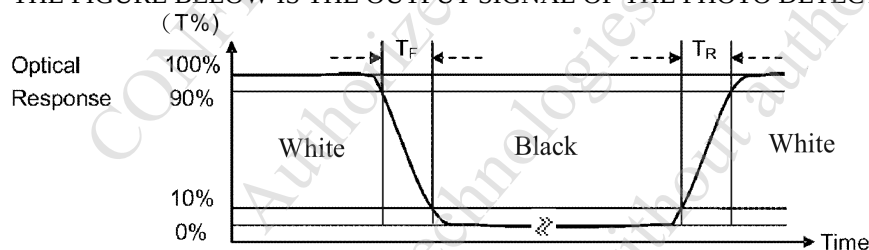
NOTE (3) : DEFINITION OF CONTRAST RATIO (CR) :

MEASURED AT THE CENTER POINT OF MODULE

$$\text{CONTRAST RATIO (CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

NOTE (4) : DEFINITION OF RESPONSE TIME : T_R AND T_F

THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



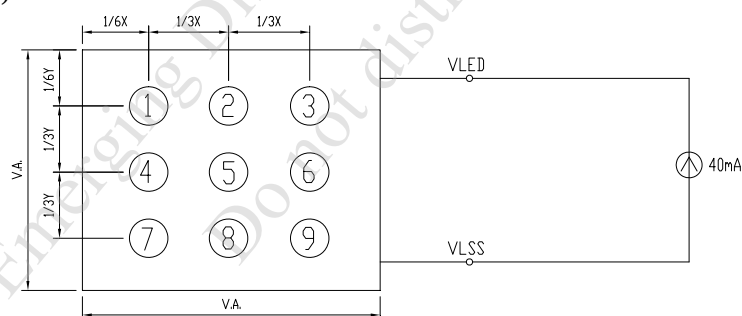
NOTE (5) : DEFINITION OF COLOR CHROMATICITY

(a) 100% RGB PIXEL DATA TRANSMISSION WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY POWERED ON.

(b) MEASURED AT THE CENTER POINT OF MODULE

NOTE (6) : MEASURED THE BRIGHTNESS OF WHITE STATE AT CENTER POINT.

NOTE (7) : (a) DEFINITION OF BRIGHTNESS UNIFORMITY

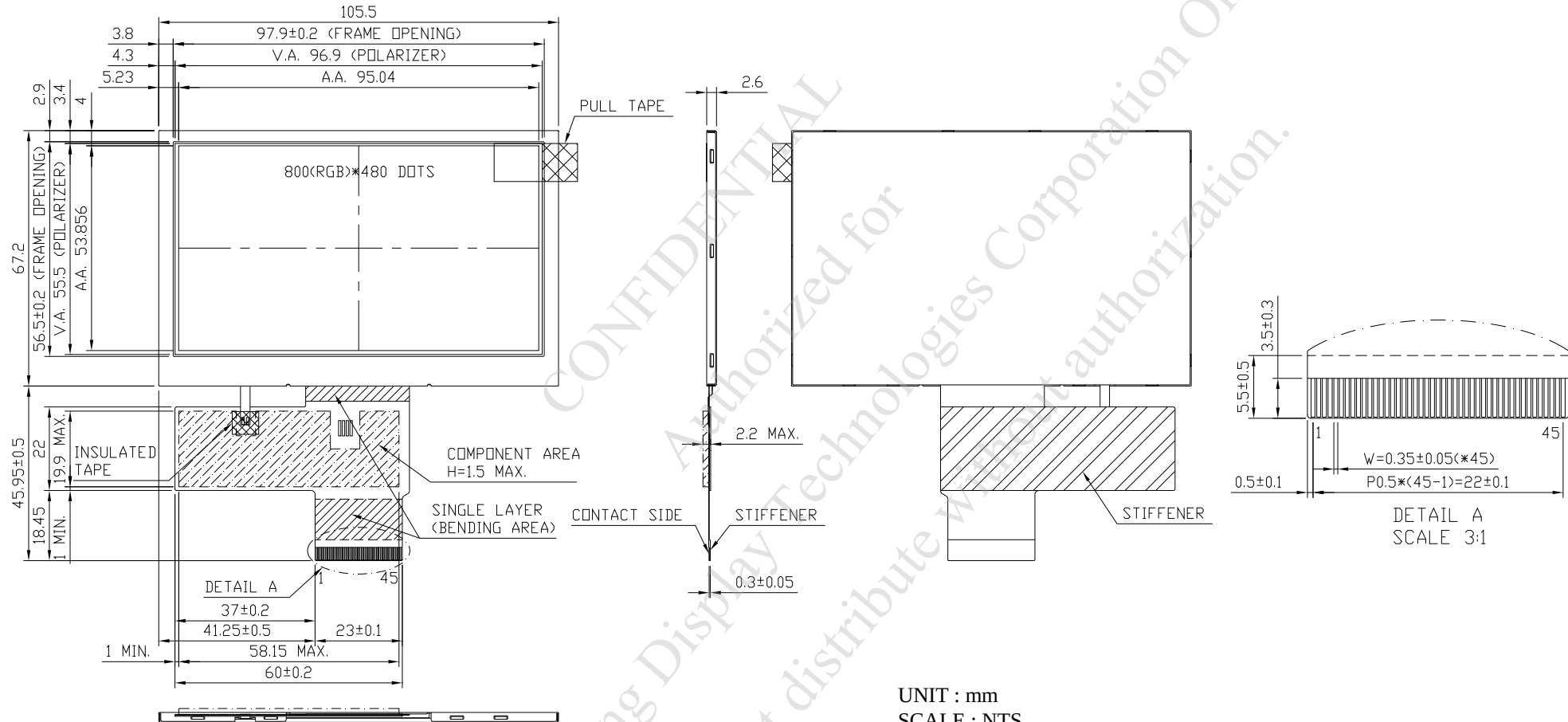


UNIT : mm

(b) THE BRIGHTNESS UNIFORMITY CALCULATING METHOD

$$\text{UNIFORMITY} : \frac{\text{MINIMUM BRIGHTNESS}}{\text{MAXIMUM BRIGHTNESS}} * 100\%$$

7. OUTLINE DIMENSIONS



UNIT : mm

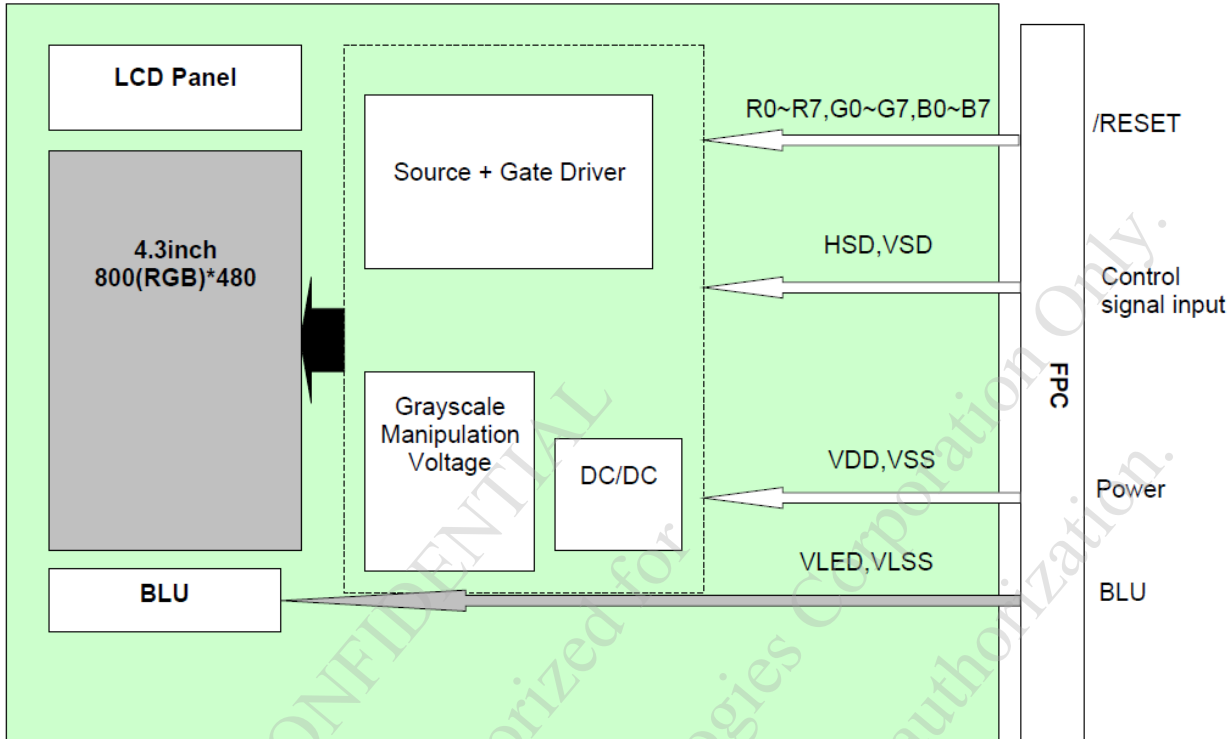
SCALE : NTS

NOT SPECIFIED TOLERANCE IS ±0.3mm

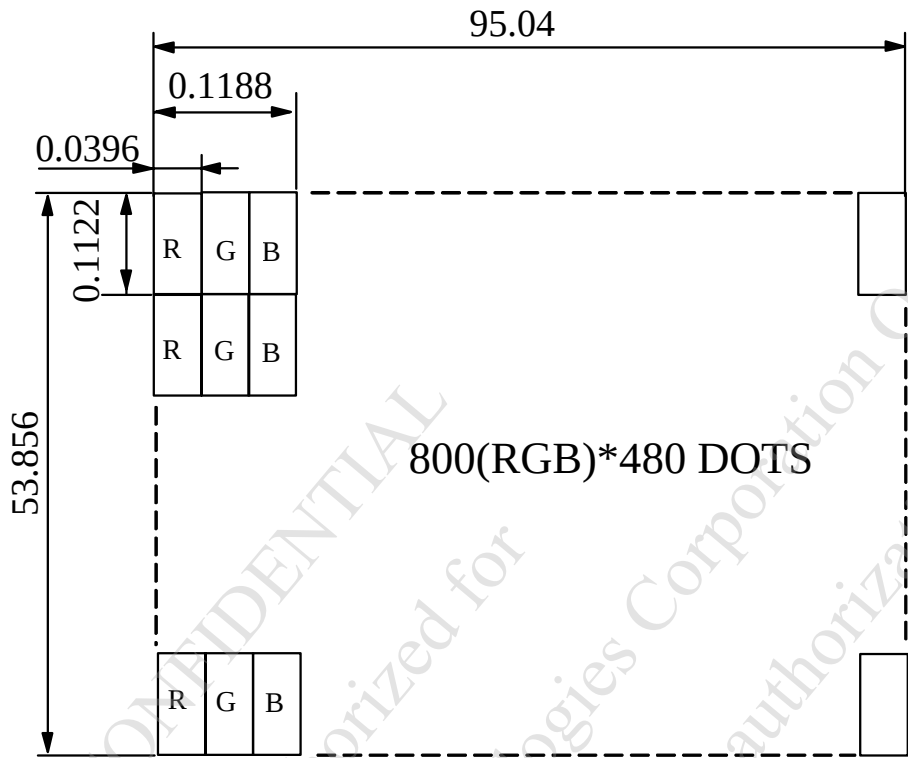
NOTE :

RECOMMEND MATCH CONNECTOR KYOCERA : 04 6240 045 SERIES

8. BLOCK DIAGRAM



9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm
SCALE : NTS
NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

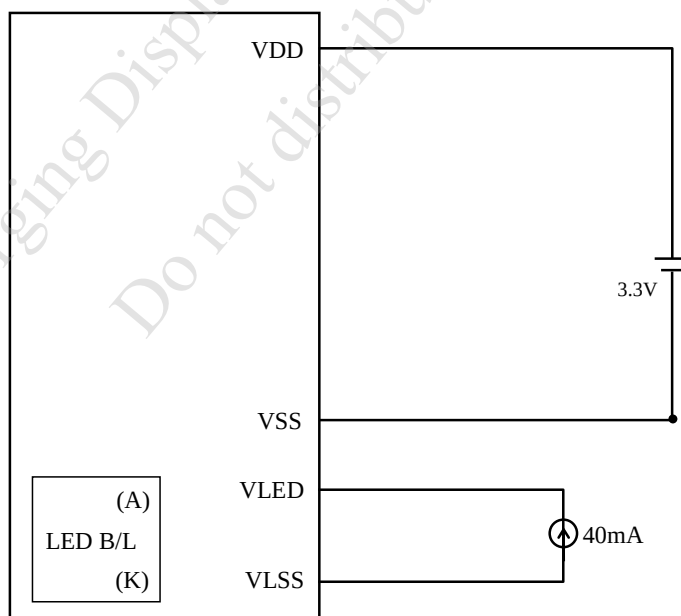
10. INTERFACE SIGNALS

PIN NO.	SYMBOL	FUNCTION
1	VSS	GROUND
2	VSS	GROUND
3	VDD	+3.3V POWER SOURCE
4	VDD	+3.3V POWER SOURCE
5	R0	RED DATA SIGNAL (LSB)
6	R1	RED DATA SIGNAL
7	R2	RED DATA SIGNAL
8	R3	RED DATA SIGNAL
9	R4	RED DATA SIGNAL
10	R5	RED DATA SIGNAL
11	R6	RED DATA SIGNAL
12	R7	RED DATA SIGNAL (MSB)
13	G0	GREEN DATA SIGNAL (LSB)
14	G1	GREEN DATA SIGNAL
15	G2	GREEN DATA SIGNAL
16	G3	GREEN DATA SIGNAL
17	G4	GREEN DATA SIGNAL
18	G5	GREEN DATA SIGNAL
19	G6	GREEN DATA SIGNAL
20	G7	GREEN DATA SIGNAL (MSB)
21	B0	BLUE DATA SIGNAL (LSB)
22	B1	BLUE DATA SIGNAL
23	B2	BLUE DATA SIGNAL
24	B3	BLUE DATA SIGNAL
25	B4	BLUE DATA SIGNAL
26	B5	BLUE DATA SIGNAL
27	B6	BLUE DATA SIGNAL
28	B7	BLUE DATA SIGNAL (MSB)
29	VSS	GROUND
30	CLK	CLOCK SIGNAL; LATCHING DATA AT THE FALLING EDGE
31	DISP	DISPLAY CONTROL / STANDBY MODE SELECTION. DISP = "LOW" : STANDBY; DISP = "HIGH" : NORMAL DISPLAY(DEFAULT)
32	HSD	HORIZONTAL SYNC SIGNAL; NEGATIVE POLARITY

PIN NO.	SYMBOL	FUNCTION																								
33	VSD	VERTICAL SYNC SIGNAL; NEGATIVE POLARITY																								
34	DE	DATA INPUT ENABLE. ACTIVE HIGH TO ENABLE THE DATA INPUT.																								
35	CSX	COLOR ENHANCE FUNCTION CONTROL:																								
		<table><tr><td>CSX</td><td>DBCM[1]</td><td>DBCM[0]</td><td>COLOR FUNCTION</td></tr><tr><td>X</td><td>X</td><td>X</td><td>CE DISABLE</td></tr><tr><td>1</td><td>0</td><td>0</td><td>CE SETTING 0 (STRONG)</td></tr><tr><td>1</td><td>0</td><td>1</td><td>CE SETTING 1 (MIDDLE)</td></tr><tr><td>1</td><td>1</td><td>0</td><td>CE SETTING 2 (WEAKEST)</td></tr><tr><td>1</td><td>1</td><td>1</td><td>CE SETTING 3 (WEAK) [DEFAULT]</td></tr></table>	CSX	DBCM[1]	DBCM[0]	COLOR FUNCTION	X	X	X	CE DISABLE	1	0	0	CE SETTING 0 (STRONG)	1	0	1	CE SETTING 1 (MIDDLE)	1	1	0	CE SETTING 2 (WEAKEST)	1	1	1	CE SETTING 3 (WEAK) [DEFAULT]
		CSX	DBCM[1]	DBCM[0]	COLOR FUNCTION																					
		X	X	X	CE DISABLE																					
		1	0	0	CE SETTING 0 (STRONG)																					
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		1	1	0	CE SETTING 2 (WEAKEST)																					
1	1	1	CE SETTING 3 (WEAK) [DEFAULT]																							
36	NC	NC																								
37	NC	NC (RESERVED FOR TOUCH PANEL, YU)																								
38	NC	NC (RESERVED FOR TOUCH PANEL, XL)																								
39	NC	NC (RESERVED FOR TOUCH PANEL, YD)																								
40	NC	NC (RESERVED FOR TOUCH PANEL, XR)																								
41	VSS	GROUND																								
42	VLSS	LED POWER SOURCE INPUT TERMINAL (CATHODE SIDE)																								
43	VLED	LED POWER SOURCE INPUT TERMINAL (ANODE SIDE)																								
44	DBCM(0)	COLOR ENHANCE FUNCTION CONTROL:																								
		<table><tr><td>CSX</td><td>DBCM[1]</td><td>DBCM[0]</td><td>COLOR FUNCTION</td></tr><tr><td>X</td><td>X</td><td>X</td><td>CE DISABLE</td></tr><tr><td>1</td><td>0</td><td>0</td><td>CE SETTING 0 (STRONG)</td></tr><tr><td>1</td><td>0</td><td>1</td><td>CE SETTING 1 (MIDDLE)</td></tr><tr><td>1</td><td>1</td><td>0</td><td>CE SETTING 2 (WEAKEST)</td></tr><tr><td>1</td><td>1</td><td>1</td><td>CE SETTING 3 (WEAK) [DEFAULT]</td></tr></table>	CSX	DBCM[1]	DBCM[0]	COLOR FUNCTION	X	X	X	CE DISABLE	1	0	0	CE SETTING 0 (STRONG)	1	0	1	CE SETTING 1 (MIDDLE)	1	1	0	CE SETTING 2 (WEAKEST)	1	1	1	CE SETTING 3 (WEAK) [DEFAULT]
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45	DBCM(1)	COLOR ENHANCE FUNCTION CONTROL:																								
		<table><tr><td>CSX</td><td>DBCM[1]</td><td>DBCM[0]</td><td>COLOR FUNCTION</td></tr><tr><td>X</td><td>X</td><td>X</td><td>CE DISABLE</td></tr><tr><td>1</td><td>0</td><td>0</td><td>CE SETTING 0 (STRONG)</td></tr><tr><td>1</td><td>0</td><td>1</td><td>CE SETTING 1 (MIDDLE)</td></tr><tr><td>1</td><td>1</td><td>0</td><td>CE SETTING 2 (WEAKEST)</td></tr><tr><td>1</td><td>1</td><td>1</td><td>CE SETTING 3 (WEAK) [DEFAULT]</td></tr></table>	CSX	DBCM[1]	DBCM[0]	COLOR FUNCTION	X	X	X	CE DISABLE	1	0	0	CE SETTING 0 (STRONG)	1	0	1	CE SETTING 1 (MIDDLE)	1	1	0	CE SETTING 2 (WEAKEST)	1	1	1	CE SETTING 3 (WEAK) [DEFAULT]
		CSX	DBCM[1]	DBCM[0]	COLOR FUNCTION																					
		X	X	X	CE DISABLE																					
		1	0	0	CE SETTING 0 (STRONG)																					
		1	0	1	CE SETTING 1 (MIDDLE)																					
		1	1	0	CE SETTING 2 (WEAKEST)																					
1	1	1	CE SETTING 3 (WEAK) [DEFAULT]																							

11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



12. INSPECTION CRITERIA

12.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

12.2 INSPECTION CONDITIONS

12.2.1 (1)OBSERVATION DISTANCE : $45\pm 5\text{cm}$

(2)VIEWING ANGLE : $\pm 45^\circ$

$\pm 45^\circ$ (FOR SECTION WITHIN VIEWING AREA), REFER TO FIG.A
 90° (FOR SECTION OUTSIDE OF VIEWING AREA), REF TO FIG.B
 PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 45°

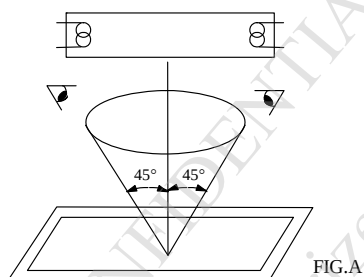


FIG.A

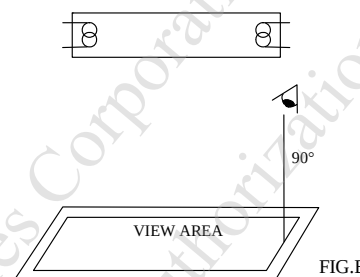


FIG.B

THE INSPECTION CRITERIA IS ACCORDING TO LINE OF SIGHT. INSPECTION SHALL BE MADE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT OF 45° WITH RESPECT TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE FLUORESCENT LAMP AND THE CONE AXIS MUST BE PERPENDICULAR TO THE LCD SURFACE.

IF THE DEFECTS ARE OUTSIDE OF VIEWING AREA, IT SHALL BE INSPECTED BY 90° WITH RESPECT TO THE VERTICAL AXIS FROM EDGE OF VIEWING AREA.

12.2.2 ENVIRONMENT CONDITIONS :

AMBIENT TEMPERATURE		$25\pm 5^\circ\text{C}$
AMBIENT HUMIDITY		$65 \pm 20\%\text{RH}$
AMBIENT ILLUMINATION	COSMETIC INSPECTION	600~800 lux
	FUNCTIONAL INSPECTION	300~500 lux
INSPECTION TIME		10 secs

12.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

12.2.4 A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD :

ANSI/ ASQ Z1.4 NORMAL INSPECTION LEVEL II

(b)AQL : MAJOR DEFECT : AQL 0.65

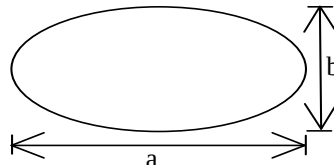
MINOR DEFECT : AQL 1.0

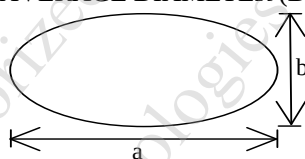
12.3 INSPECTION STANDARDS

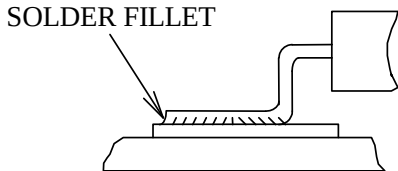
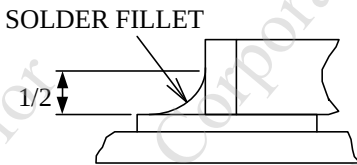
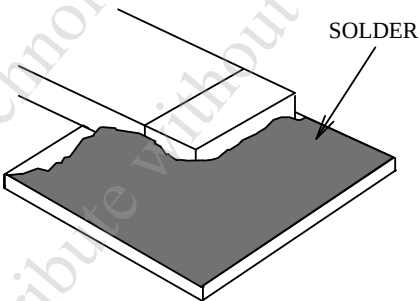
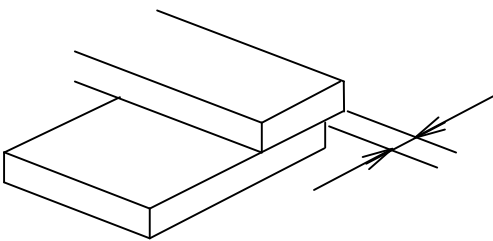
12.3.1 VISUAL DEFECTS CLASSIFICATION

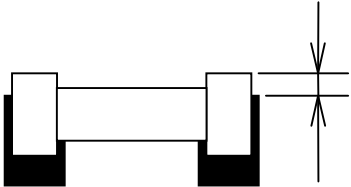
TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS - EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
	2.BACKLIGHT	• NO LIGHT • FLICKERING AND OTHER ABNORMAL ILLUMINATION	
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
MINOR DEFECT	1.DISPLAY ZONE	• BLACK/WHITE SPOT • BUBBLES ON POLARIZER • NEWTON RING • BLACK/WHITE LINE • SCRATCH • CONTAMINATION • LEVER COLOR SPREAD	1.0
	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	
	3.SOLDERING	• INSUFFICIENT SOLDER • SOLDERED IN INCORRECT POSITION • CONVEX SOLDERING SPOT • SOLDER BALLS • SOLDER SCRAPS	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

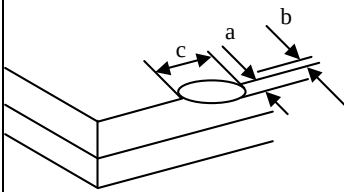
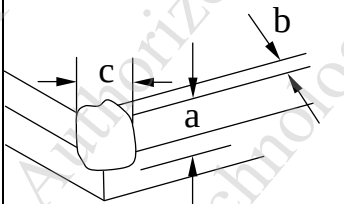
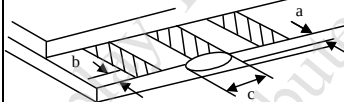
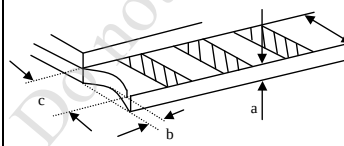
12.3.2 MODULE DEFECTS CLASSIFICATION

NO.	ITEM	CRITERIA												
1	DISPLAY ON INSPECTION	1. INCORRECT PATTERN 2. MISSING SEGMENT 3. DIM SEGMENT 4. OPERATING VOLTAGE BEYOND SPEC												
2	OVERALL DIMENSIONS	1. OVERALL DIMENSION BEYOND SPEC												
3	DOT DEFECT	1. INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS. 2. <table><tr><th>ITEMS</th><th>ACCEPTABLE COUNT</th></tr><tr><td>BRIGHT DOT</td><td>$N \leq 2$</td></tr><tr><td>DARK DOT</td><td>$N \leq 3$</td></tr><tr><td>TOTAL BRIGHT AND DARK DOTS</td><td>$N \leq 4$</td></tr></table> NOTE : 1. THE DEFINITION OF DOT : THE SIZE OF A DEFECTIVE DOT OVER 1/2 OF WHOLE DOT IS REGARDED AS ONE DEFECTIVE DOT. THE BRIGHT DOT DEFECT MUST BE VISIBLE THROUGH A 5% ND FILTER 2. BRIGHT DOT : DOTS APPEAR BRIGHT AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER BLACK PATTERN. 3. DARK DOT : DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURE.	ITEMS	ACCEPTABLE COUNT	BRIGHT DOT	$N \leq 2$	DARK DOT	$N \leq 3$	TOTAL BRIGHT AND DARK DOTS	$N \leq 4$				
ITEMS	ACCEPTABLE COUNT													
BRIGHT DOT	$N \leq 2$													
DARK DOT	$N \leq 3$													
TOTAL BRIGHT AND DARK DOTS	$N \leq 4$													
4	FOREIGN BLACK/WHITE/ BRIGHT LINE	<table><tr><th>LENGTH : L</th><th>WIDTH : W</th><th>PERMISSIBLE NO.</th></tr><tr><td>$L \leq 0.3$</td><td>$W \leq 0.05$</td><td>IGNORE</td></tr><tr><td>$0.3 < L \leq 3.0$</td><td>$0.05 < W \leq 0.1$</td><td>4</td></tr><tr><td>$3.0 < L$</td><td>$0.1 < W$</td><td>NONE</td></tr></table> WIDTH : W mm, LENGTH : L mm	LENGTH : L	WIDTH : W	PERMISSIBLE NO.	$L \leq 0.3$	$W \leq 0.05$	IGNORE	$0.3 < L \leq 3.0$	$0.05 < W \leq 0.1$	4	$3.0 < L$	$0.1 < W$	NONE
LENGTH : L	WIDTH : W	PERMISSIBLE NO.												
$L \leq 0.3$	$W \leq 0.05$	IGNORE												
$0.3 < L \leq 3.0$	$0.05 < W \leq 0.1$	4												
$3.0 < L$	$0.1 < W$	NONE												
5	POLARIZER SCRATCHES	<table><tr><th>LENGTH : L</th><th>WIDTH : W</th><th>PERMISSIBLE NO.</th></tr><tr><td>$L \leq 0.3$</td><td>$W \leq 0.05$</td><td>IGNORE</td></tr><tr><td>$0.3 < L \leq 3.0$</td><td>$0.05 < W \leq 0.1$</td><td>4</td></tr><tr><td>$3.0 < L$</td><td>$0.1 < W$</td><td>NONE</td></tr></table> WIDTH : W mm, LENGTH : L mm	LENGTH : L	WIDTH : W	PERMISSIBLE NO.	$L \leq 0.3$	$W \leq 0.05$	IGNORE	$0.3 < L \leq 3.0$	$0.05 < W \leq 0.1$	4	$3.0 < L$	$0.1 < W$	NONE
LENGTH : L	WIDTH : W	PERMISSIBLE NO.												
$L \leq 0.3$	$W \leq 0.05$	IGNORE												
$0.3 < L \leq 3.0$	$0.05 < W \leq 0.1$	4												
$3.0 < L$	$0.1 < W$	NONE												
6	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	<table><tr><th>AVERAGE DIAMETER (mm): D</th><th>NUMBER OF PIECES PERMITTED</th></tr><tr><td>$D \leq 0.15$</td><td>IGNORE</td></tr><tr><td>$0.15 < D \leq 0.5$</td><td>4</td></tr><tr><td>$0.5 < D$</td><td>NONE</td></tr></table> NOTE : DIAMETER $D=(a+b)/2$ 	AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	$D \leq 0.15$	IGNORE	$0.15 < D \leq 0.5$	4	$0.5 < D$	NONE				
AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED													
$D \leq 0.15$	IGNORE													
$0.15 < D \leq 0.5$	4													
$0.5 < D$	NONE													

NO.	ITEM	CRITERIA																								
7	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS	<table><tr><td></td><td>AVERAGE DIAMETER (mm) : D</td><td>NUMBER OF PIECES PERMITTED</td></tr><tr><td rowspan="3">BUBBLE ON THE POLARIZER</td><td>$D \leq 0.25$</td><td>IGNORE</td></tr><tr><td>$0.25 < D \leq 0.5$</td><td>$N \leq 5$</td></tr><tr><td>$0.5 < D$</td><td>NONE</td></tr><tr><td rowspan="3">SURFACE STAINS / DIRT / DENT</td><td>$D < 0.25$</td><td>IGNORE</td></tr><tr><td>$0.25 < D \leq 0.4$</td><td>$N \leq 3$</td></tr><tr><td>$D > 0.4$</td><td>NONE</td></tr><tr><td rowspan="3">CF FAIL / SPOT</td><td>$D < 0.1$</td><td>IGNORE</td></tr><tr><td>$0.1 < D \leq 0.3$</td><td>$N \leq 3$</td></tr><tr><td>$D > 0.3$</td><td>NONE</td></tr></table>		AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED	BUBBLE ON THE POLARIZER	$D \leq 0.25$	IGNORE	$0.25 < D \leq 0.5$	$N \leq 5$	$0.5 < D$	NONE	SURFACE STAINS / DIRT / DENT	$D < 0.25$	IGNORE	$0.25 < D \leq 0.4$	$N \leq 3$	$D > 0.4$	NONE	CF FAIL / SPOT	$D < 0.1$	IGNORE	$0.1 < D \leq 0.3$	$N \leq 3$	$D > 0.3$	NONE
			AVERAGE DIAMETER (mm) : D	NUMBER OF PIECES PERMITTED																						
		BUBBLE ON THE POLARIZER	$D \leq 0.25$	IGNORE																						
			$0.25 < D \leq 0.5$	$N \leq 5$																						
			$0.5 < D$	NONE																						
		SURFACE STAINS / DIRT / DENT	$D < 0.25$	IGNORE																						
			$0.25 < D \leq 0.4$	$N \leq 3$																						
			$D > 0.4$	NONE																						
		CF FAIL / SPOT	$D < 0.1$	IGNORE																						
			$0.1 < D \leq 0.3$	$N \leq 3$																						
			$D > 0.3$	NONE																						
		NOTE : (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEIOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2																								
																										
8	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED																								
9	MURA ON DISPLAY	IT'S OK IF MURA IS SLIGHT VISIBLE THROUGH 5% ND FILTER																								
10	UNEVEN COLOR SPREAD, COLORATION	1. TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.																								
11	BEZEL APPEARANCE	1. BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. 2. BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.																								
12	PCB	1. THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. 2. NO OXIDATION OR CONTAMINATION PCB TERMINALS. 3. PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. 4. THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. 5. IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD: MAKE SURE IT IS SMOOTHED DOWN.																								

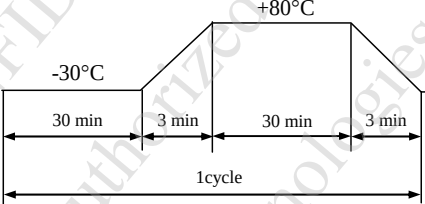
NO.	ITEM	CRITERIA
13	SOLDERING	1. NO SOLDERING FOUND ON THE SPECIFIED PLACE 2. INSUFFICIENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD  (b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING  · SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED  3. PARTS ALIGNMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE 

NO.	ITEM	CRITERIA
13	SOLDERING	(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE  4. NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. 5. NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. 6. NO RESIDUE OR SOLDER BALLS ON PCB. 7. NO SHORT CIRCUITS IN COMPONENTS ON PCB.
14	BACKLIGHT	1. NO LIGHT 2. FLICKERING AND OTHER ABNORMAL ILLUMINATION 3. SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. 4. BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
15	GENERAL APPEARANCE	1. NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. 2. NO CRACKS ON INTERFACE PIN (OLB) OF TCP. 3. NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. 4. THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. 5. THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. 6. THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. 7. SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. 8. PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. 9. LCD PIN LOOSE OR MISSING PINS. 10. PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. 11. PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. 12. THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

NO.	ITEM	CRITERIA									
16	CRACKED GLASS	THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE									
		GENERAL GLASS CHIP :  <table><tr><th>a</th><th>b</th><th>c</th></tr><tr><td>$\leq t/2$</td><td>< VIEWING AREA</td><td>$\leq 1/8X$</td></tr><tr><td>$t/2 >, \leq 2t$</td><td>$\leq W/2$</td><td>$\leq 1/8X$</td></tr></table> *W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS	a	b	c	$\leq t/2$	< VIEWING AREA	$\leq 1/8X$	$t/2 >, \leq 2t$	$\leq W/2$	$\leq 1/8X$
		a	b	c							
		$\leq t/2$	< VIEWING AREA	$\leq 1/8X$							
$t/2 >, \leq 2t$	$\leq W/2$	$\leq 1/8X$									
CORNER PART :  <table><tr><th>a</th><th>b</th><th>c</th></tr><tr><td>$\leq t/2$</td><td>< VIEWING AREA</td><td>$\leq 1/8X$</td></tr><tr><td>$> t/2, \leq 2t$</td><td>$\leq W/2$</td><td>$\leq 1/8X$</td></tr></table> *W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS	a	b	c	$\leq t/2$	< VIEWING AREA	$\leq 1/8X$	$> t/2, \leq 2t$	$\leq W/2$	$\leq 1/8X$		
a	b	c									
$\leq t/2$	< VIEWING AREA	$\leq 1/8X$									
$> t/2, \leq 2t$	$\leq W/2$	$\leq 1/8X$									
CHIP ON ELECTRODE PAD  <table><tr><th>a</th><th>b</th><th>c</th></tr><tr><td>$\leq t$</td><td>$\leq 0.5\text{mm}$</td><td>$\leq 1/8X$</td></tr></table> * X=LCD SIDE WIDTH t=GLASS THICKNESS	a	b	c	$\leq t$	$\leq 0.5\text{mm}$	$\leq 1/8X$					
a	b	c									
$\leq t$	$\leq 0.5\text{mm}$	$\leq 1/8X$									
 <table><tr><th>a</th><th>b</th><th>c</th></tr><tr><td>$\leq t$</td><td>$\leq 1/8X$</td><td>$\leq L$</td></tr></table> *X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH ①IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS ②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DAMAGED	a	b	c	$\leq t$	$\leq 1/8X$	$\leq L$					
a	b	c									
$\leq t$	$\leq 1/8X$	$\leq L$									

13. RELIABILITY TEST

13.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMPERATURE /HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: 
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE $\pm 12KV$ CONTACT DISCHARGE $\pm 8KV$ ACCORDING TO IEC-61000-4-2

NOTE (1) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

13.2 TESTING CONDITIONS AND INSPECTION CRITERIA

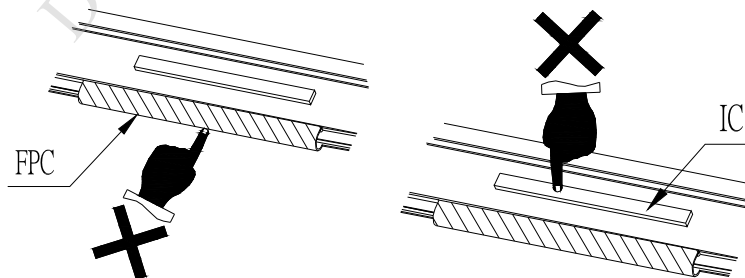
FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 13.1, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

14. CAUTION

14.1 OPERATION

- 14.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 14.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR . WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 14.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 14.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.
IF ABOVE SEQUENCE IS NOT FOLLOWED , CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM.
- 14.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!
DO NOT STRESS FPC AND IC ON THE MODULE!



14.2 HANDLING

- 14.2.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD.
- 14.2.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED.
- 14.2.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 14.2.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 14.2.5 DON'T GIVE EXTERNAL SHOCK.
- 14.2.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 14.2.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 14.2.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 14.2.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 14.2.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 14.2.11 REWIRING: NO MORE THAN 3 TIMES.