MINED BY:		FILE NO . CAS-0008341
Lucica Lu	EMERGING DISPLAY	ISSUE : OCT.05, 2017
OVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 23
Vincent Uh		VERSION: 3
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
	Display Children	Station only

EMERG	ING D	ISPLAY	MODEL NO	Э.			VERSI	ON	PAGE
	OGIES CORI		ЕТ1	01002	D B 6		3		0-1
			DOC . FIR		<i>D D</i> 0				<u> </u>
RECORD	S OF R	EVISION	DOC . TIK	SI ISSUL				ΑU	JG.29, 201 <i>6</i>
RECORD	REVISED								
DATE	PAGE			SUMM	ARY				
	NO.								
JAN.10, 2017	1	2. MECHANICAL (3)MODULE SI			AV)				
	4	4. ELECTRICAL			ΑΛ.)				
	·	MODIFY NOTE		1100					
	9	MODIFY 6.2 THE		IOD OF BRI	GHTNES	S ANI	O UNIF	ORN	ИТY
	10	7. OUTLINE DIM		E THICKN	ECC 6- D/I	EDC	^)
		MARKA: MOD	PCB DIMEN		ESS & D/1	J FPC,		,	
OCT.05, 2017	2	3.1 ELECTRICAL			1 RATING	SS			
		LED BACKLI	\			. (' ')			5
		LED BACKLÍO	_ /			- Y			
	3	LED BACKLIO 4. ELECTRICAL			jE : MAX	1.=(20.	.4)→7.:	2	~
	3	VDD CURREN			AX = (250)	→190		0,	
		POWER SUPPL						,	
		TYP.=(550)→50	00, MAX.=(65	50)→625	3	• ,	1		
		LED LIFE TIMI					<u> </u>		
	5,6	5. TIMING CHAF REVISING THI		77.7	5	0			
	8	6.1 OPTICAL CHA				, y			
		ITEM	SYMBOL θ_{v+}	CONDITION	MIN. TYP. (60) (70)	MAX. U	NIT REM	/ARK	
		VIEWING ANGLE	θ_{y} , θ_{x} ,	$CR \ge 10$ $\theta_x = 0^{\circ}$	(70) (80) (70) (80)			E (2) E (3)	
		CONTRAST RATIO	θ_{x} - CR	$\theta_y=0^{\circ}$ $\theta x=0^{\circ}$, $\theta y=0^{\circ}$	(70) (80) (400) (500)	_		E (3)	
		RESPONSE TIME	T _R (rise)+ T _F (fall)	θx=0°, θy=0°	(16)			E (4)	
		COLOR OF RED	Wy	04-08 0-08	(0.32) (0.37)	(0.42)	_		
		CIE COORDINATE GREE	N Gx	θx=0°, θy=0° NTSC: 45% VLED-VSS=12.0V		(0.336)	NOT	E (6)	
		BLUI	Bx			(0.20)			
		THE BRIGHTNESS OF MODULE	By B	θx=0°, θy=0° VLED-VSS=12.0V	(0.095) (0.145) (800) (1000)		d/m ² NOT	E (5)	
		ITEM	SYMBOL	VLED-V33=12:0V ↓ CONDITION	MIN. TYP.	MAX. U	NIT REM	/ARK	
			θ_{y+} θ_{y-}	$\theta_x=0^{\circ}$	60 70 70 80			E (2)	
		VIEWING ANGLE	$\theta_{x^{+}}$ $\theta_{x^{-}}$	$CR \ge 10$ $\theta_y=0^{\circ}$	70 80 70 80	(leg. NOT	E (3)	
	and different to the second	CONTRAST RATIO RESPONSE TIME	CR $T_R (rise) + T_F (fall)$	θx=0°, θy=0° θx=0°, θy=0°	510 680 — 16		- NOT		
	.62	WHIT	W/v	on o , oy o	0.25 0.30 0.30 0.35	0.35 0.40	_	L (+)	
		COLOR OF RED	Ry	θx=0°, θy=0°	0.54 0.59 0.31 0.36	0.64			
	0	CIE COORDINATE GREE	N Gx Gy	NTSC : 45% VLED-VSS=12.0V	0.25 0.30 0.52 0.57	0.35	- NOT	E (6)	
4		BLUI	Bx By		0.10 0.15 0.07 0.12	0.20 0.17	_		
	Y	THE BRIGHTNESS OF MODULE	В	$\theta x=0^{\circ}$, $\theta y=0^{\circ}$ VLED-VSS=12.0V	800 1000	c	d/m ² NOT	E (5)	
	13	11. POWER SUPPLY							
		DELETE 11.2	POWER . SIG	GNAL SEQU	JENCE				

E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

MODEL NO. VERSION PAGE
E T 1 0 1 0 0 2 D B 6 3 0-2

TABLE OF CONTENTS

NO.	ITEM	PAGE
1.	GENERAL SPECIFICATIONS	A41°
2.	MECHANICAL SPECIFICATIONS	1
3.	ABSOLUTE MAXIMUM RATINGS	2
4.	ELECTRICAL CHARACTERISTICS	3,4
5.	TIMING CHARACTERISTICS	5,6
6.	OPTICAL CHARACTERISTICS	7,8
7.	OUTLINE DIMENSIONS	9
8.	BLOCK DIAGRAM	10
9.	DETAIL DRAWING OF DOT MATRIX	11
10.	INTERFACE SIGNALS	12
11.	POWER SUPPLY	13
12.	INSPECTION CRITERION	14 ~ 23
	Engereine Donot distribute willhow	

EMERGING	DISPLAY	N
TECHNOLOGIES	CORPORATION	

MODEL NO.	VERSION	PAGE
ET101002DB6	3	1

1. GENERAL SPECIFICATIONS

1.1 DATA SHEETS FOR CONTROLLER/DRIVER PLEASE REFER TO :

SITRONIX ST5651CB SITRONIX ST5021

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EDT GREEN PRODUCT (GP)

REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS

CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM,

POLYBROMINATED BIPHENYLS (PBB), POLYBROMINATED

DIPHENYL ETHERS (PBDE), POLYCHLORINATED BIPHENYLS (PCB)

CATEGORY, POLYCHLORINATED NAPHTHALENE (PCN) CATEGORY,

POLYCHLORINATED TERPHENYLS (PCT) CATEGORY, CHLORINATED

PARAFFINS (CP) CATEGORY, TRIBUTHYL TIN CATEGORY / TRIPHENYL TIN

CATEGORY, ASBESTOS, SPECIFIC AZO COMPOUNDS, FORMALDEHYDE,

POLYVINYL CHLORIDE (PVC) AND PVC BLENDS, OTHER BROMINATED

ORGANIC COMPOUNDS AND OTHER CHLORINATED ORGANIC COMPOUNDS.

2. MECHANICAL SPECIFICATIONS

(1) DISPLAY SIZE	10.1 inch
(2) NUMBER OF DOTS	1024W * (RGB) * 600H DOTS
(3) MODULE SIZE	235.5W * 143H * 9.3D(MAX.) mm
(4) VIEWING AREA	225.4W * 128H mm
(5) ACTIVE AREA	222.72W * 125.28H mm
(6) DOT SIZE	0.0725W * 0.2088H mm
(7) PIXEL SIZE	0.2175W * 0.2088H mm
(8) LCD TYPE	\ensuremath{TFT} , $\ensuremath{TRANSMISSIVE}$, $\ensuremath{ANTI-GLARE}$
(9) COLOR	16.7 M
(10) VIEWING DIRECTION	6 O'CLOCK (GRAY LEVEL INVERSION)
(11) BACK LIGHT	LED , COLOR : WHITE
(12) INTERFACE MODE	LVDS

E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

MODEL NO.	VERSION	PAGE
ET101002DB6	3	2

3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.5	5.0	V	
LED BACKLIGHT POWER	PD		18.36	W	
DISSIPATION	TD		10.50	VV	
LED BACKLIGHT FORWARD	IF		900	mA	14.
CURRENT	II.		900	ША	
LED BACKLIGHT REVERSE	VR		7.2	V	O
VOLTAGE	VK		1.2	•	

NOTE (1): LCM SHOULD BE GROUND DURING HANDLING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPER.	OPERATING		RAGE	REMARK	
HEM	MIN.	MAX.	MIN.	MAX.	KEWIAKK	
AMBIENT TEMPERATURE	-30°C	80°C	-30°C	80°C	NOTE (1), (2)	
HUMIDITY	NOTI	E(3)	NOTI	E(3)	WITHOUT	
	,	49	. (2)		CONDENSATION	
VIBRATION	AP^	2.45 m/s^2		11.76m/s^2	10~55Hz	
VIBRATION		(0.25 G)	00	(1.2 G)	X,Y,Z,EACH 2HRS	
		20.4. (2)		100.0 (2	6 ms	
SHOCK		29.4 m/s ² (3 G)	_	490.0 m/s^2 (50 G)	XYZ DIRECTIONS	
		(3 3)	00	(300)	3 TIMES EACH	
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACC	EPTABLE		

NOTE (1): Ta AT -30°C: WILL BE 48HRS MAX.

80°C: WILL BE 48HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3): $Ta \le 60^{\circ}C : 90\%RH MAX$. (96HRS MAX).

Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C (96HRS MAX).

MODEL NO.	VERSION	PAGE
ET101002DB6	3	3

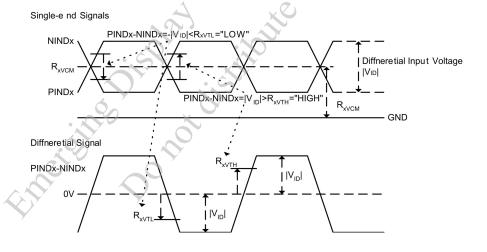
4. ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
LOGIC/LCD DRIVE VOLTAGE	VDD-VSS	_	3.0	3.3	3.6	V	
VDD CURRENT	IDD			150	190	mA	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VLED-VSS	_	11.7	12.0	12.3	V	
POWER SUPPLY CURRENT FOR LED DRIVER	IVLED	VLED-VSS=12.0V		500	625	mA	\ •
DIFFERENTIAL INPUT HIGH THRESHOLD VOLTAGE	RxVTH	RxVCM=1.2V	_		0.1	V	
DIFFERENTIAL INPUT LOW THRESHOLD VOLTAGE	RxVTL	KX V CIVI-1.2 V	-0.1			v	
INPUT VOLTAGE RANGE (SINGLED-END)	RxVIN		0		2.4	V	
DIFFERENTIAL INPUT COMMON MODE VOLTAGE	RxVCM	_	VID /2	_<	2.4- VID /2	V	0.
DIFFERENTIAL INPUT VOLTAGE	VID		0.2		0.6	V) '
DIFFERENTIAL INPUT LEAKAGE CURRENT	RVxIiz	x d	-10		+10	μΑ	
LVDS DIGITAL OPERATING CURRENT	Iddlvds	Fclk=65MHz, VDD-VSS=3.3V)	40	50	mA	
LVDS DIGITAL STANDBY CURRENT	Istlvds	CLOCK & ALL FUNCTIONS ARE STOPPED	5-	10	50	μΑ	
LED LIFE TIME	NA.	ILED=120mA (PER. LED)	50K	7	_	HRS	NOTE (5) NOTE (6)

NOTE (1): A. INPUT SIGNALS SHALL BE LOW OR HI-Z STATE WHEN VDD IS OFF.

B. ALL ELECTRICAL CHARACTERISTICS FOR LVDS SIGNAL ARE DEFINED AND SHALL BE MEASURED AT THE INTERFACE CONNECTOR OF LCD.

NOTE (2): LVDS INPUT CHARACTERISTICS ARE AS FOLLOW



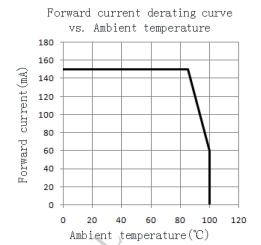
DISPLAY EMERGING TECHNOLOGIES CORPORATION

MODEL NO. 3

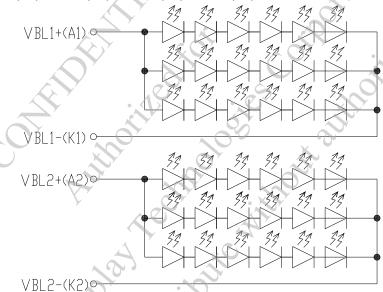
VERSION **PAGE** 4

ET101002DB6

NOTE (3): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT. (PER. LED)



NOTE (4): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT VF=VBL1+(A1)-VBL1-(K1)=VBL2+(A2)-VBL2-(K2)



NOTE (5): CONDITIONS; TA=25 °C, CONTINUOUS LIGHTING

NOTE (6): DEFINITIONS OF LIFE TIME

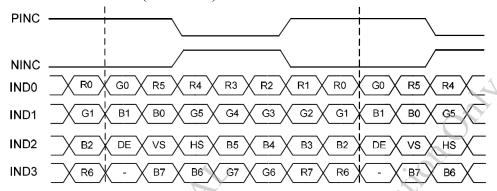
LCM LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

MODEL NO.	VERSION	PAGE
ET101002DB6	3	5

5. TIMING CHARACTERISTICS

5.1 DATA INPUT FORMAT

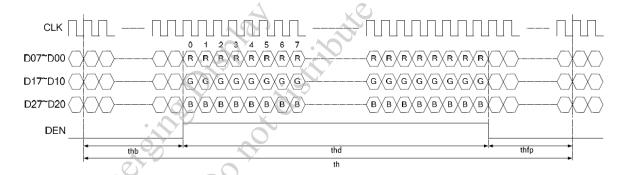
5.1.1 8 BIT LVDS INPUT(HSD="L")



5.2 TIMING TABLE

5.2.1 MODE ="H": DE MODE

	. /				
ITEM	SYMBOL	MIN.	CTYP.	MAX.	UNIT
CLKIN FREQUENCY@ FRAME RATE = 60Hz	felk	40.8	51.2	67.2	MHz
HORIZONTAL DISPLAY AREA	thd	40	1024		
1 HORIZONTAL LINE	th	1114	1344	1400	CLKIN
HSD BLANKING	thb+thfp	90	320	376	
VERTICAL DISPLAY AREA	tvd		600		
1 VERTICAL LINE	tv	610	635	800	Н
VSD BLANKING	tvb+tvfp	10	45	220	

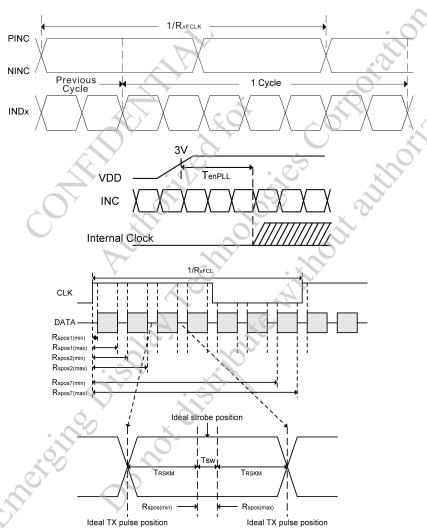


MODEL NO.	VERSION	PAGE
ET101002DB6	3	6

5.3 AC ELECTRICAL CHARACTERISTICS

LVDS MODE

ETDOMODE						
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITION
CLOCK FREQUENCY	RxFCLK	20	_	71	MHz	
INPUT DATA SKEW MARGIN	Trskm	500	_	_	pS	$\begin{aligned} V_{ID} &= 400 mV \\ R_{xVCM} &= 1.2 V \\ R_{xFCLK} &= 71 \ MHz \end{aligned}$
CLOCK HIGH TIME	Tlvch		4/(7* RxFCLK)		ns	14
CLOCK LOW TIME	Tlvcl		3/(7* Rxfclk)		ns	
PLL WAKE-UP TIME	TenPLL		_	150	us	



Trskm: Receiver strobe margin Rspos Receiver strobe position Tsw: Strobe width (internal data sampling window)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
MODULATION FREQUENCY	SSCMF		23	_	93	KHz
MODULATION RATE	SSCMR	LVDS CLOCK=71MHz CENTER SPREAD	_		±3	%

MODEL NO.	VERSION	PAGE
ET101002DB6	3	7

6. OPTICAL CHARACTERISTICS (NOTE 1)

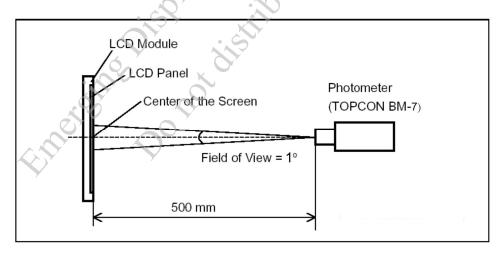
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

$1u - 25 \pm 2$					u 23 ± 2 C							
ITEM		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK			
		$\theta_{\mathrm{y}^{+}}$	$\theta_{\rm x}=0^{\circ}$	60	70							
VIEWING ANGI	E	$\theta_{ ext{y-}}$	CR ≥ 10	Θ_{X} =0	70	80	_	dog	NOTE (2)			
VIEWING ANGI	∠ E	θ_{x^+}		0 -00	70	80		deg.	NOTE (3)			
		$\theta_{ ext{x-}}$		$\theta_y=0^{\circ}$	70	80		<	3			
CONTRAST RA	ПО	CR	θx=0°,	θy=0°	510	680			NOTE (3)			
RESPONSE TIM	E	T_R (rise)+ T_F (fall)	θx=0°,	θy=0°		16	32	msec	NOTE (4)			
	WHITE	Wx	4		0.25	0.30	0.35					
	WHILE	Wy			0.30	0.35	0.40	_				
COLOR OF	DED	Rx			0.54	0.59	0.64		~			
COLOR OF	RED	RY		$\theta x=0^{\circ}$,					0.31	0.36	0.41	- (
CIE COORDINATE	GREEN	Gx	NTSC VLED-VS		0.25	0.30	0.35		NOTE (6)			
COORDINATE	GKEEN	Gy	X	9	0.52	0.57	0.62 •					
	BLUE	Bx	6	,	0.10	0.15	0.20	Y				
	BLUE	Ву	40		0.07	0.12	0.17					
THE BRIGHTNE	SS	В	3,1	6	800	1000		cd/m ²	NOTE (5)			
OF MODULE) ' В		θy=0°	800	1000		Cu/III	NOIE (3)			
THE UNIFORMITY OF			VLED-VS	SS=12.0V	70			%	NOTE (5)			
MODULE					70			/0	NOIE (3)			
TOTTE (1) · TEE	TE EQUID	A CENTER OF THE A		7								

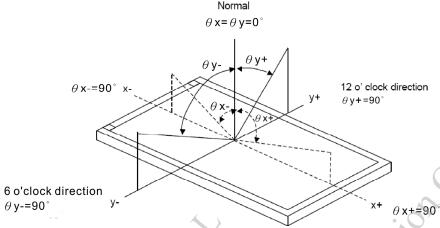
NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



MODEL NO.	VERSION	PAGE
ET101002DB6	3	8

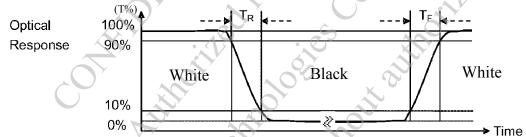
NOTE (2): DEFINITION OF VIEWING ANGLE:



NOTE (3): DEFINITION OF CONTRAST RATIO:

 $CONTRAST RATIO(CR) = \frac{BRIGHTNESS \text{ MEASURED WHEN LCD IS AT "WHITE STATE"}}{BRIGHTNESS \text{ MEASURED WHEN LCD IS AT "BLACK STATE"}}$

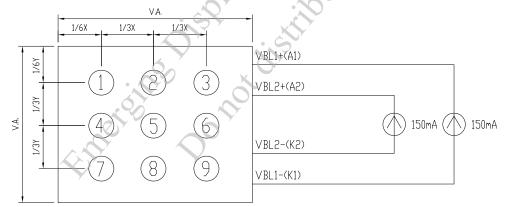
NOTE (4): DEFINITION OF RESPONSE TIME: T_R AND T_F THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5): BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

NOTE (6): THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY

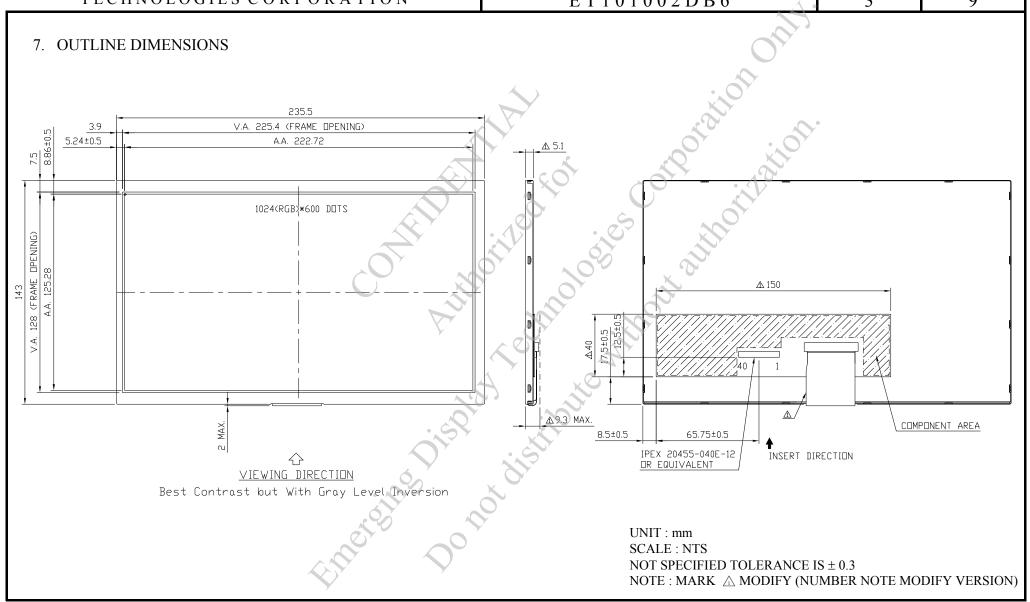


UNIT: mm

6.3 THE CALCULATING METHOD OF UNIFORMITY

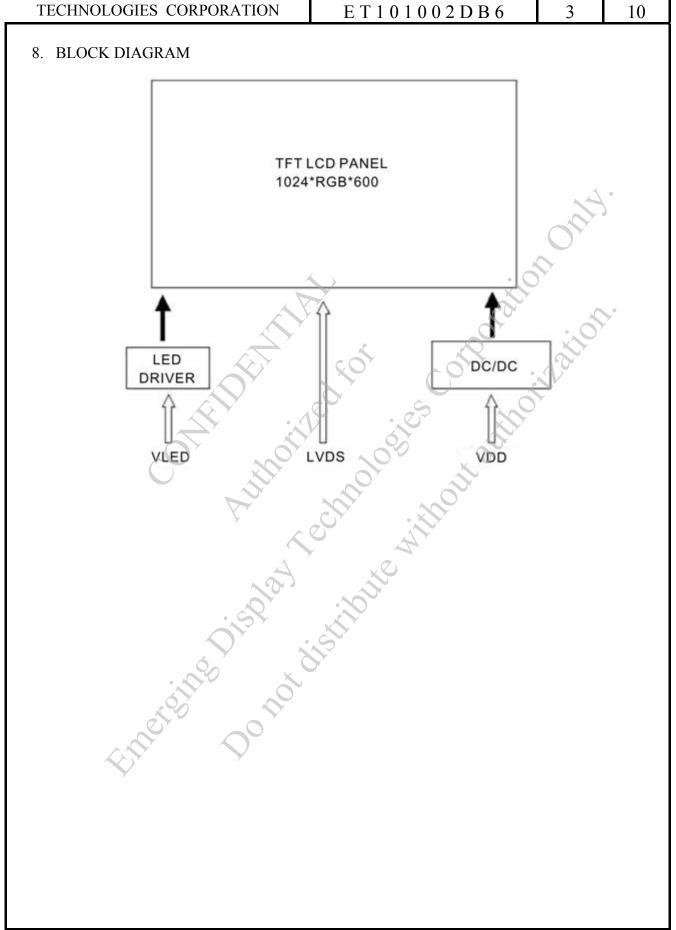
E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

MODEL NO.	VERSION	PAGE
ET101002DB6	3	9



 MODEL NO.
 VERSION
 PAGE

 E T 1 0 1 0 0 2 D B 6
 3
 10



MODEL NO.	VERSION	PAGE
ET101002DB6	3	12

10. INTERFACE SIGNALS

PIN NO.	SYMBOL	FUNCTION
1	NC	NON CONNECTION
2	VDD	POWER SUPPLY VOLTAGE
3	VDD	POWER SUPPLY VOLTAGE
4	NC	NON CONNECTION
5	NC	NON CONNECTION
6	NC	NON CONNECTION
7	NC	NON CONNECTION
8	IND0-	LVDS SIGNAL (-) — CHANNEL 0 (R0~R5,G0)
9	IND0+	LVDS SIGNAL (+) — CHANNEL 0 (R0~R5,G0)
10	VSS	GROUND
11	IND1-	LVDS SIGNAL (-) — CHANNEL 1 (G1~G5,B0~B1)
12	IND1+	LVDS SIGNAL (+) — CHANNEL 1 (G1~G5,B0~B1)
13	VSS	GROUND
14	IND2-	LVDS SIGNAL (-) — CHANNEL 2 (B2~B5, VS, HS, DE)
15	IND2+	LVDS SIGNAL (+) — CHANNEL 2 (B2~B5,VS,HS,DE)
16	VSS	GROUND
17	NINC	LVDS CLOCK SIGNAL (–)
18	PINC	LVDS CLOCK SIGNAL (+)
19	VSS	GROUND
20	IND3-	LVDS SIGNAL (-) — CHANNEL 3 (R6,R7,G6,G7,B6,B7)
21	IND3+	LVDS SIGNAL (+) — CHANNEL 3 (R6,R7,G6,G7,B6,B7)
22	VSS	GROUND
23	NC	NON CONNECTION
24	NC	NON CONNECTION
25	VSS	GROUND
26	NC •	NON CONNECTION
27	NC	NON CONNECTION
28	VSS	GROUND
29	NC	NON CONNECTION
30	NC	NON CONNECTION
31	VSS	GROUND
32	VSS	GROUND
33	VSS	GROUND
34	NC	NON CONNECTION
35	PWM	ADJUST FOR LED BRIGHTNESS
36	LED-ENABLE	INTERNAL PULL UP TO +3.3V
37	NC	NON CONNECTION
38	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT
39	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT
40	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT

MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION ET101002DB6 3 13 11. POWER SUPPLY 11.1 POWER SUPPLY FOR LCM **VDD** TFT LCD 3.3V John Janes VSS **VLED** LED DRIVER 12.0V CIRCUIT VSS LED-ENABLE **PWM** NOTE (1): PWM ADJUST BRIGHTNESS TO CONTROL PIN PULSE DUTY THE BIGGER THE BRIGHTER. LUMIN ANCE LUMIN ANCE BRIGHT LUMIN ANCE → DUTY DUTY(0%) DUTY(100%) NOTE (2): PWM SIGNAL=0~3.3V, OPERATION FREQUENCY: 20KHz±5KHz F=20KHz±5 KHz 3.3V

0V

MODEL NO.	VERSION	PAGE
ET101002DB6	3	14

12. INSPECTION CRITERION

12.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

12.2 INSPECTION CONDITIONS

12.2.1 (1)OBSERVATION DISTANCE: 35±5cm

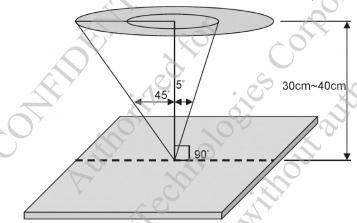
(2) VIEW ANGLE:

NON-OPERATION CONDITION: ±5°

(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION: ±45°

(PERPENDICULAR TO LCD PANEL SURFACE)



12.2.2 ENVIRONMENT CONDITIONS:

AMBIENT	25±5°C	
AMBIE	65±20%RH	
AMBIENT	COSMETIC INSPECTION	MORE THAN 600Lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 Lux

12.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

12.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a) APPLICABLE STANDARD:

MIL-STD-105E

NORMAL INSPECTION, SINGLE SAMPLING

Level II

(b)AQL : MAJOR DEFECT : AQL 0.65 MINOR DEFECT : AQL 1.0

MODEL NO.	VERSION	PAGE
ET101002DB6	3	15

12.3 INSPECTION STANDARDS

12.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
MAJOR DEFECT	2.BACKLIGHT	NO LIGHTFLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65
	3.DIMENSIONS	 SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS 	> °
MINOR DEFECT	1.DISPLAY ZONE 2.BEZEL ZONE 3.SOLDERING 4.DISPLAY ON (ALL ON)	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION UNEVEN COLOR SPREAD STAINS SCRATCHES FOREIGN MATTER INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS LIGHT LINE 	1.0

MODEL NO.	VERSION	PAGE
ET101002DB6	3	16

12.3.2 MODULE DEFECTS CLASSIFICATION

NO.	ITEM	CRITERIA		
1	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC		
2	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND	SPEC	
3	DOT DEFECT	(1)INSPECTION PATTERN: FULL WE AND BLUE SCREENS. (2) ITEMS BRIGHT DOT DARK DOT TOTAL BRIGHT AND DARK DOTS NOTE: 1. THE DEFINITION OF DOT: THE SIZE OF A DEFECTIVE DOT OR REGARDED AS ONE DEFECTIVE I 2. BRIGHT DOT: DOTS APPEAR BRIGHT AND UNCE PANEL IS DISPLAYING UNDER BI 3. DARK DOT: DOTS APPEAR DARK AND UNCH PANEL IS DISPLAYING UNDER PI	ACCEPTABLE COUNT N≤3 N≤4 N≤5 OVER 1/2 OF WHOLE DOT IS DOT. HANGED IN SIZE IN WHICH LCD LACK PATTERN. ANGED IN SIZE IN WHICH LCD	
4	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA		PERMISSIBLE NO. IGNORE 4 NONE	
5	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	AVERAGE DIAMETER (mm): D $D \le 0.15$ $0.15 < D \le 0.5$ $0.5 < D$ NOTE: DIAMETER D=(a+b)/2	NUMBER OF PIECES PERMITTED IGNORE 4 NONE	

 MODEL NO .
 VERSION
 PAGE

 ET101002DB6
 3
 17

NO.	ITEM	CRITERIA		
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED
		BUBBLE ON THE	D ≤ 0.15	LGNORE
		POLARIZER	$0.15 < D \le 0.5$	N ≤ 4
			0.5 < D	NONE
		SURFACE STAINS	D < 0.1	IGNORE
			$0.1 < D \le 0.3$	N ≤ 3 IGNORE
		CF FAIL / SPOT	$D < 0.1 0.1 < D \le 0.3$	N≤3
6	NOTE: (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPE ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBI APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINE AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2			DEFECT OF POLARIZER HE POLARIZER BUBBLE CTIVE DISPLAY AREA. S DEFINED AS IT CAN BE IS POWER ON.
7	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED		
8	MURA ON DISPLAY	IT'S OK IF MURA IS SLIGHT VISIBLE THROUGH 5% ND FILTER		
9	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE. (1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS. (1)THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. (2)NO OXIDATION OR CONTAMINATION PCB TERMINALS. (3)PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. (4)THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. (5)IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.		
10	BEZEL APPEARANCE			
11	PCB CITE OF THE POPULATION OF			

 MODEL NO.
 VERSION
 PAGE

 E T 1 0 1 0 0 2 D B 6
 3
 18

(1)NO SOLDERING FOUND ON THE SPECIFIED PLA (2)INSUFFICIENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN L
*HEEL" OF LEAD AND PAD SOLDER FILLET (b)CHIP COMPONENT · SOLDER IS LESS THAN 50% OF SIDES AND WETTING SOLDER FILLET 1/2 · SOLDER WETS 3 SIDES OF TERMINAL, BUT SIDES AND FRONT SURFACE AREA ARE CO

 MODEL NO.
 VERSION
 PAGE

 ET101002DB6
 3
 19

110		OD TEED I
NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12	SOLDERING	
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

 MODEL NO.
 VERSION
 PAGE

 ET101002DB6
 3
 20

NO.	ITEM		CRITERIA		
		THE LCD WITH EXTENSIVE (CRACK IS NOT	ACCEPTABLE	Ξ
		GENERAL GLASS CHIP:	a	b	С
		, b	≤ t/2	< VIEWING AREA	≤ 1/8X
		c a	$t/2 > , \le 2t$	≤ W/2	≤ 1/8X
			*W=DISTANC		
		` `		AREA AND LO	CD
			PANEL EI		4 .
			X = LCD SID t = GLASS T		4
		W. C.	l = ULASS 1	HICKINESS	0,
) 7
		a		~	
				. 0	
		Th I		X	
					· .
		C		·	
		N _b	~	×	
				10	
		a			
		CODNED DART	a	b	c
	X	CORNER PART	≤ t/2	< VIEWING AREA	≤ 1/8X
		b	> t/2 , ≤ 2t	≤ W/2	≤ 1/8X
	== :		*W=DISTANC		
15	CRACKED GLASS			AREA AND LO	CD
		a	PANEL EI X = LCD SID		
			t = GLASS T		
			t-UEASS I	HICKINESS	
		CHIP ON ELECTRODE PAD	a	b	С
		a a	≤t	≤ 0.5mm	≤ 1/8X
			* X=LCD SIDE		
			t=GLASS TI	HICKNESS	
		~ c	-	T	ī
		.67	a ≤ t	b ≤ 1/8X	c ≤ L
	4		*X=LCD SIDE		> L
	6		t = GLASS TH		
				DE PAD LENGT	ГН
				HIPPING THE I	
	200			, OVER 2/3 OF	
	CY -			ND BE, INSPEC	
		a		G TO ELECTRO	
		b		SPECIFICATION	
	Y			DUCT WILL BE THE CUSTOM	
				MENT MARK N	
			BE DEMAGI		
		4	l		

MODEL NO.	VERSION	PAGE
ET101002DB6	3	21

12.4 RELIABILITY TEST

12.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMPERATURE / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: +80°C -30°C -30°C -30°C 1 CYCLE
7	(ELECTROSTATIC DISCHARGE)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

MODEL NO.	VERSION	PAGE
ET101002DB6	3	22

12.5 TESTING CONDITIONS AND INSPECTION CRITERIA
FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM
TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 12.5,
STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN
ORDER TO ENSURE STABILITY.

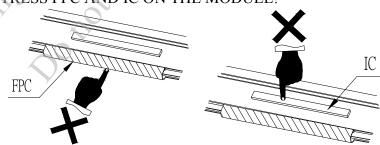
NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION		THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST		AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

12.6 OPERATION

- 12.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 12.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 12.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 12.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

 IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 12.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



MODEL NO.	VERSION	PAGE
ET101002DB6	3	23

12.7 NOTICE

- 12.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD
- 12.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED.
- 12.7.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 12.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 12.7.5 DON'T GIVE EXTERNAL SHOCK.
- 12.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 12.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
 WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 12.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 12.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 12.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 12.7.11 REWIRING: NO MORE THAN 3 TIMES.