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Sam Chou		ISSUE : OCT.13, 2017
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<i>Chris Wu</i>		VERSION : 2

CUSTOMER	ACCEPTANCE	SPECIFICATIONS
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MODEL NO. :

ETML070016DRA

(RoHS)

FOR MESSRS :  
\_\_\_\_\_

CUSTOMER'S APPROVAL

DATE :

\_\_\_\_\_

BY :

\_\_\_\_\_

EMERGING DISPLAY  
TECHNOLOGIES CORPORATION

MODEL NO.

ETML070016DRA

VERSION

2

PAGE

0-1

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JUL.28, 2017

RECORDS OF REVISION

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PAGE  
NO.

SUMMARY

OCT.13, 2017

2

2.2 CAPACITIVE TOUCH PANEL MECHANICAL SPECIFICATIONS  
(7) RESOLUTION:1024\*600→4096\*4096

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## 1. GENERAL SPECIFICATIONS

### 1.1 DATA SHEETS FOR CONTROLLER/DRIVER

PLEASE REFER TO :

SITRONIX ST5651CB  
SITRONIX ST5021CA

### 1.2 APPLICATION NOTES FOR CAPACITIVE TOUCH PANEL CONTROLLER/DRIVER PLEASE REFER TO :

SIS9255B

### 1.3 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS,  
INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD,  
MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED  
BIPHENYLS (PBB) AND POLYBROMINATED  
DIPHENYL ETHERS (PBDE)

## 2. MECHANICAL SPECIFICATIONS

### 2.1 LCD MODULE MECHANICAL SPECIFICATIONS

( 1 ) DISPLAY SIZE	7 inch
( 2 ) NUMBER OF DOTS	1024W * (RGB) * 600H DOTS
( 3 ) MODULE SIZE	166.2W * 100.3H * 11.0D(MAX.) mm (NOT INCLUDED FPC)
( 4 ) VIEWING AREA	156.2W * 87.9H mm
( 5 ) ACTIVE AREA	154.2144W * 85.92H mm
( 6 ) DOT SIZE	0.0502W * 0.1432H mm
( 7 ) PIXEL SIZE	0.1506W * 0.1432H mm
( 8 ) LCD TYPE	TFT , TRANSMISSIVE , ANTI-GLARE
( 9 ) COLOR	16.7M
( 10 ) VIEWING DIRECTION	SUPER WIDE VIEW
( 11 ) BACK LIGHT	LED , COLOR : WHITE
( 12 ) INTERFACE MODE	LVDS

## 2.2 CAPACITIVE TOUCH PANEL MECHANICAL SPECIFICATIONS

- (1) TOUCH PANEL SIZE ----- 7 inch
- (2) OUTER DIMENSION ----- 163.7W \* 100.3H \* 1.3D mm  
(NOT INCLUDED FPC)
- (3) ACTIVE AREA ----- 155.6W \* 91.01H mm
- (4) INPUT TYPE ----- MULTI-TOUCH
- (5) NUMBER OF TOUCH SENSOR ----- 28\*16 SENSORS
- (6) INTERFACE MODE ----- I2C
- (7) RESOLUTION ----- 4096\*4096

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### 3. ABSOLUTE MAXIMUM RATINGS

#### 3.1 TFT MODULE ELECTRICAL ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.5	6.5	V	
INPUT VOLTAGE	VIN-VSS	-0.3	VDD+0.3	V	

#### 3.2 CAPACITIVE TOUCH PANEL ELECTRICAL ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY FOR DRIVER	VCC-GND	-0.3	3.6	V	

#### 3.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE ( 1 ) , ( 2 )
HUMIDITY	NOTE ( 3 )		NOTE ( 3 )		WITHOUT CONDENSATION
VIBRATION	—	2.45 m/s <sup>2</sup> ( 0.25 G )	—	11.76 m/s <sup>2</sup> ( 1.2 G )	10~55Hz , X,Y,Z, EACH 2HRS
SHOCK	—	29.4 m/s <sup>2</sup> ( 3 G )	—	490 m/s <sup>2</sup> ( 50 G )	10 ms XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		

NOTE ( 1 ) : Ta AT -30°C : 48HRS MAX.  
80°C : 48HRS MAX.

NOTE ( 2 ) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE ( 3 ) : Ta ≤ 60°C : 90%RH MAX. (96HRS MAX).

Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C (48HRS MAX).

#### 4. ELECTRICAL CHARACTERISTICS

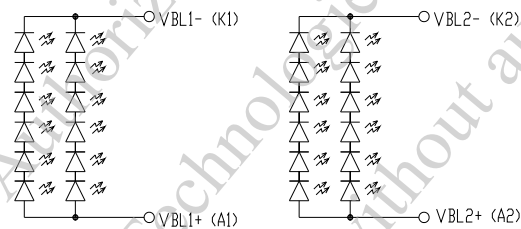
##### 4.1 TFT MODULE ELECTRICAL CHARACTERISTICS

Ta = 25 °C

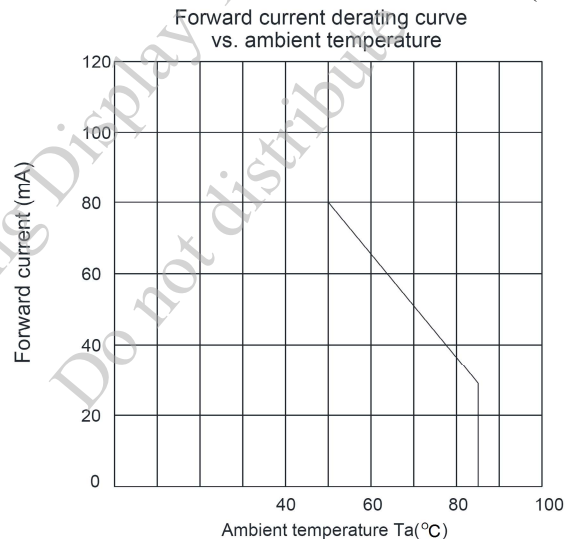
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	—	3.15	3.3	3.45	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VLED-VSS	—	9	12	15	V	
LOW LEVEL INPUT VOLTAGE	VIL	—	0	—	0.3*VDD	V	NOTE ( 1 )
HIGH LEVEL INPUT VOLTAGE	VIH	—	0.7*VDD	—	VDD	V	NOTE ( 1 )
POWER SUPPLY CURRENT	IDD	VDD-VSS = 3.3V	—	130	160	mA	NOTE ( 2 )
POWER SUPPLY CURRENT FOR LED DRIVER	ILED	VLED-VSS=12V LED B/L=ON	—	420	510	mA	
LED LIFE TIME	—	ILED=50mA PER. LED	—	50K	—	hrs	NOTE ( 4 ) NOTE ( 5 )

NOTE ( 1 ) : THE DISPLAY PATTERN IS ALL “BLACK”.

NOTE ( 2 ) : INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE ( 3 ) : AMBIENT TEMP .VS. ALLOWABLE FORWARD CURRENT (PER LED)



NOTE ( 4 ) : CONDITIONS; TA=25 °C, CONTINUOUS LIGHTING.

NOTE ( 5 ) : DEFINITIONS OF FAILURE :

LCD LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

## 4.2 CAPACITIVE TOUCH PANEL ELECTRICAL CHARACTERISTICS

Ta=25°C

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
POWER SUPPLY FOR DRIVER	VCC-GND	—	3.15	3.3	3.45	V
INPUT HIGH-LEVEL VOLTAGE	VIH	—	2	—	VDD+0.3	V
INPUT LOW-LEVEL VOLTAGE	VIL	—	-0.3	—	0.8	V
OUTPUT HIGH-LEVEL VOLTAGE	VOH	—	0.9*VDD	—	—	V
OUTPUT LOW-LEVEL VOLTAGE	VOL	—	—	—	0.45	V
POWER SUPPLY CURRENT CONSUMPTION FOR OPERATION	ICC	VDD-VSS=3.3V	—	30	40	mA

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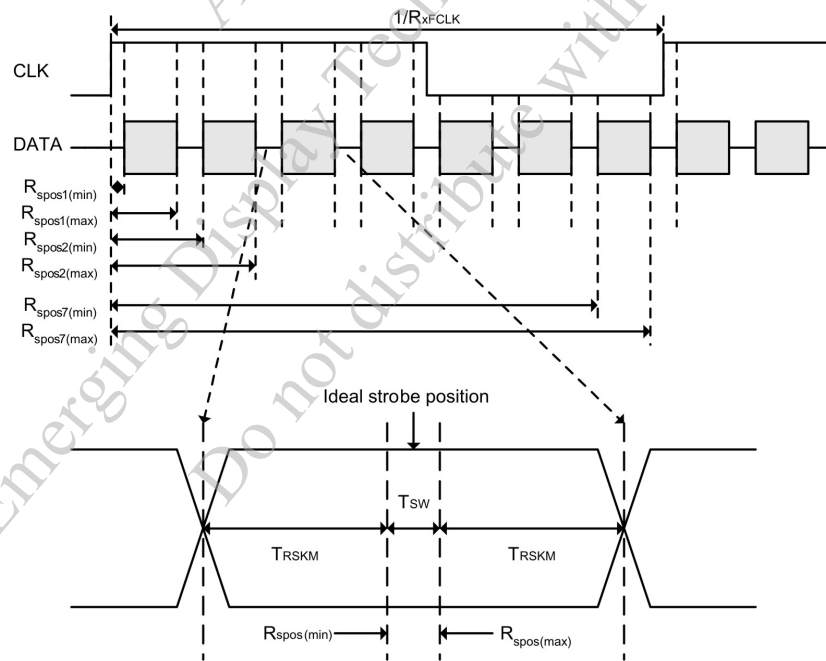
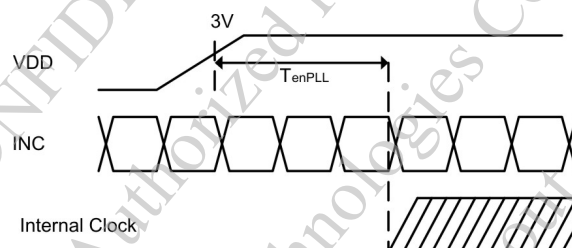
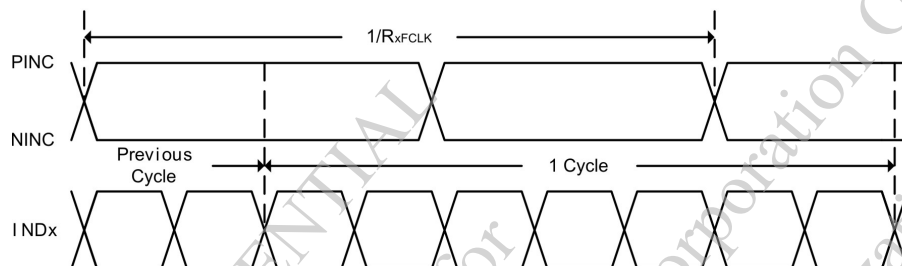


## 5. TIMING CHARACTERISTICS

### 5.1 FOR TFT MODULE

#### 5.1.1 AC ELECTRICAL CHARACTERISTICS (LVDS)

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
CLOCK FREQUENCY	$R_{xCLK}$	—	20	—	71	MHz
INPUT DATA SKEW MARGIN	$TR_{SKM}$	$ V_{ID}  = 400mV$ $R_{xVCM} = 1.2V$ $R_{xCLK} = 71 MHz$	500	—	—	pS
CLOCK HIGH TIME	$TLVCH$	—	—	$4/(7 * R_{xCLK})$	—	ns
CLOCK LOW TIME	$TLVCL$	—	—	$3/(7 * R_{xCLK})$	—	ns
PLL WAKE-UP TIME	$T_{enPLL}$	—	—	—	150	$\mu s$



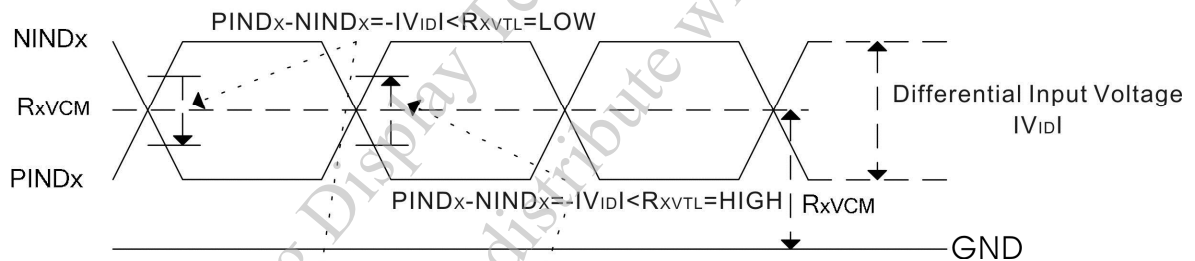
Ideal TX Pulse Position  
 $TR_{SKM}$  : RECEIVER STROBE MARGIN  
 $RSPOS$  : RECEIVER STROBE POSITION  
 $T_{SW}$  : STROBE WIDTH (INTERNAL DATA SAMPLING WINDOW)

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
MODULATION FREQUENCY	SSCMF	—	23	—	93	KHz
MODULATION RATE	SSCMR	LVDS clock = 71MHz CENTER SPREAD	—	—	±3	%

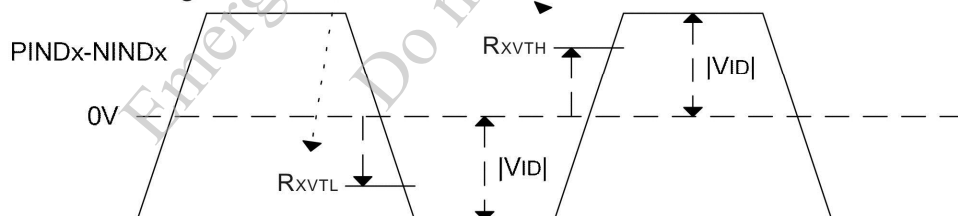
### 5.1.2 DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
DIFFERENTIAL INPUT HIGH THRESHOLD VOLTAGE	$R_{xVTH}$	—	—	+0.1	V	$R_{xVCM}=1.2V$
DIFFERENTIAL INPUT LOW THRESHOLD VOLTAGE	$R_{xVTL}$	-0.1	—		V	
INPUT VOLTAGE RANGE (SINGLED-END)	$R_{xVIN}$	0	—	2.4	V	
DIFFERENTIAL INPUT COMMON MODE VOLTAGE	$R_{xVCM}$	$ V_{ID} /2$	—	$2.4- V_{ID} /2$	V	
DIFFERENTIAL VOLTAGE	$ V_{ID} $	0.2	—	0.6	V	
DIFFERENTIAL INPUT LEAKAGE CURRENT	$R_{Vxliz}$	-10	—	+10	$\mu A$	
LVDS DIGITAL OPERATING CURRENT	LDDLVS	—	40	50	mA	FCLK=65MHZ VDD=3.3V
LVDS DIGITAL STAND-BY CURRENT	LSTLVDS	—	10	50	$\mu A$	CLOCK & ALL FUNCTIONS ARE STOPPED

#### Single end signals



#### Differential signals

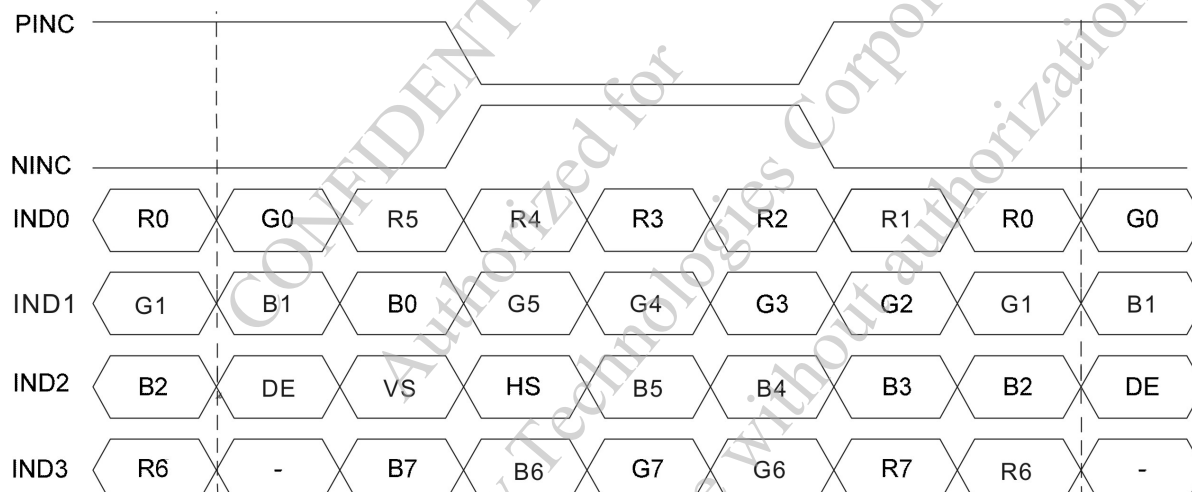


### 5.1.3 TIMING

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
CLOCK FREQUENCY	fclk	40.8	51.2	67.2	MHz	FRAME RATE=60HZ
HORIZONTAL DISPLAY AREA	thd	1024			DCLK	
HS PERIOD TIME	th	1114	1344	1400	DCLK	
HS BLANKING	thb	90	320	376	DCLK	
VERTICAL DISPLAY AREA	tvd	600			H	
VS PERIOD TIME	tv	610	635	800	H	
VS BLANKING	tvd + tvfp	10	35	200	H	

### DATA INPUT FORMAT

#### 8-BIT LVDS INPUT

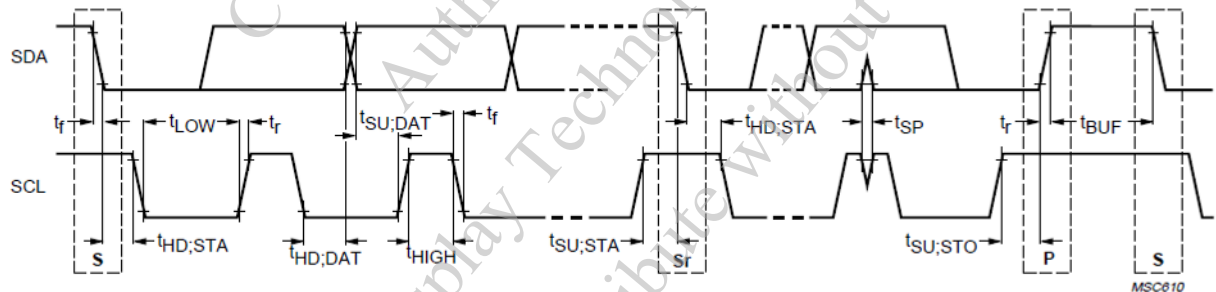


## 5.2 FOR CTP MODULE

### 5.2.1 I2C INTERFACE TIMING CHARACTERISTICS

SYMBOL	PARAMETER	STANDARD-MODE		FAST-MODE		UNIT
		MIN.	TYP.	MIN.	TYP.	
$f_{SCL}$	SCL CLOCK FREQUENCY	0	100	0	400	kHz
$t_{HD,STA}$	HOLD TIME (REPEATED) START CONDITION. AFTER THIS PERIOD, THE FIRST CLOCK PULSE IS GENERATED	4	—	0.6	—	$\mu$ S
$t_{LOW}$	LOW PERIOD OF THE SCL CLOCK	4.7	—	1.3	—	$\mu$ S
$t_{HIGH}$	HIGH PERIOD OF THE SCL CLOCK	4	—	0.6	—	$\mu$ S
$t_{SU,STA}$	SET-UP TIME FOR A REPEATED START CONDITION	4.7	—	0.6	—	$\mu$ S
$t_{HD,DAT}$	DATA HOLD TIME: FOR CBUS COMPATIBLE MASTERS	0	—	0	—	$\mu$ S
$t_{SU,DAT}$	DATA SET-UP TIME	250	—	100	—	nS
$t_r$	RISE TIME OF BOTH SDA AND SCL SIGNALS	—	1000	—	300	nS
$t_f$	FALL TIME OF BOTH SDA AND SCL SIGNALS	—	300	—	300	nS
$t_{SU,STO}$	SET-UP TIME FOR STOP CONDITION	4	—	0.6	—	$\mu$ S
$t_{BUF}$	BUS FREE TIME BETWEEN A STOP AND START CONDITION	4.7	—	1.3	—	$\mu$ S

### 5.2.2 I2C BUS TIMING



### 5.2.3 RESET# CIRCUIT NOTICE

THE 3.3V NEEDS TO BE STABLE BEFORE THE RESET SIGNAL IS ACTIVATED AND REACHING 2V AT LEAST 5ms. IF THE RESET SIGNAL IS CONTROLLED BY GPIO, THE DESIGNER HAS TO MAKE SURE THAT THE POWER SEQUENCE IS FOLLOWING SIS'S RECOMMENDATION AS SHOWING IN FIGURE.

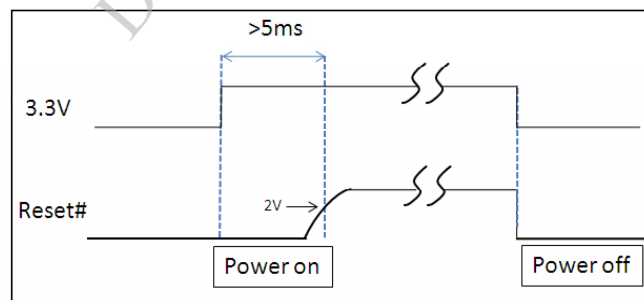


FIGURE POWER ON/OFF SEQUENCE

## 6. OPTICAL CHARACTERISTICS

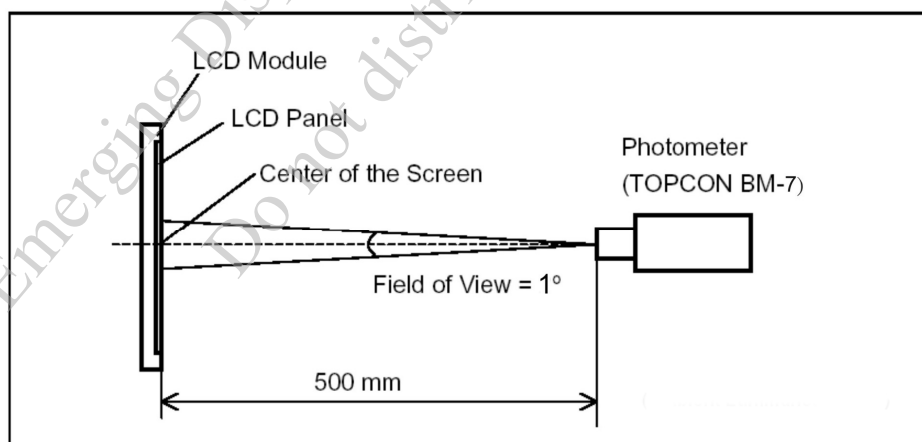
### 6.1 OPTICAL SPECIFICATIONS

Ta = 25 ± 2 °C

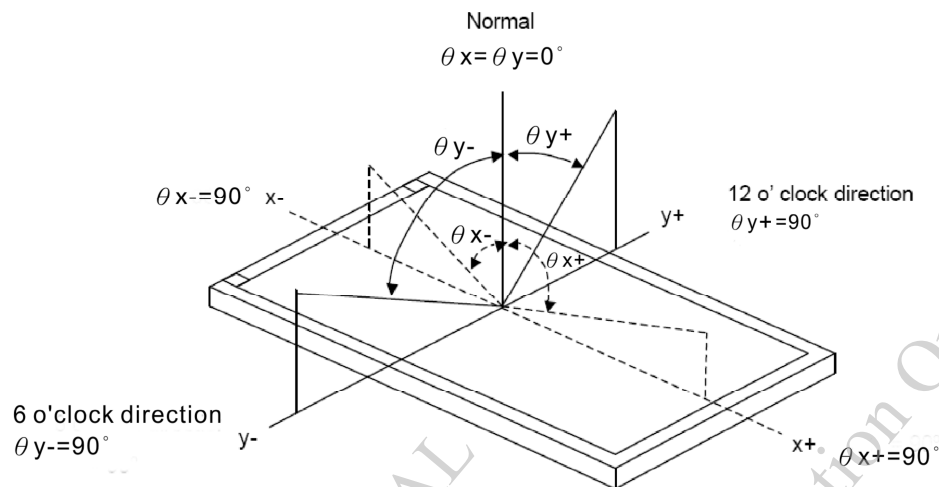
ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT	REMARK
VIEWING ANGLE		$\theta_{y+}$	$CR \geq 10$	$\theta_x=0^\circ$	80	85	—	deg.	NOTE ( 2 ) NOTE ( 3 )
		$\theta_{y-}$			80	85	—		
		$\theta_{x+}$		$\theta_y=0^\circ$	80	85	—		
		$\theta_{x-}$			80	85	—		
CONTRAST RATIO		CR	$\theta_x=0^\circ$ , $\theta_y=0^\circ$		600	800	—	—	NOTE ( 3 )
RESPONSE TIME		$T_R$ ( rise )	$\theta_x=0^\circ$ , $\theta_y=0^\circ$		—	13	20	msec	NOTE ( 4 )
		$T_F$ ( fall )			—	15	25		
COLOR OF CIE COORDINATE	WHITE	$W_x$	$\theta_x=0^\circ$ , $\theta_y=0^\circ$ VDD-VSS=3.3V VLED-VSS=12V NTSC : 47 %		0.26	0.31	0.36	—	NOTE ( 5 )
		$W_y$			0.30	0.35	0.40		
	RED	$R_x$			0.53	0.58	0.63		
		$R_y$			0.31	0.36	0.41		
	GREEN	$G_x$			0.30	0.35	0.40		
		$G_y$			0.53	0.58	0.63		
	BLUE	$B_x$			0.11	0.16	0.21		
		$B_y$			0.06	0.11	0.16		
THE BRIGHTNESS OF MODULE		B	$\theta_x=0^\circ$ , $\theta_y=0^\circ$ VDD-VSS=3.3V VLED-VSS=12V		680	850	—	cd/m <sup>2</sup>	NOTE ( 6 )
THE UNIFORMITY OF MODULE		—			65	70	—	%	

NOTE ( 1 ) : TEST EQUIPMENT SETUP :

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



NOTE ( 2 ) : DEFINITION OF VIEWING ANGLE :

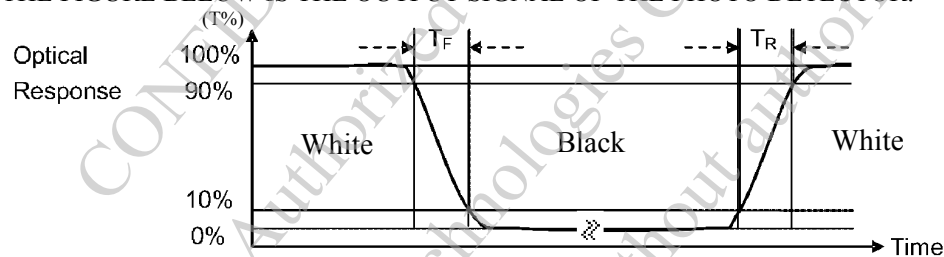


NOTE ( 3 ) : DEFINITION OF CONTRAST RATIO :

$$\text{CONTRAST RATIO (CR)} = \frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$$

NOTE ( 4 ) : DEFINITION OF RESPONSE TIME :  $T_R$  AND  $T_F$

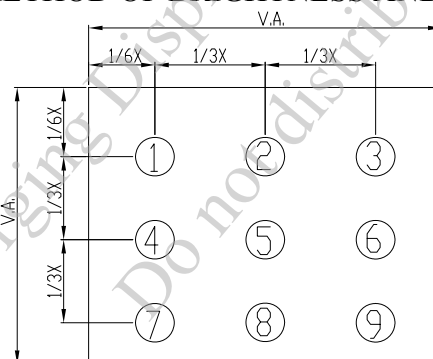
THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE ( 5 ) : THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

NOTE ( 6 ) : BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

## 6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY

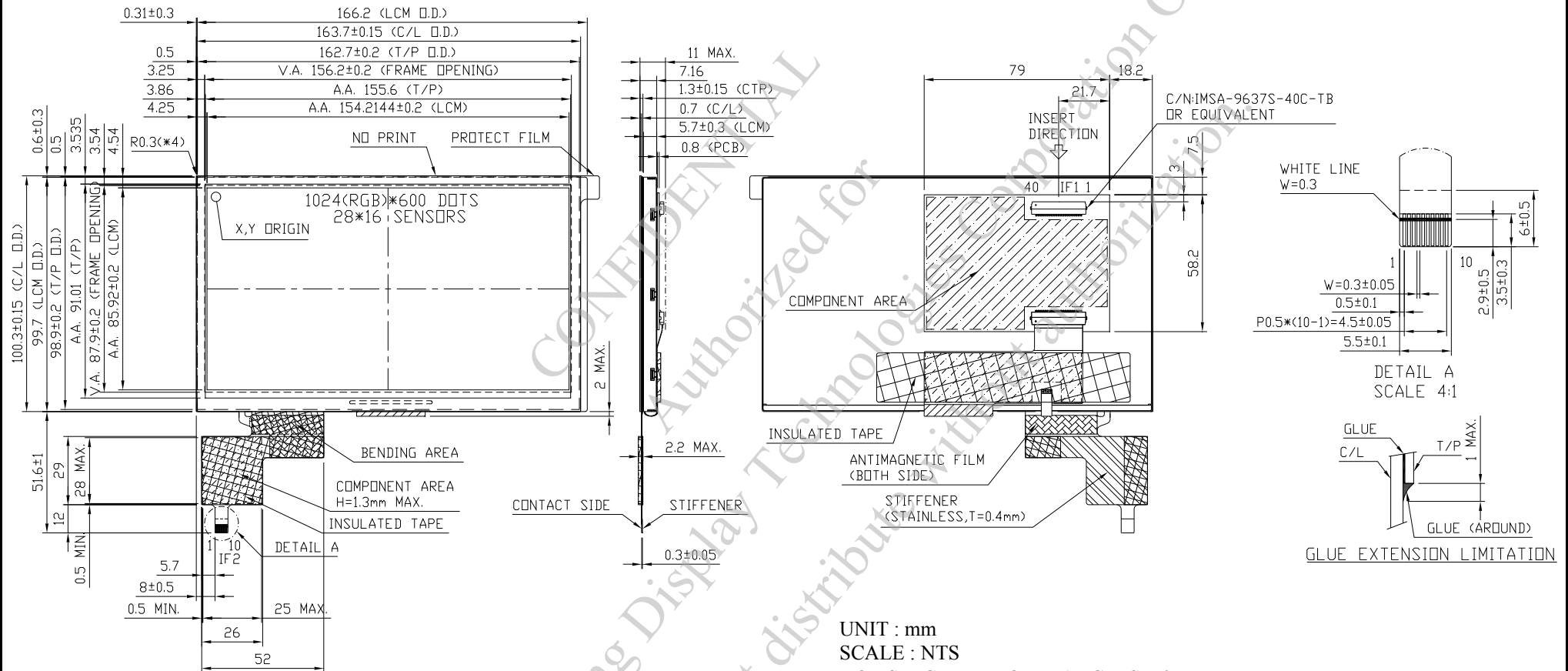


UNIT : mm

## 6.3 THE CALCULATING METHOD OF UNIFORMITY

$$\text{UNIFORMITY} = \left[ 1 - \frac{\text{MAXIMUM BRIGHTNESS} - \text{MINIMUM BRIGHTNESS}}{\text{AVERAGE BRIGHTNESS}} \right] \times 100\%$$

## 7. OUTLINE DIMENSIONS



UNIT : mm

SCALE : NTS

NOT SPECIFIED TOLERANCE IS ± 0.5

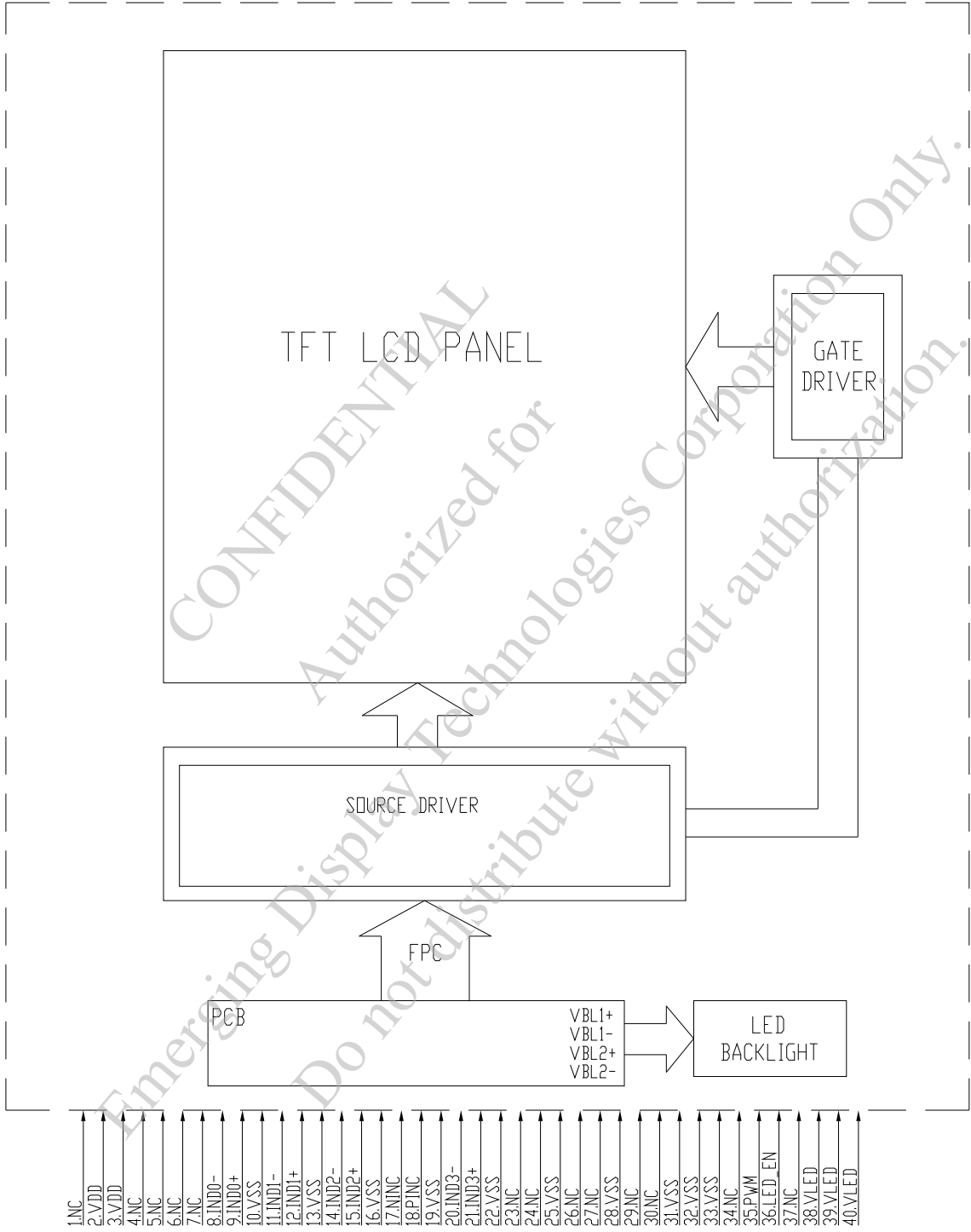
NOTE :

1.C/L GLASS : SODA LIME

2.RECOMMEND IF2 MATCH CONNECTOR KYOCERA : 04 6240 010 SERIES

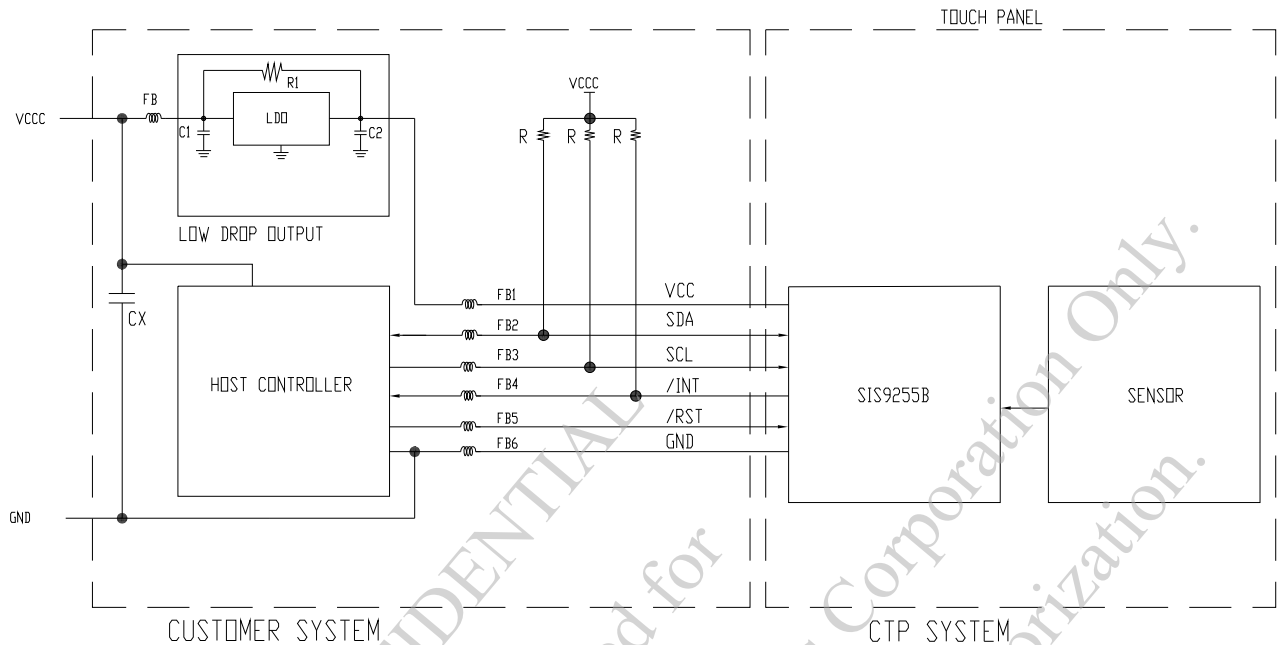
8. BLOCK DIAGRAM

8.1 TFT





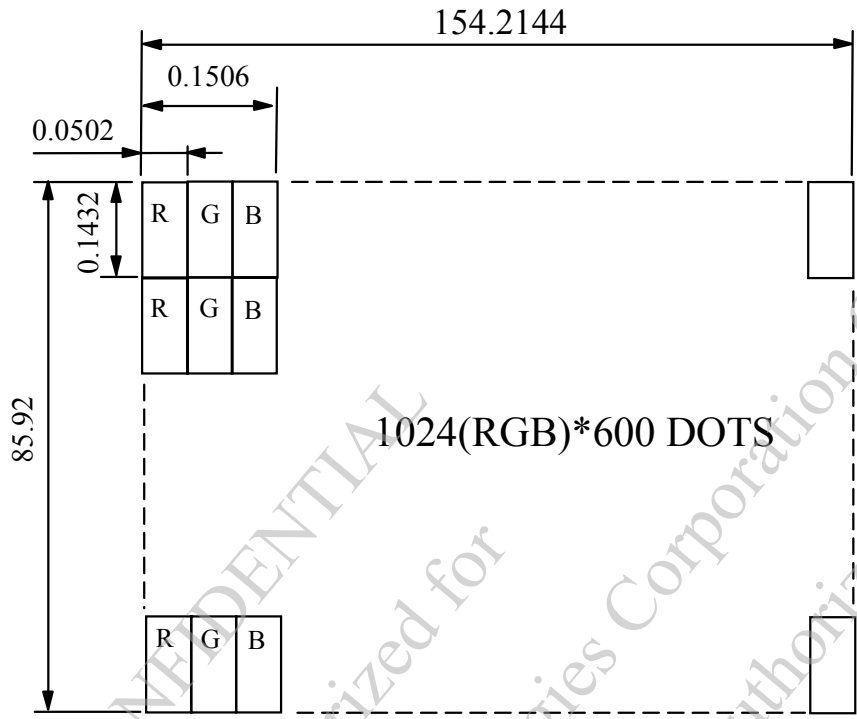
## 8.2 CTP



NOTE ( 1 ) : USE APPROPRIATE RESISTOR VALUE DURING HIGH SPEED SCL CLOCK.  
SUGGEST RESISTOR RECOMMENDATION : 2.2 K ohm.

NOTE ( 2 ) : POWER SUPPLY SHALL BE CLEAN AND NOISE FREE. ADDITIONAL  
FILTERING OR A SEPARATE LDO (LOW DROP OUT) REGULATOR CAN BE  
REQUIRED. C1 AND C2 CAPACITORS RECOMMENDATION: 4.7 $\mu$ F OR 10  $\mu$ F

9. DETAIL DRAWING OF DOT MATRIX



UNIT : mm  
SCALE : NTS  
NOT SPECIFIED TOLERANCE IS  $\pm 0.1$   
DOTS MATRIX TOLERANCE IS  $\pm 0.01$

## 10. INTERFACE SIGNALS

### 10.1 TFT

PIN NO.	SYMBOL	FUNCTION
1	NC	NON CONNECTION
2	VDD	POWER SUPPLY VOLTAGE
3	VDD	POWER SUPPLY VOLTAGE
4	NC	NON CONNECTION
5	NC	NON CONNECTION
6	NC	NON CONNECTION
7	NC	NON CONNECTION
8	IND0-	LVDS SIGNAL (-) — CHANNEL 0 (R0~R5,G0)
9	IND0+	LVDS SIGNAL (+) — CHANNEL 0 (R0~R5,G0)
10	VSS	GROUND
11	IND1-	LVDS SIGNAL (-) — CHANNEL 1 (G1~G5,B0~B1)
12	IND1+	LVDS SIGNAL (+) — CHANNEL 1 (G1~G5,B0~B1)
13	VSS	GROUND
14	IND2-	LVDS SIGNAL (-) — CHANNEL 2 (B2~B5,VS,HS,DE)
15	IND2+	LVDS SIGNAL (+) — CHANNEL 2 (B2~B5,VS,HS,DE)
16	VSS	GROUND
17	NINC	LVDS CLOCK SIGNAL (-)
18	PINC	LVDS CLOCK SIGNAL (+)
19	VSS	GROUND
20	IND3-	LVDS SIGNAL (-) — CHANNEL 3 (R6,R7,G6,G7,B6,B7)
21	IND3+	LVDS SIGNAL (+) — CHANNEL 3 (R6,R7,G6,G7,B6,B7)
22	VSS	GROUND
23	NC	NON CONNECTION
24	NC	NON CONNECTION
25	VSS	GROUND
26	NC	NON CONNECTION
27	NC	NON CONNECTION
28	VSS	GROUND
29	NC	NON CONNECTION
30	NC	NON CONNECTION
31	VSS	GROUND
32	VSS	GROUND
33	VSS	GROUND
34	NC	NON CONNECTION
35	PWM	ADJUST FOR LED BRIGHTNESS
36	LED_EN	LED ENABLE PIN
37	NC	NON CONNECTION
38	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT
39	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT
40	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT

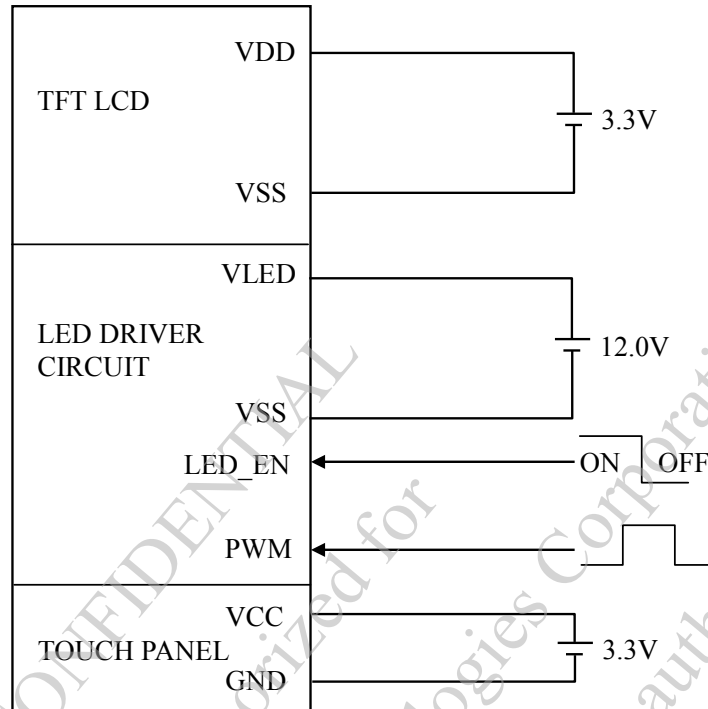
## 10.2 CTP

PIN NO.	SYMBOL	FUNCTION
1	VCC	POWER SUPPLY VOLTAGE(3.3V)
2	RST	EXTERNAL RESET, LOW IS ACTIVE
3	INT	EXTERNAL INTERRUPT TO THE HOST(3.3V)
4	SDA	I2C DATA INPUT AND OUTPUT(3.3V)
5	SCL	I2C CLOCK INPUT(3.3V)
6	GND	GROUND
7	NC	THE PIN WAS RESERVED FOR USB USB GND
8	NC	THE PIN WAS RESERVED FOR USB USB D+
9	NC	THE PIN WAS RESERVED FOR USB USB D-
10	NC	THE PIN WAS RESERVED FOR USB VCC(5V)

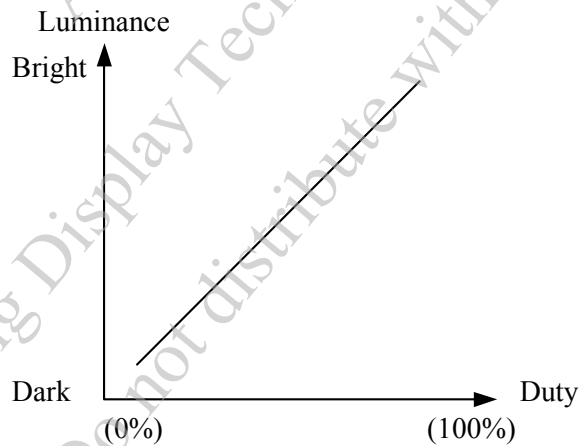
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## 11. POWER SUPPLY

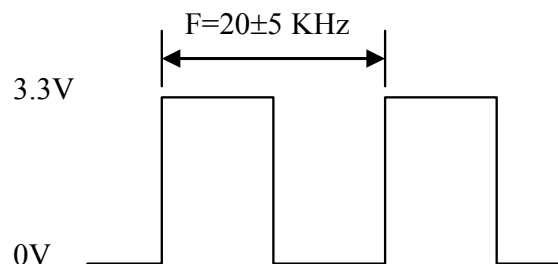
### 11.1 POWER SUPPLY FOR LCM



NOTE ( 1 ) : ADJUST THE PWM SIGNAL IN ORDER TO CONTROL LED BACKLIGHT'S BRIGHTNESS. THE HIGHER THE DUTY CYCLE, THE HIGHER THE BRIGHTNESS



NOTE ( 2 ) : PWM SIGNAL OPERATION FREQUENCY IS  $20 \pm 5$  KHz.



## 12. CAPACITIVE TOUCH PANEL SPECIFICATION

### 12.1 OPTICAL CHARACTERISTICS

ITEM	CONDITION	MIN.	TYP.	MAX.	UNIT
TRANSPARENCY NOTE ( 1 )	$T_a = 25^{\circ}\text{C}$ $\lambda = 550\text{nm}$	85	—	—	%

NOTE ( 1 ) : OPTICAL MEASUREMENT SHOULD BE EXECUTED AFTER PANEL IS SECURED.

MEASUREMENT PROCESS SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.

OPTICAL SPECIFICATIONS SHOULD BE MEASURED BY SPECTROPHOTOMETER.

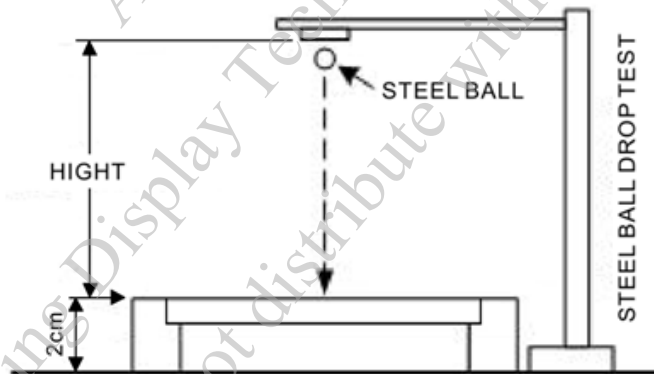
### 12.2 HARDNESS

ITEM	DESCRIPTION
SURFACE HARDNESS	7H (min)

### 12.3 DURABILITY

USING STEEL BALL AND FALLING ON TOUCH PANEL SURFACE, FROM THE HEIGHT MUST PASS BELOW CONDITIONS :

ITEM	CONDITION	INSPECTION METHOD	DESCRIPTION
STEEL BALL DROP TEST	WEIGHT : 67g HEIGHT OF FALL : 30 cm	VISUAL INSPECTION	SIGN OF FRACTURE OR DAMAGE IS NOT ACCEPTABLE 3 TIMES/ 1 POINTS, 25°C(CENTER TEST)



## 12.4 PROTOCOL

SLAVE ADDRESS : 0x5C

	FUNCTION OF I2C BUFFER	STATUS
Byte 0	ADDRESS ID	R
Byte 1	DATA LENGTH LOW	R
Byte 2	DATA LENGTH HIGH	R
Byte 3	REPORT ID (0x10)	R
Byte 4	1st TOUCH STATUS	R
Byte 5	1st TOUCH ID	R
Byte 6	1st X	R
Byte 7	1st X	R
Byte 8	1st Y	R
Byte 9	1st Y	R
Byte 10	2nd TOUCH STATUS	R
Byte 11	2nd TOUCH ID	R
Byte 12	2nd X	R
Byte 13	2nd X	R
Byte 14	2nd Y	R
Byte 15	2nd Y	R
...	...	R
...	...	R
Byte 55	10th TOUCH STATUS	R
Byte 56	10th TOUCH ID	R
Byte 57	10th X	R
Byte 58	10th X	R
Byte 59	10th Y	R
Byte 60	10th Y	R
Byte 61	CRC CHECK	R
Byte 62	CRC CHECK	R

### 13. INSPECTION CRITERION

#### 13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.( E.D.T ) TO CUSTOMERS

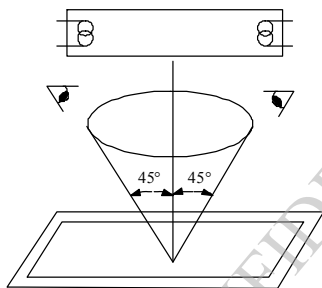
#### 13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE :  $35 \pm 5\text{cm}$

(2)VIEW ANGLE :  $\pm 45^\circ$

PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN  $45^\circ$



LINE OF SIGHT FOR INSPECTION SHALL BE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT  $45^\circ$  WITH RESPECTS TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE CONE AXIS MUST BE PERPENDICULAR NORMAL TO LCD SURFACE AND PASSES THROUGH THE FLUORESCENT LAMP.

#### 13.2.2 ENVIRONMENT CONDITIONS :

AMBIENT TEMPERATURE		$25 \pm 5^\circ\text{C}$
AMBIENT HUMIDITY		$65 \pm 20\%\text{RH}$
AMBIENT ILLUMINATION	COSMETIC INSPECTION	600~800 lux
	FUNCTIONAL INSPECTION	300~500 lux
INSPECTION TIME		15 secs

#### 13.2.3 INSPECTION LOT

QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD :

MIL-STD-105E LEVEL II

NORMAL INSPECTION, SINGLE SAMPLING

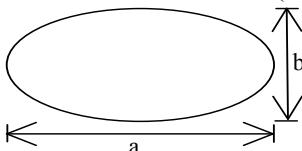
(b)AQL : MAJOR DEFECT : AQL 0.65

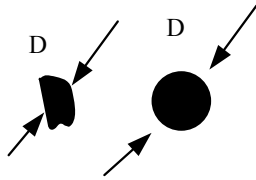
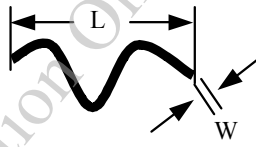
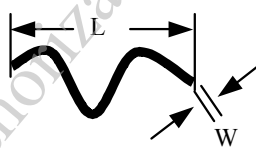
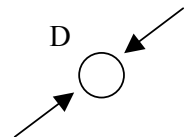
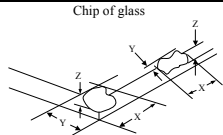
MINOR DEFECT : AQL 1.0

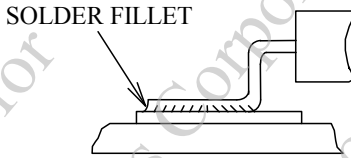
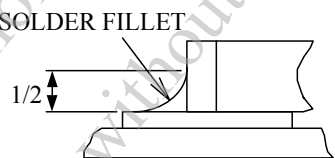
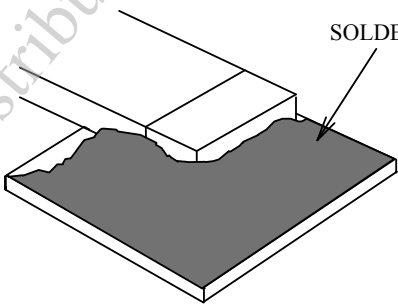
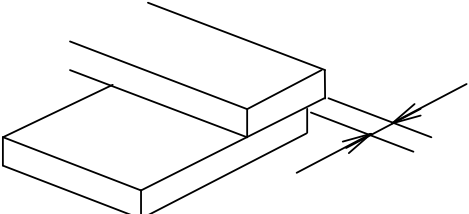


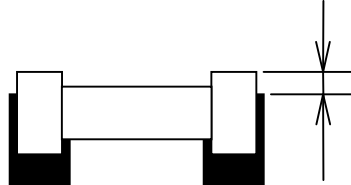
### 13.3 DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
MAJOR DEFECT	1.DISPLAY ON	<ul style="list-style-type: none"> <li>• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS</li> <li>EX: DISCONNECTION, SHORT CIRCUIT ETC</li> </ul>	0.65
	2.CTP FUNCTION	<ul style="list-style-type: none"> <li>• NO FUNCTION</li> <li>• BROKEN LINE</li> <li>• FALSE TOUCH</li> </ul>	
	3.BACKLIGHT	<ul style="list-style-type: none"> <li>• NO LIGHT</li> <li>• FLICKERING AND OTHER ABNORMAL ILLUMINATION</li> </ul>	
	4.DIMENSIONS	<ul style="list-style-type: none"> <li>• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS</li> </ul>	
MINOR DEFECT	1.DISPLAY ZONE (VIEWING AREA)	<ul style="list-style-type: none"> <li>• BLACK/WHITE SPOT / CIRCULAR TYPE</li> <li>• BUBBLES ON POLARIZER</li> <li>• NEWTON RING</li> <li>• BLACK/WHITE LINE / LINEAR TYPE</li> <li>• SCRATCH</li> <li>• CONTAMINATION</li> <li>• UNEVEN COLOR SPREAD</li> </ul>	1.0
	2.BEZEL ZONE	<ul style="list-style-type: none"> <li>• STAINS</li> <li>• SCRATCHES</li> <li>• FOREIGN MATTER</li> </ul>	
	3.SOLDERING	<ul style="list-style-type: none"> <li>• INSUFFICIENT SOLDER</li> <li>• SOLDERED IN INCORRECT POSITION</li> <li>• CONVEX SOLDERING SPOT</li> <li>• SOLDER BALLS</li> <li>• SOLDER SCRAPS</li> </ul>	
	4.DISPLAY ON (ALL ON)	<ul style="list-style-type: none"> <li>• LIGHT LINE</li> </ul>	

NO.	ITEM	CRITERIA																				
1	DISPLAY ON INSPECTION	1. INCORRECT PATTERN 2. MISSING SEGMENT 3. DIM SEGMENT 4. OPERATING VOLTAGE BEYOND SPEC																				
2	OVERALL DIMENSIONS	1. OVERALL DIMENSION BEYOND SPEC																				
3	DOT DEFECT	<div>1. INSPECTION PATTERN: FULL WHITE, FULL BLACK, RED, GREEN AND BLUE SCREENS.</div> <div>2.<table><tr><th>ITEMS</th><th>PERMISSIBLE NO.</th></tr><tr><td>BRIGHT DOT</td><td><math>N \leq 1</math></td></tr><tr><td>DARK DOT</td><td><math>N \leq 4</math></td></tr><tr><td>TOTAL BRIGHT AND DARK DOTS</td><td><math>N \leq 5</math></td></tr></table></div> <div>NOTE : (1)THE DEFINITION OF DOT : THE SIZE OF A DEFECTIVE DOT OVER 1/2 OF WHOLE DOT IS REGARDED AS ONE DEFECTIVE DOT. (2)BRIGHT DOT : DOTS APPEAR BRIGHT AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER BLACK PATTERN. THE BRIGHT DOT DEFECT MUST BE VISIBLE THROUGH 2% ND FILTER. (3)DARK DOT : DOTS APPEAR DARK AND UNCHANGED IN SIZE IN WHICH LCD PANEL IS DISPLAYING UNDER PURE RED, GREEN, BLUE PICTURE.</div>	ITEMS	PERMISSIBLE NO.	BRIGHT DOT	$N \leq 1$	DARK DOT	$N \leq 4$	TOTAL BRIGHT AND DARK DOTS	$N \leq 5$												
ITEMS	PERMISSIBLE NO.																					
BRIGHT DOT	$N \leq 1$																					
DARK DOT	$N \leq 4$																					
TOTAL BRIGHT AND DARK DOTS	$N \leq 5$																					
4	BUBBLES ON POLARIZER /SURFACE STAINS /DIRT/CF FAIL/SPOT	<table><tr><td></td><td>AVERAGE DIAMETER (mm) : D</td><td>PERMISSIBLE NO.</td></tr><tr><td rowspan="3">BUBBLE ON POLARIZER</td><td><math>D \leq 0.25</math></td><td>IGNORE</td></tr><tr><td><math>0.25 &lt; D \leq 0.5</math></td><td><math>N \leq 5</math></td></tr><tr><td><math>0.5 &lt; D</math></td><td>0</td></tr><tr><td rowspan="2">SURFACE STAINS / DIRT ON POLARIZER</td><td><math>D &lt; 0.1</math></td><td>IGNORE</td></tr><tr><td><math>0.1 &lt; D \leq 0.3</math></td><td><math>N \leq 3</math></td></tr><tr><td rowspan="2">CF FAIL / SPOT</td><td><math>D &lt; 0.1</math></td><td>IGNORE</td></tr><tr><td><math>0.1 &lt; D \leq 0.3</math></td><td><math>N \leq 3</math></td></tr></table> <div>NOTE : (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEIOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2</div> <div></div>		AVERAGE DIAMETER (mm) : D	PERMISSIBLE NO.	BUBBLE ON POLARIZER	$D \leq 0.25$	IGNORE	$0.25 < D \leq 0.5$	$N \leq 5$	$0.5 < D$	0	SURFACE STAINS / DIRT ON POLARIZER	$D < 0.1$	IGNORE	$0.1 < D \leq 0.3$	$N \leq 3$	CF FAIL / SPOT	$D < 0.1$	IGNORE	$0.1 < D \leq 0.3$	$N \leq 3$
	AVERAGE DIAMETER (mm) : D	PERMISSIBLE NO.																				
BUBBLE ON POLARIZER	$D \leq 0.25$	IGNORE																				
	$0.25 < D \leq 0.5$	$N \leq 5$																				
	$0.5 < D$	0																				
SURFACE STAINS / DIRT ON POLARIZER	$D < 0.1$	IGNORE																				
	$0.1 < D \leq 0.3$	$N \leq 3$																				
CF FAIL / SPOT	$D < 0.1$	IGNORE																				
	$0.1 < D \leq 0.3$	$N \leq 3$																				

NO.	ITEM	CRITERIA											
5	BLACK/WHITE SPOT CIRCULAR TYPE	<div>THE FOLLOWING BLACK/WHITE SPOT ARE WITHIN THE VIEWING AREA. AVERAGE DIAMETER : D (mm)</div> <table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td><math>D \leq 0.1</math></td><td>IGNORE</td></tr><tr><td><math>0.1 &lt; D \leq 0.3</math></td><td>5</td></tr><tr><td><math>0.3 &lt; D \leq 0.5</math></td><td>5</td></tr><tr><td><math>D &gt; 0.5</math></td><td>0</td></tr></table> <div>NOTE ( 1 ) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</div>	SIZE D	PERMISSIBLE NO.	$D \leq 0.1$	IGNORE	$0.1 < D \leq 0.3$	5	$0.3 < D \leq 0.5$	5	$D > 0.5$	0	
SIZE D	PERMISSIBLE NO.												
$D \leq 0.1$	IGNORE												
$0.1 < D \leq 0.3$	5												
$0.3 < D \leq 0.5$	5												
$D > 0.5$	0												
6	SCRATCH	<div>THE FOLLOWING SCRATCH IS WITHIN THE VIEWING AREA. WIDTH : W (mm) , LENGTH : L (mm)</div> <table><tr><th>SIZE W &amp; L</th><th>PERMISSIBLE NO.</th></tr><tr><td><math>W \leq 0.05</math></td><td>IGNORE</td></tr><tr><td><math>0.05 &lt; W \leq 0.08, L \leq 8</math></td><td>3</td></tr><tr><td><math>0.08 &lt; W \leq 0.1, L \leq 5</math></td><td>2</td></tr><tr><td><math>W &gt; 0.1</math></td><td>0</td></tr></table> <div>NOTE ( 1 ) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</div>	SIZE W & L	PERMISSIBLE NO.	$W \leq 0.05$	IGNORE	$0.05 < W \leq 0.08, L \leq 8$	3	$0.08 < W \leq 0.1, L \leq 5$	2	$W > 0.1$	0	
SIZE W & L	PERMISSIBLE NO.												
$W \leq 0.05$	IGNORE												
$0.05 < W \leq 0.08, L \leq 8$	3												
$0.08 < W \leq 0.1, L \leq 5$	2												
$W > 0.1$	0												
7	BLACK / WHITE LINE LINEAR TYPE / FOREIGN FIBER	<div>THE FOLLOWING BLACK LINE, WHITE LINE IS WITHIN THE VIEWING AREA. WIDTH : W (mm) , LENGTH : L (mm)</div> <table><tr><th>SIZE W &amp; L</th><th>PERMISSIBLE NO.</th></tr><tr><td><math>W \leq 0.05</math></td><td>IGNORE</td></tr><tr><td><math>0.05 &lt; W \leq 0.08, L \leq 8</math></td><td>3</td></tr><tr><td><math>0.08 &lt; W \leq 0.1, L \leq 5</math></td><td>2</td></tr><tr><td><math>W &gt; 0.1</math></td><td>0</td></tr></table> <div>NOTE ( 1 ) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</div>	SIZE W & L	PERMISSIBLE NO.	$W \leq 0.05$	IGNORE	$0.05 < W \leq 0.08, L \leq 8$	3	$0.08 < W \leq 0.1, L \leq 5$	2	$W > 0.1$	0	
SIZE W & L	PERMISSIBLE NO.												
$W \leq 0.05$	IGNORE												
$0.05 < W \leq 0.08, L \leq 8$	3												
$0.08 < W \leq 0.1, L \leq 5$	2												
$W > 0.1$	0												
8	BUBBLE / DENT FOR OPTICAL BONDING	<div>BUBBLES WITHIN VIEWING AREA. AVERAGE DIAMETER : D (mm)</div> <table><tr><th>SIZE D</th><th>PERMISSIBLE NO.</th></tr><tr><td><math>D \leq 0.2</math></td><td>IGNORE</td></tr><tr><td><math>0.2 &lt; D \leq 0.3</math></td><td>3</td></tr><tr><td><math>0.3 &lt; D \leq 0.5</math></td><td>1</td></tr><tr><td><math>D &gt; 0.5</math></td><td>0</td></tr></table> <div>NOTE ( 1 ) : THE DISTANCE BETWEEN DEFECTS SHOULD BE MORE THAN 10mm APART.</div>	SIZE D	PERMISSIBLE NO.	$D \leq 0.2$	IGNORE	$0.2 < D \leq 0.3$	3	$0.3 < D \leq 0.5$	1	$D > 0.5$	0	
SIZE D	PERMISSIBLE NO.												
$D \leq 0.2$	IGNORE												
$0.2 < D \leq 0.3$	3												
$0.3 < D \leq 0.5$	1												
$D > 0.5$	0												
9	CHIPPING	<table><tr><td>CORNER</td><td><math>X \leq 3\text{mm} \cdot Y \leq 3\text{mm} \cdot Z \leq t</math> (t : THICKNESS)</td></tr><tr><td>EDGE</td><td><math>X \leq 6\text{mm} \cdot Y \leq 1\text{mm} \cdot Z &lt; t</math> (t : THICKNESS)</td></tr></table>	CORNER	$X \leq 3\text{mm} \cdot Y \leq 3\text{mm} \cdot Z \leq t$ (t : THICKNESS)	EDGE	$X \leq 6\text{mm} \cdot Y \leq 1\text{mm} \cdot Z < t$ (t : THICKNESS)	<div>Chip of glass</div> 						
CORNER	$X \leq 3\text{mm} \cdot Y \leq 3\text{mm} \cdot Z \leq t$ (t : THICKNESS)												
EDGE	$X \leq 6\text{mm} \cdot Y \leq 1\text{mm} \cdot Z < t$ (t : THICKNESS)												
10	CRACKED GLASS	NOT ACCEPTABLE											
11	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED.											
12	MURA ON DISPLAY	IT'S ACCEPTABLE, IF MURA IS SLIGHT VISIBLE THROUGH 5% ND FILTER.											
13	UNEVEN COLOR SPREAD, COLORATION	TO BE DETERMINED BASED UPON THE LIMITED SAMPLE.											
14	BEZEL APPEARANCE	1. BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. 2. BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.											

NO.	ITEM	CRITERIA
15	PCB	<ol style="list-style-type: none"> <li>1. THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES.</li> <li>2. NO OXIDATION OR CONTAMINATION ON PCB TERMINALS.</li> <li>3. PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS.</li> <li>4. THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART.</li> <li>5. IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.</li> </ol>
16	SOLDERING	<ol style="list-style-type: none"> <li>1. NO SOLDERING FOUND ON THE SPECIFIED PLACE</li> <li>2. INSUFFICIENT SOLDER <ol style="list-style-type: none"> <li>(a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD   </li> <li>(b)CHIP COMPONENT  <ul style="list-style-type: none"> <li>· SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING</li> </ul>  <ul style="list-style-type: none"> <li>· SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED</li> </ul>  </li> </ol> </li> <li>3. PARTS ALIGNMENT <ol style="list-style-type: none"> <li>(a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE   </li> </ol> </li> </ol>

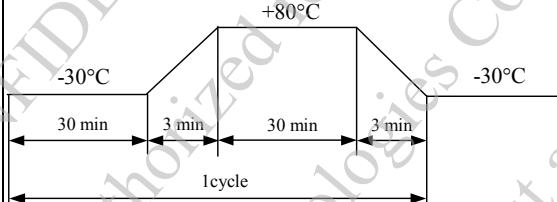
NO.	ITEM	CRITERIA
16	SOLDERING	<p>(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE</p>  <p>4. NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. 5. NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. 6. NO RESIDUE OR SOLDER BALLS ON PCB. 7. NO SHORT CIRCUITS IN COMPONENTS ON PCB.</p>
17	BACKLIGHT	<p>1. NO LIGHT 2. FLICKERING AND OTHER ABNORMAL ILLUMINATION 3. SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. 4. BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.</p>
18	GENERAL APPEARANCE	<p>1. NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. 2. NO CRACKS ON INTERFACE PIN (OLB) OF TCP. 3. NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. 4. THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. 5. THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. 6. THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. 7. SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. 8. PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. 9. LCD PIN LOOSE OR MISSING PINS. 10. PRODUCT PACKAGING MUST BE THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. 11. PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. 12. THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.</p>

NOTE :

1. FOR ANY SPOTS OR LINES, WHICH ARE NOT OBSERVED UNDER APPROPRIATE PANEL OPERATING CONDITION ARE DEEMED ACCEPTABLE.
2. THE FOREIGN MATERIALS THAT CAN BE BLOWN OUT BY AIR AND REMOVED BY WET CLEANING ARE NOT REGARDED AS DEFECTS.

## 14. RELIABILITY TEST

### 14.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMP STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMPERATURE /HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	<p>THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION:</p> 
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	<p>AIR DISCHARGE <math>\pm 12\text{KV}</math>  CONTACT DISCHARGE <math>\pm 8\text{KV}</math>  ACCORDING TO IEC-61000-4-2</p>

NOTE ( 1 ) : THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION FAILURE ISSUE OCCURRED.

### 14.2 TESTING CONDITIONS AND INSPECTION CRITERIA

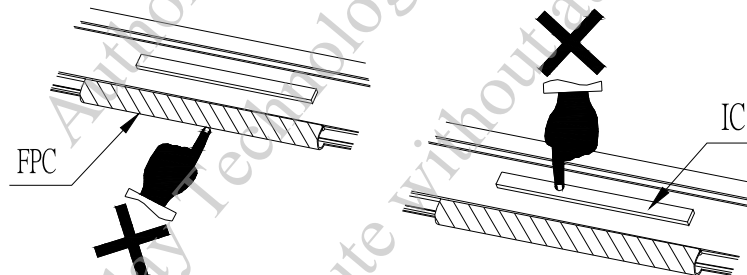
FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 14.1, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

## 15. PRECAUTION

### 15.1 OPERATION

- 15.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 15.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 15.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 15.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE .  
IF ABOVE SEQUENCE IS NOT FOLLOWED , CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM .
- 15.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!  
DO NOT STRESS FPC AND IC ON THE MODULE!



## 15.2 HANDLING

- 15.2.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 15.2.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 15.2.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL .
- 15.2.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 15.2.5 DON'T GIVE EXTERNAL SHOCK.
- 15.2.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 15.2.7 LIQUID CRISTAL IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.  
WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC.  
WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 15.2.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 15.2.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 15.2.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 15.2.11 REWIRING: NO MORE THAN 3 TIMES.