AMINED BY:		FILE NO . CAS-0008797
Evin kuo	EMERGING DISPLAY	ISSUE : SEP.15, 2020
ROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 26
This the		VERSION: 2
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
FOR	ODEL NO.: ETML070016NDHA (RoHS) MESSRS:	A STATE OF THE STA

MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION E T M L 0 7 0 0 1 6 N D H A 0 - 1DOC . FIRST ISSUE APR.16, 2019 RECORDS OF REVISION REVISED DATE **PAGE** SUMMARY NO. SEP.15, 2020 11.1 POWER SUPPLY FOR LCM 16 NOTE (2): PWM SIGNAL OPERATION FREQUENCY IS 100~10KHz→ THE OPERATION FREQUENCY OF PWM SIGNAL IS 100~10KHz F=100~10KHz 5.0V

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- 1. GENERAL SPECIFICATIONS
- 1.1 DATA SHEETS FOR CONTROLLER/DRIVER PLEASE REFER TO:

SITRONIX ST5651CB SITRONIX ST5021CA

1.2 APPLICATION NOTES FOR CAPACITIVE TOUCH PANEL CONTROLLER/DRIVER PLEASE REFER TO:

ILITEK ILI2511

- 1.3 MATERIAL SAFETY DESCRIPTION
 ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS,
 INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD,
 MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED
 BIPHENYLS (PBB) AND POLYBROMINATED
 DIPHENYL ETHERS (PBDE), BIS(2-ETHYLHEXYL) PHTHALATE (DEHP), BUTYL
 BENZYL PHTHALATE (BBP), DIBUTYL PHTHALATE (DBP), DIISOBUTYL
 PHTHALATE (DIBP).
- 2. MECHANICAL SPECIFICATIONS
 - 2.1 LCD MODULE MECHANICAL SPECIFICATIONS

(1) DISPLAY SIZE	7 inch
(2) NUMBER OF DOTS	1024W * (RGB) * 600H DOTS
(3) MODULE SIZE	166.2W * 100.3H * 11.0D (MAX.) mm
7.0	(NOT INCLUDED FPC & PROTECT FILM)
(4) VIEWING AREA	155.6W * 87.9H mm
(5) ACTIVE AREA	154.2144W * 85.92H mm
(6) DOT SIZE	0.0502W * 0.1432H mm
(7) PIXEL SIZE	0.1506W * 0.1432H mm
(8) LCD TYPE	TFT , TRANSMISSIVE ,
	NORMALLY BLACK, ANTI-GLARE
(9) COLOR	16.7M
(10) VIEWING DIRECTION	SUPER WIDE VIEW
(11) BACK LIGHT	LED , COLOR : WHITE
(12) INTERFACE MODE	LVDS

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2.2 CAPACITIVE TOUCH PANEL MECHANICAL SPECIFICATIONS

(1) TOUCH PANEL SIZE	7 inch
(2) OUTER DIMENSION	163.7W * 100.3H * 1.3D mm (NOT INCLUDED FPC)
(3) VIEWING AREA	
(4) ACTIVE AREA	155.6W * 91.01H mm
(5) INPUT TYPE	MULTI-TOUCH
(6) NUMBER OF TOUCH SENSOR	28*16 SENSORS
(7) INTERFACE MODE	USB
(8) RESOLUTION	16384*9600
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3. ABSOLUTE MAXIMUM RATINGS

3.1 LCD MODULE ELECTRICAL ABSOLUTE MAXIMUM RATINGS

NOTE (1)

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD	-0.5	6.5	V	
INPUT VOLTAGE	VIN	- 0.3	VDD+0.3	V	

3.2 CAPACITIVE TOUCH PANEL ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VCC	-0.3	6	V	\sim

3.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STOR	RAGE	- REMARK	
HEM	MIN. MAX.		MIN.	MAX.		
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE (1), (2)	
HUMIDITY	NOTE(3)		NOTE(3)		WITHOUT CONDENSATION	
VIBRATION	- 6	2.45 m/s ² (0.25 G)		4.9 m/s^2 (0.5 G)	10~55Hz , X,Y,Z EACH 2HRS	
SHOCK	A. J.	29.4 m/s ² (3 G)	5 - -	490 m/s ² (50 G)	10 ms XYZ DIRECTIONS 1 TIME EACH	
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE			

NOTE (1): Ta AT -30°C: WILL BE 48HRS MAX.

80°C: WILL BE 48HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3): $Ta \le 60^{\circ}C : 90\% RH MAX. (96HRS MAX).$

Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C (48HRS MAX).

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4. ELECTRICAL CHARACTERISTICS

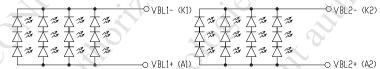
4.1 TFT MODULE ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$

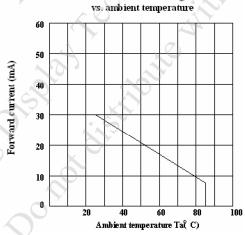
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS		3.15	3.3	3.45	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VLED-VSS		4.7	5	5.3	V	4.
LOW LEVEL INPUT VOLTAGE	VIL	_	0	_	0.3*VDD	V	NOTE (1)
HIGH LEVEL INPUT VOLTAGE	VIH		0.7*VDD		VDD	V	NOTE (1)
POWER SUPPLY CURRENT	IDD	VDD-VSS=3.3V		130	160	mA	NOTE (2)
POWER SUPPLY CURRENT FOR LED DRIVER	ILED	VLED-VSS=5V LED B/L=ON		350	400	mA	OC.
LED LIFE TIME		IF=20mA PER LED	50K	-0	_	hrs	NOTE (4) NOTE (5)

NOTE (1): THE DISPLAY PATTERN IS ALL "BLACK".

NOTE (2): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (3) : AMBIENT TEMP .VS. ALLOWABLE FORWARD CURRENT (PER LED) Forward current derating curve



NOTE (4): CONDITIONS; TA=25 °C, CONTINUOUS LIGHTING.

NOTE (5): DEFINITIONS OF FAILURE

LCD LUMINANCE BECOMES HALF OF THE INITIAL VALUE.

4.2 CAPACITIVE TOUCH PANEL ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$

	_	_				1a – 23 C
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
POWER SUPPLY FOR DRIVER	VCC	_	4.75	5.0	5.25	V
POWER SUPPLY CURRENT	ICC	VCC-GND		(100)	(130)	A
CONSUMPTION FOR OPERATION	icc	=5.0V		(100)	(130)	mA

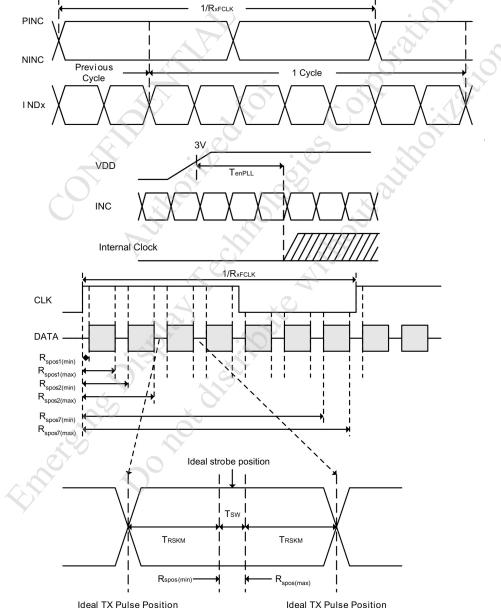
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5. TIMING CHARACTERISTICS

5.1 FOR TFT MODULE

5.1.1 AC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
CLOCK FREQUENCY	RxFCLK		20	_	71	MHz
INPUT DATA SKEW MARGIN	Trskm	$ V_{ID} = 400 mV$ $R_{xVCM} = 1.2V$ $R_{xFCLK} = 71 \text{ MHz}$	500		_	pS
CLOCK HIGH TIME	TLVCH			4/(7* Rxfclk)	-2	ns
CLOCK LOW TIME	TLVCL			3/(7* Rxfclk)		ns
PLL WAKE-UP TIME	TenPLL				150	μs



TRSKM: RECEIVER STROBE MARGIN RSPOS: RECEIVER STROBE POSITION

 $T_{SW}: STROBE\ WIDTH\ (INTERNAL\ DATA\ SAMPLING\ WINDOW)$

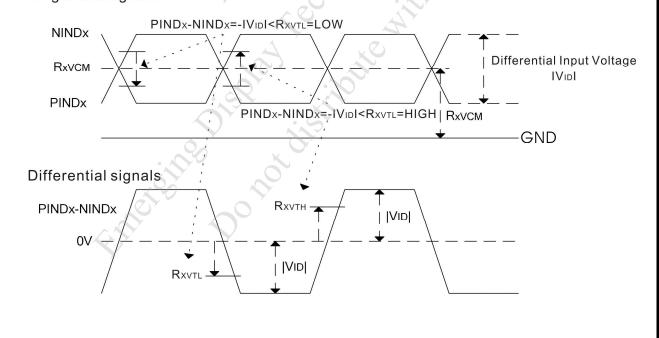
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PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
MODULATION FREQUENCY	SSCMF	_	23	_	93	KHz
MODULATION RATE	SSCMR	LVDS clock = 71MHz CENTER SPREAD	_	_	±3	%

5.1.2 DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
DIFFERENTIAL INPUT HIGH THRESHOLD VOLTAGE	R_{xVTH}			+0.1	V	D -1 2V
DIFFERENTIAL INPUT LOW THRESHOLD VOLTAGE	R_{xVTL}	-0.1	_		V	R _{XVCM} =1.2V
INPUT VOLTAGE RANGE (SINGLED-END)	R_{xVIN}	0		2.4	V	Y
DIFFERENTIAL INPUT COMMON MODE VOLTAGE	R _{xVCM}	V _{ID} /2	_	2.4- V _{ID} /2	V	. 67.
DIFFERENTIAL VOLTAGE	$ V_{ID} $	0.2	_	0.6	V	XX
DIFFERENTIAL INPUT LEAKAGE CURRENT	RVxliz	-10	, <u> </u>	+10	μΑ	
LVDS DIGITAL OPERATING CURRENT	LDDLVDS	100	40	50	mA	FCLK=65MHz VDD=3.3V
LVDS DIGITAL STAND-BY CURRENT	LSTLVDS	- 20	10	50	μΑ	CLOCK & ALL FUNCTIONS ARE STOPPED

Single end signals

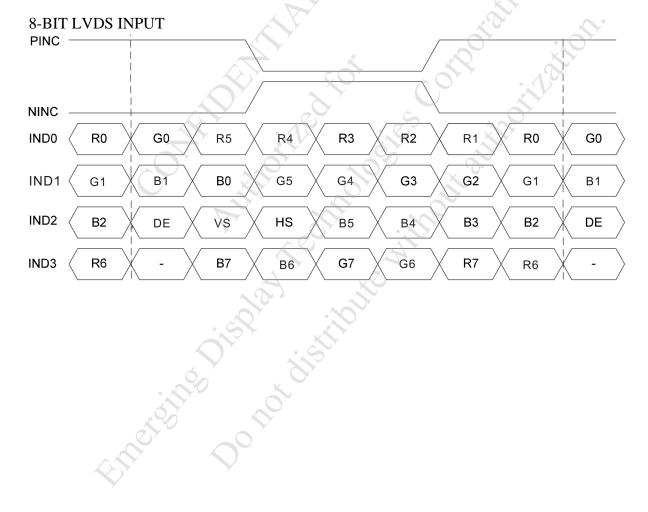


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5.1.3 TIMING

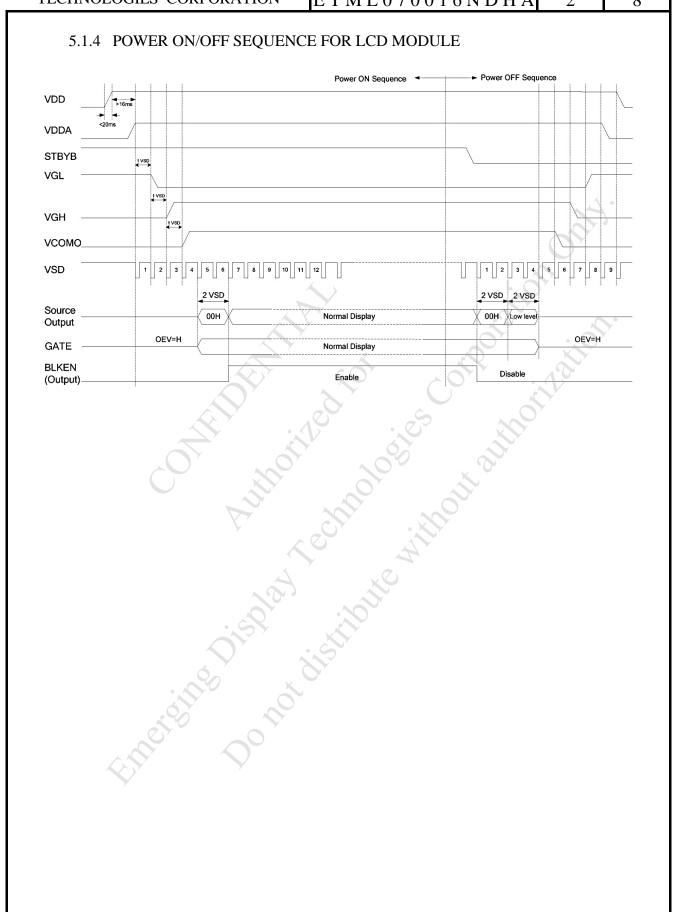
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	REMARK
CLOCK FREQUENCY	fclk	40.8	51.2	67.2	MHz	FRAME RATE=60Hz
HORIZONTAL DISPLAY AREA	thd		1024		DCLK	
HS PERIOD TIME	th	1114	1344	1400	DCLK	
HS BLANKING	thb	90	320	376	DCLK	,
VERTICAL DISPLAY AREA	tvd		600		Н	14.
VS PERIOD TIME	tv	610	635	800	Н	
VS BLANKING	tvd +tvfp	10	35	200	Н	

DATA INPUT FORMAT



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6. OPTICAL CHARACTERISTICS

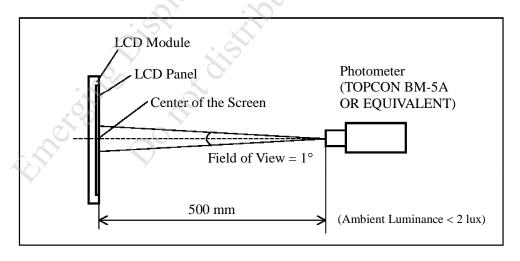
6.1 OPTICAL SPECIFICATIONS

 $Ta = 25 \pm 2$ °C

ITEM		SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK
		θ_{y+}		0 00	80	85	—		
VIEWING ANGLE		$\theta_{ ext{y-}}$	CR ≥ 10	$\theta_x=0^{\circ}$	80	85		dog	NOTE(2)
VIEWING ANGLE		θ_{x+}	CR ≥ 10	0 -00	80	85		deg.	NOIE(2)
		θ_{x}		θ _y =0°	80	85			1.
CONTRAST RATIO (CENTER))	CR	θx=0°,	θу=0°	600	800			NOTE (3)
RESPONSE TIME		T _R (rise)	000	000		13	20	maga	NOTE (4)
RESPONSE TIME		T _F (fall)	θx=0°,	θy=0°	_	15	25	msec	NOIE (4)
	WHITE	Wx			0.26	0.31	0.36		
	WIIIIE	Wy			0.28	0.33	0.38		₩.
GOL OB	RED	Rx	Y		0.53	0.58	0.63	Ů.A	O
COLOR CHROMATICITY	KED	Ry			0.31	0.36	0.41	100	NOTE (5)
(CENTER)	GREEN	Gx	θx=0°,	θv=0°	0.30	0.35	0.40		NOIE (3)
	GREEN	Gy	VDD-V	SS=3.3V	0.53	0.58	0.63		
	BLUE	Bx		VSS=5V	0.11	0.16	0.21		
	BLUE	Ву	NTSC	:4/%	0.06	0.11	0.16		
THE BRIGHTNESS		L X	O'	10	340	425	0	cd/m ²	NOTE (6)
OF MODULE (CEN	OF MODULE (CENTER)		7	20 ²	340	425		Cu/III	MOIE (0)
THE UNIFORMITY OF		A 3	^		70	75	_	%	NOTE (7)
MODULE		<i>></i>) Y		Y		, ,	()

NOTE (1): TEST CONDITION:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.



EMERGING DISPLAY

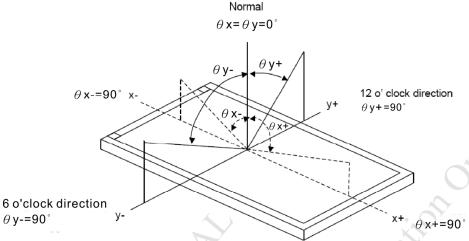
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NOTE (2): DEFINITION OF VIEWING ANGLE:



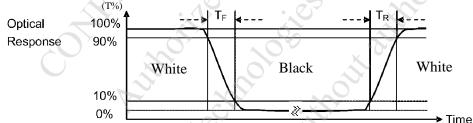
NOTE (3): DEFINITION OF CONTRAST RATIO (CR):

MEASURED AT THE CENTER POINT OF MODULE

 $CONTRAST \ \ RATIO(CR) = \frac{BRIGHTNESS \ MEASURED \ WHEN \ LCD \ IS \ AT \ "WHITE \ STATE"}{BRIGHTNESS \ MEASURED \ WHEN \ LCD \ IS \ AT \ "BLACK \ STATE"}$

NOTE (4) : DEFINITION OF RESPONSE TIME : T_R AND T_F

THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



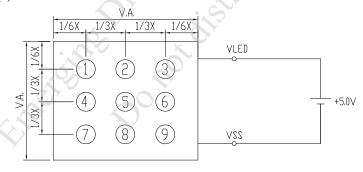
NOTE (5): DEFINITION OF COLOR CHROMATICITY

(a)100% RGB PIXEL DATA TRANSMISSION WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY POWERED ON.

(b)MEASURED AT THE CENTER POINT OF MODULE

NOTE (6): MEASURED THE BRIGHTNESS OF WHITE STATE AT CENTER POINT.

NOTE (7): (a) DEFINITION OF BRIGHTNESS UNIFORMITY



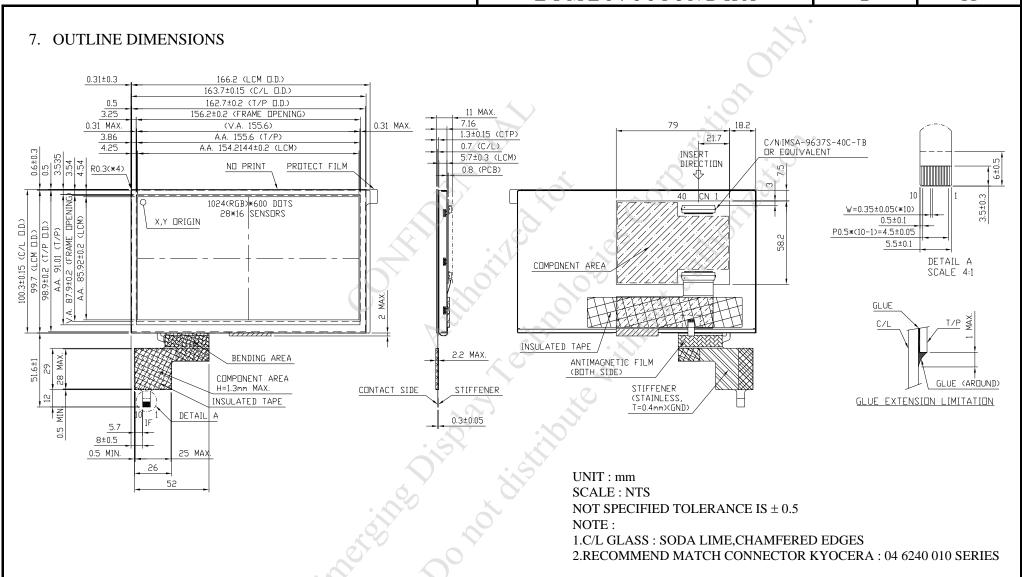
UNIT: mm

(b)THE BRIGHTNESS UNIFORMITY CALCULATING METHOD

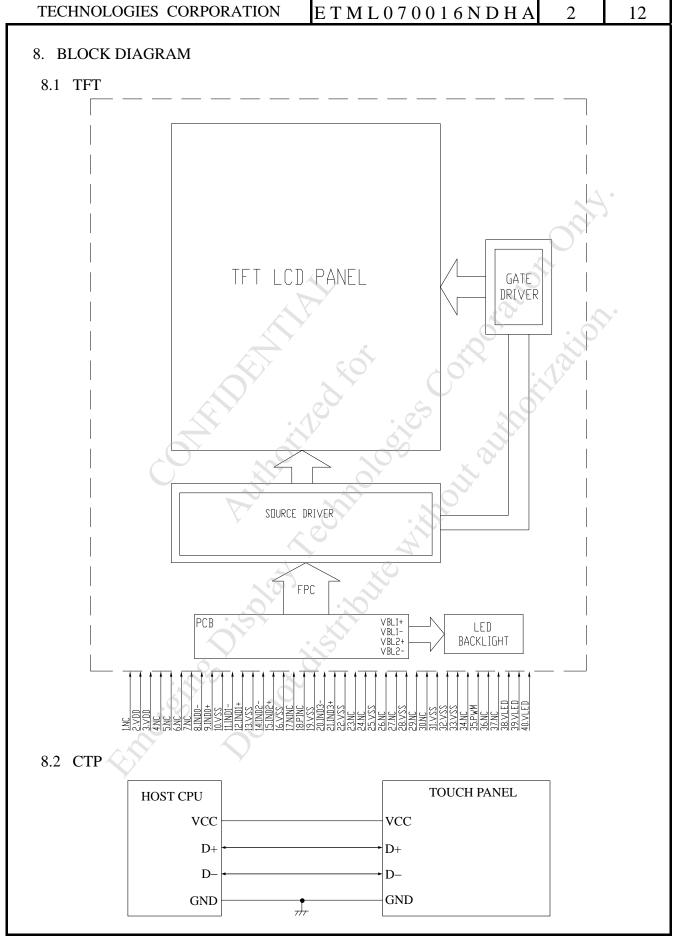
UNIFORMITY: MINIMUM BRIGHTNESS

MAXIMUM BRIGHTNESS **100%

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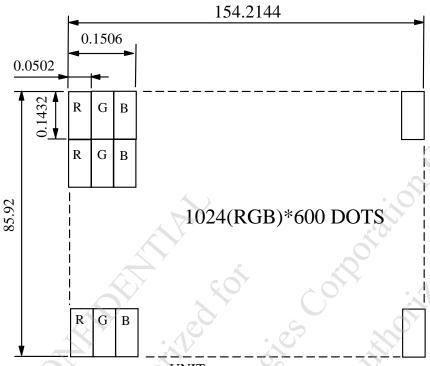
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UNIT : mm SCALE : NTS

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NOT SPECIFIED TOLERANCE IS \pm 0.1 DOTS MATRIX TOLERANCE IS \pm 0.01

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10. INTERFACE SIGNALS

10.1 TFT MODULE INTERFACE

PIN NO.	SYMBOL	FUNCTION
1	NC	NON CONNECTION
2	VDD	POWER SUPPLY VOLTAGE
3	VDD	POWER SUPPLY VOLTAGE
4	NC	NON CONNECTION
5	NC	NON CONNECTION
6	NC	NON CONNECTION
7	NC	NON CONNECTION
8	IND0-	LVDS SIGNAL (-) — CHANNEL 0 (R0~R5,G0)
9	IND0+	LVDS SIGNAL (+) — CHANNEL 0 (R0~R5,G0)
10	VSS	GROUND
11	IND1-	LVDS SIGNAL (-) — CHANNEL 1 (G1~G5,B0~B1)
12	IND1+	LVDS SIGNAL (+) — CHANNEL 1 (G1~G5,B0~B1)
13	VSS	GROUND
14	IND2-	LVDS SIGNAL (-) — CHANNEL 2 (B2~B5,VS,HS,DE)
15	IND2+	LVDS SIGNAL (+) — CHANNEL 2 (B2~B5,VS,HS,DE)
16	VSS	GROUND
17	NINC	LVDS CLOCK SIGNAL (–)
18	PINC	LVDS CLOCK SIGNAL (+)
19	VSS	GROUND
20	IND3-	LVDS SIGNAL (-) — CHANNEL 3 (R6,R7,G6,G7,B6,B7)
21	IND3+	LVDS SIGNAL (+) — CHANNEL 3 (R6,R7,G6,G7,B6,B7)
22	VSS	GROUND
23	NC	NON CONNECTION
24	NC	NON CONNECTION
25	VSS	GROUND
26	NC	NON CONNECTION
27	NC	NON CONNECTION
28	VSS 6	GROUND
29	NC	NON CONNECTION
30	NC	NON CONNECTION
31	VSS	GROUND
32	VSS	GROUND
33	VSS	GROUND
34	NC	NON CONNECTION
35	PWM	ADJUST FOR LED BRIGHTNESS
36	NC	NON CONNECTION
37	NC	NON CONNECTION
38	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT
39	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT
40	VLED	POWER SUPPLY VOLTAGE FOR LED BACKLIGHT

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10.2 CTP MODULE INTERFACE

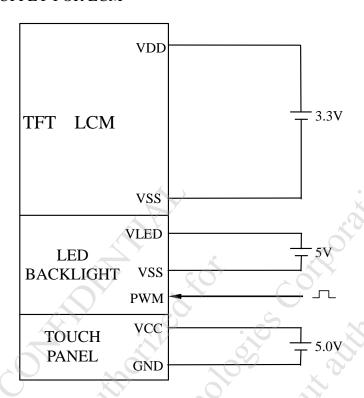
PIN NO.	SYMBOL	FUNCTION
1	NC	THE PIN WAS RESERVED FOR I2C GND
2	NC	THE PIN WAS RESERVED FOR I2C /RST
3	NC	THE PIN WAS RESERVED FOR I2C /INT
4	NC	THE PIN WAS RESERVED FOR I2C SCL
5	NC	THE PIN WAS RESERVED FOR I2C SDA
6	NC	THE PIN WAS RESERVED FOR I2C VCC(+3.3V)
7	VCC	POWER SUPPLY VOLTAGE(+5V)
8	D-	USB D-
9	D+	USB D+
10	GND	GROUND
	CONTRACTOR	Alikaitled for coll alikaitla di la lika di

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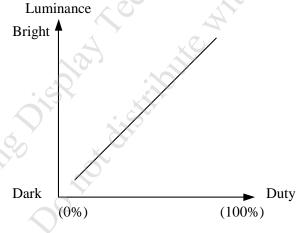
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11. POWER SUPPLY

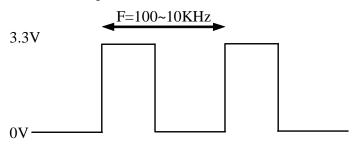
11.1 POWER SUPPLY FOR LCM



NOTE ($\bf 1$) : ADJUST THE PWM SIGNAL IN ORDER TO CONTROL LED BACKLIGHT'S BRIGHTNESS. THE HIGHER THE DUTY CYCLE, THE HIGHER THE BRIGHTNESS



NOTE (2): THE OPERATION FREQUENCY OF PWM SIGNAL IS 100~10KHz.



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12. CAPACITIVE TOUCH PANEL SPECIFICATION

12.1 OPTICAL CHARACTERISTICS

ITEM	CONDITION	MIN.	TYP.	MAX.	UNIT
TRANSPARENCY NOTE (1)	$Ta = 25^{\circ}C$ $\lambda = 550 nm$	(85)	_	_	%

NOTE (1) : OPTICAL MEASUREMENT SHOULD BE EXECUTED AFTER PANEL IS SECURED. MEASUREMENT PROCESS SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM.

OPTICAL SPECIFICATIONS SHOULD BE MEASURED BY SPECTROPHOTOMETER.

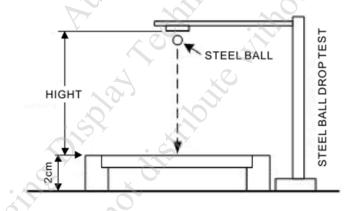
12.2 HARDNESS

ITEM	DESCRIPTION
SURFACE HARDNESS	7H (min)

12.3 DURABILITY

USING STEEL BALL AND FALLING ON TOUCH PANEL SURFACE, FROM THE HEIGHT MUST PASS BELOW CONDITIONS:

ITEM	CONDITION	INSPECTION METHOD	DESCRIPTION
STEEL BALL DROP TEST	WEIGHT: 67g HEIGHT OF FALL: 30 cm	VISUAL	SIGN OF FRACTURE OR DAMAGE IS NOT ACCEPTABLE 3 TIME/ 1 POINTS, 25°C(CENTER POINT)



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13. INSPECTION CRITERION

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.($\rm E.D.T$) TO CUSTOMERS

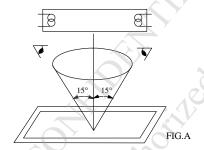
13.2 INSPECTION CONDITIONS

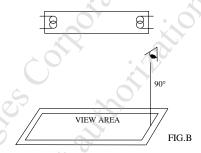
13.2.1 (1)OBSERVATION DISTANCE: 45±5cm

(2) VIEWING ANGLE: ±15°

±15° (FOR SECTION WITHIN VIEWING AREA), REFER TO FIG.A 90° (FOR SECTION OUTSIDE OF VIEWING AREA), REF TO FIG.B PERPENDICULAR TO MODULE SURFACE

VIEWING ANGLE SHOULD BE SMALLER THAN 15°





THE INSPECTION CRITERIA IS ACCORDING TO LINE OF SIGHT. INSPECTION SHALL BE MADE WITHIN THE HALF SECTION OF THE VIEWING CONE GENERATED BY LINE SEGMENT OF 15° WITH RESPECTS TO THE VERTICAL AXIS FROM CENTER VERTEX OF LCD, THE FLUORESCENT LAMP AND THE CONE AXIS MUST BE PERPENDICULAR TO THE LCD SURFACE.

IF THE DEFECTS ARE OUTSIDE OF VIEWING AREA, IT SHALL BE INSPECTED BY 90° WITH RESPECTS TO THE VERTICAL AXIS FROM EDGE OF VIEWING AREA.

13.2.2 ENVIRONMENT CONDITIONS:

AMBIEN	25±5°C	
AMBI	$65 \pm 20\%$ RH	
AMBIENT	COSMETIC INSPECTION	600~800 lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 lux
INSP	15 secs	

13.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY (a)APPLICABLE STANDARD:

ANSI/ ASQ Z1.4 NORMAL INSPECTION LEVEL II

(b)AQL: MAJOR DEFECT: AQL 0.65 MINOR DEFECT: AQL 1.0

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13.3 DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	
MAJOR DEFECT	2.CTP FUNCTION	NO FUNCTION BROKEN LINE FALSE TOUCH	0.65
	3.BACKLIGHT	NO LIGHTFLICKERING AND OTHER ABNORMAL ILLUMINATION	
	4.DIMENSIONS	SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	,
	1.DISPLAY ZONE (VIEWING AREA)	 BLACK/WHITE SPOT / CIRCULAR TYPE BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE / LINEAR TYPE SCRATCH CONTAMINATION UNEVEN COLOR SPREAD 	
MINOR DEFECT	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	1.0
•	3.SOLDERING	 • INSUFFICIENT SOLDER • SOLDERED IN INCORRECT POSITION • CONVEX SOLDERING SPOT • SOLDER BALLS • SOLDER SCRAPS 	
a co	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

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NO	ITEM		CDITEDI	\	
NO.	I I EIVI	1. INCORRECT PATTE	CRITERIA	1	
	DISPLAY ON	2. MISSING SEGMENT			
1	INSPECTION	3. DIM SEGMENT	•		
	INSPECTION	4. OPERATING VOLTA	AGE BEYOND SPEC		
2	OVERALL	1. OVERALL DIMENS			
<u> </u>	DIMENSIONS				CDEEN
		1. INSPECTION PATTI AND BLUE SCREEN		FULL BLACK, KED	, GREEN
		2.	ND.		1.
		ITE	MS	ACCEPTABLE COUN	
		BRIGHT DOT	IVIS F		
				N ≤ 1	
		DARK DOT		N ≤ 4	
		TOTAL BRIGHT AN	ND DARK DOTS	N ≤ 5	
		NOTE:		X	
3	DOT DEFECT	(1)THE DEFINITION C	OF DOT:	10	₽.
)	DOI DEFECT	77.5	FECTIVE DOT OVER		OT IS
		REGARDED AS ON	E DEFECTIVE DOT.	NO KA	
		(2)BRIGHT DOT:			
		DOTS APPEAR BRI	GHT AND UNCHAN	IGED IN SIZE IN W	HICH LCD
		The state of the s	YING UNDER BLAC		
	/	THE BRIGHT DOT	DEFECT MUST BE V	ISIBLE THROUGH	2% ND
		FILTER.			
		(3)DARK DOT:	92		
			RK AND UNCHANG		
		PANEL IS DISPLAY	ING UNDER PURE	RED, GREEN, BLUI	E PICTURE.
			AVERAGE DIAMETER (mm): D	PERMISSIBLE NO.	
		V	D ≤ 0.25	IGNORE	
		BUBBLE ON POLARIZER	$0.25 < D \le 0.5$	N ≤ 5	
			0.5 < D D < 0.1	0 IGNORE	
		SURFACE STAINS / DIRT	$0.1 < D \le 0.3$	N ≤ 3	
		ON POLARIZER	0.3 < D	0	
		CF FAIL / SPOT	D < 0.1	IGNORE N < 2	
		CI TAIL STOI	$0.1 < D \le 0.3$ 0.3 < D	$N \le 3$	
		NOTE : (1)POLARIZE	7	NED AS THE RURR	I E ADDEADS
	BUBBLES ON		E DISPLAY AREA. T		
4	POLARIZER		HALL BE IGNORED		
	/SURFACE STAINS	501)	ON THE OUTSIDE C		
	/DIRT/CF FAIL/SPOT				
	300	(2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON.			
		(3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED			
		` '	AS FOLLOWING.		
		AVERAGE	E DIAMETER (D)=(a-	+b)/2	
			$\overline{}$		
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			八,		
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			<u></u>		

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	ITEM	_	CRITERIA	
		THE FOLLOWING BLACK/WHITE SF	OT ARE WITHIN THE	
		VIEWING AREA. AVERAGE DIAMET	TER: D (mm)	
		SIZE D	PERMISSIBLE NO.	D / D
	BLACK/WHITE	D≤0.1	IGNORE	D /
5	SPOT CIRCULAR	0.1 <d≤0.3< td=""><td>5</td><td></td></d≤0.3<>	5	
	TYPE	0.3 <d≤0.5< td=""><td>5</td><td></td></d≤0.5<>	5	
		D>0.5	0	/ /
		NOTE (1): THE DISTANCE BETW	EEN DEFECTS	
		SHOULD BE MORE TH		4
		THE FOLLOWING SCRATCH IS WITH		[AA]
		WIDTH: W (mm), LENGTH: L (mm)		
		SIZE W & L	PERMISSIBLE NO.	
		W≤0.05	IGNORE	
ó	SCRATCH	0.05 <w≤0.08, l≤8<="" td=""><td>3</td><td></td></w≤0.08,>	3	
,	Benefici	0.08 <w≤0.1, l≤5<="" td=""><td>2</td><td></td></w≤0.1,>	2	
		W>0.1	0	
		NOTE (1): THE DISTANCE BETW		
		SHOULD BE MORE TH		
		THE FOLLOWING BLACK LINE, WH		
		VIEWING AREA. WIDTH: W (mm),		X
		SIZE W & L	PERMISSIBLE NO.	. 12
	BLACK /	W≤0.05		
7	WHITE LINE		IGNORE	
	LINEAR TYPE /	0.05 <w≤0.08, l≤8<="" td=""><td>3</td><td></td></w≤0.08,>	3	
	FOREIGN FIBER	0.08 <w≤0.1, l≤5<="" td=""><td>2</td><td></td></w≤0.1,>	2	
		W>0.1	0	
		NOTE (1): THE DISTANCE BETW		
		SHOULD BE MORE TH	AN 10mm APART.	
		BUBBLES WITHIN VIEWING AREA.		
		AVERAGE DIAMETER : D (mm)		
				D /
		SIZE D	PERMISSIBLE NO.	
	BUBBLE / DENT	D≤0.2	IGNORE	
3	FOR OPTICAL		IGNORE 3	
3		D≤0.2	IGNORE 3 1	
3	FOR OPTICAL	D≤0.2 0.2 <d≤0.3 0.3<d≤0.5 D>0.5</d≤0.5 </d≤0.3 	3 1 0	
3	FOR OPTICAL	D≤0.2 0.2 <d≤0.3 0.3<d≤0.5< td=""><td>3 1 0</td><td></td></d≤0.5<></d≤0.3 	3 1 0	
3	FOR OPTICAL	D≤0.2 0.2 <d≤0.3 0.3<d≤0.5 D>0.5</d≤0.5 </d≤0.3 	IGNORE 3 1 0 EEN DEFECTS	
3	FOR OPTICAL	D≤0.2 0.2 <d≤0.3 0.3<d≤0.5 D>0.5 NOTE (1): THE DISTANCE BETW SHOULD BE MORE TH</d≤0.5 </d≤0.3 	IGNORE 3 1 0 EEN DEFECTS	Chip of glass
	FOR OPTICAL BONDING	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ \hline SHOULD BE MORE TH \\ \hline \\ CORNER & X \leq 3mm \\ \hline \end{array}$	IGNORE 3 1 0 EEN DEFECTS AN 10mm APART. Y ≤ 3mm \ Z ≤ t	Chip of glass
)	FOR OPTICAL	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline \\ CORNER & X \leq 3mm \\ (t: T \\ X \leq 6mm \\ \end{array}$	IGNORE 3 1 0 EEN DEFECTS AN 10mm APART. Y ≤ 3mm \ Z ≤ t HICKNESS)	Chip of glass
	FOR OPTICAL BONDING	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ \hline SHOULD BE MORE TH \\ \hline CORNER & X \leq 3mm \\ (t: T) \\ \hline EDGE & X \leq 6mm \\ \end{array}$	IGNORE 3 1 0 EEN DEFECTS AN 10mm APART. Y \leq 3mm \cdot Z \leq t HICKNESS) , Y \leq 1mm , Z $<$ t	Chip of glass
)	FOR OPTICAL BONDING CHIPPING	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline \\ CORNER & X \leq 3mm \\ (t: T \\ EDGE & X \leq 6mm \\ (t: T \\ \hline \end{array}$	$\begin{array}{c} \text{IGNORE} \\ 3 \\ 1 \\ 0 \\ \end{array}$ EEN DEFECTS $\begin{array}{c} \text{AN 10mm APART.} \\ \text{Y \leq 3mm \cdot $Z \leq t} \\ \text{HICKNESS)} \\ \text{, $Y \leq 1mm $, $Z $<$ t} \\ \text{HICKNESS)} \\ \end{array}$	Chip of glass
)	FOR OPTICAL BONDING CHIPPING CRACKED GLASS	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ \hline SHOULD BE MORE TH \\ \hline CORNER & X \leq 3mm \\ (t: T) \\ \hline EDGE & X \leq 6mm \\ \end{array}$	$\begin{array}{c} \text{IGNORE} \\ 3 \\ 1 \\ 0 \\ \end{array}$ EEN DEFECTS $\begin{array}{c} \text{AN 10mm APART.} \\ \text{Y \leq 3mm \cdot $Z \leq t} \\ \text{HICKNESS)} \\ \text{, $Y \leq 1mm $, $Z $<$ t} \\ \text{HICKNESS)} \\ \end{array}$	Chip of glass
)	FOR OPTICAL BONDING CHIPPING CRACKED GLASS LINE DEFECT ON DISPLAY	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline \\ CORNER & X \leq 3mm \\ (t: T \\ EDGE & X \leq 6mm \\ (t: T \\ \hline \end{array}$	$IGNORE$ 3 1 0 $EEN DEFECTS$ $AN 10mm APART.$ $Y \le 3mm \cdot Z \le t$ $HICKNESS)$ $Y \le 1mm , Z < t$ $HICKNESS)$ $ABLE$	1 V
0	FOR OPTICAL BONDING CHIPPING CRACKED GLASS LINE DEFECT ON DISPLAY MURA ON DISPLAY	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline CORNER & X \leq 3mm \\ (t: T) \\ EDGE & X \leq 6mm \\ (t: T) \\ \hline NOT ACCEPTA \\ \hline \end{array}$	IGNORE 3 1 0 0	S NOT ALLOWED
0 1 2	FOR OPTICAL BONDING CHIPPING CRACKED GLASS LINE DEFECT ON DISPLAY MURA ON	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline \\ CORNER & X \leq 3mm \\ (t:T) \\ \hline EDGE & X \leq 6mm \\ (t:T) \\ \hline NOT ACCEPTA \\ \hline OBVIOUS VERTICAL OR HORIZOUS \\ \hline \\ \hline \end{tikzpicture}$	IGNORE 3 1 0 EEN DEFECTS AN 10mm APART. Y ≤ 3mm · Z ≤ t HICKNESS) , Y ≤ 1mm , Z < t HICKNESS) ABLE ONTAL LINE DEFECT IS SLIGHT VISIBLE THROU	S NOT ALLOWED UGH 5% ND FILTI
0 1 2	FOR OPTICAL BONDING CHIPPING CRACKED GLASS LINE DEFECT ON DISPLAY MURA ON DISPLAY UNEVEN COLOR SPREAD, COLORATION	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline CORNER & X \leq 3mm \\ (t: T) \\ EDGE & X \leq 6mm \\ (t: T) \\ \hline NOT ACCEPTA \\ \hline OBVIOUS VERTICAL OR HORIZOUT'S ACCEPTABLE, IF MURA IS $2.50.} \\ \hline \end{tabular}$	IGNORE 3 1 0 EEN DEFECTS AN 10mm APART. Y ≤ 3mm · Z ≤ t HICKNESS) , Y ≤ 1mm , Z < t HICKNESS) ABLE ONTAL LINE DEFECT IS SLIGHT VISIBLE THROUGHT	S NOT ALLOWED UGH 5% ND FILTI PLE.
0 1 2	FOR OPTICAL BONDING CHIPPING CRACKED GLASS LINE DEFECT ON DISPLAY MURA ON DISPLAY UNEVEN COLOR SPREAD,	$\begin{array}{c c} D \leq 0.2 \\ \hline 0.2 < D \leq 0.3 \\ \hline 0.3 < D \leq 0.5 \\ \hline D > 0.5 \\ \hline NOTE (1): THE DISTANCE BETW \\ SHOULD BE MORE TH \\ \hline CORNER & X \leq 3mm \\ (t: T) \\ EDGE & X \leq 6mm \\ (t: T) \\ \hline NOT ACCEPTA \\ \hline OBVIOUS VERTICAL OR HORIZOUTO'S ACCEPTABLE, IF MURA IS SET OF BE DETERMINED BASED UPOn the second secon$	IGNORE 3 1 0 EEN DEFECTS AN 10mm APART. Y ≤ 3mm · Z ≤ t HICKNESS) , Y ≤ 1mm , Z < t HICKNESS) ABLE ONTAL LINE DEFECT IS SLIGHT VISIBLE THROUGHT ON THE LIMITED SAME , BE DEFORMED OR HA	S NOT ALLOWED UGH 5% ND FILTI PLE.

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	1	
NO.	ITEM	CRITERIA
15	РСВ	 THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. NO OXIDATION OR CONTAMINATION ON PCB TERMINALS. PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.
16	SOLDERING	1. NO SOLDERING FOUND ON THE SPECIFIED PLACE 2. INSUFFICIENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD SOLDER FILLET (b)CHIP COMPONENT SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING SOLDER FILLET 1/2 • SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED SOLDER

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NO.	ITEM	CRITERIA
	SOLDERING	3. PARTS ALIGNMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE (b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE 4. NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. 5. NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS,
17	BACKLIGHT	OXIDATION OR ICICLE. 6. NO RESIDUE OR SOLDER BALLS ON PCB. 7. NO SHORT CIRCUITS IN COMPONENTS ON PCB. 1. NO LIGHT 2. FLICKERING AND OTHER ABNORMAL ILLUMINATION 3. SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. 4. BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
1 1×	GENERAL APPEARANCE	 NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. NO CRACKS ON INTERFACE PIN (OLB) OF TCP. NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. LCD PIN LOOSE OR MISSING PINS. PRODUCT PACKAGING MUST BE THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

NOTE:

- 1.FOR ANY SPOTS OR LINES, WHICH ARE NOT OBSERVED UNDER APPROPRIATE PANEL OPERATING CONDITION ARE DEEMED ACCEPTABLE.
- 2. THE FOREIGN MATERIALS THAT CAN BE BLOWN OUT BY AIR AND REMOVED BY WET CLEANING ARE NOT REGARDED AS DEFECTS.

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14. RELIABILITY TEST

14.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION		
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS		
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS		
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS		
4	LOW TEMP	THE CAMPLE CHOLLED BE ALLOWED TO STAND AT 20°C FOR 240 HBS		
4	STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS		
5	HIGH TEMPERATURE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH		
	/HUMIDITY TEST STORAGE	240 HRS		
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: +80°C -30°C -30°C 30 min 1 cycle		
7	(ELECTROSTATIC DISCHARGE)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)		

- NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION FAILURE ISSUE OCCURRED.
- NOTE (2): WHEN THE LCD MODULE IS OPERATED AT A HIGHER AMBIENT TEMPERATURE THAN 60°C, THE PWM DUTY CYCLE OF THE LED BACKLIGHT SHOULD BE ADJUSTED TO BE LESS THAN 60%. IF THE MODULE IS OPERATED AT A HIGHER DUTY CYCLE THAN TBD, THEN THERE IS A POSSIBILITY OF DISTORTION AND IRREGULARITY OF THE PICTURE DUE TO LIQUID CRYSTAL BEHAVIOR.
 - 14.2 TESTING CONDITIONS AND INSPECTION CRITERIA
 FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM
 TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 14.1,
 STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN
 ORDER TO ENSURE STABILITY.

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION		THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

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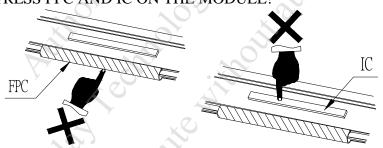
15. CAUTION

15.1 OPERATION

- 15.1.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 15.1.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 15.1.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 15.1.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

 IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 15.1.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



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15.2 HANDLING

- 15.2.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGHHOLE-PAD .
- 15.2.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 15.2.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 15.2.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 15.2.5 DON'T GIVE EXTERNAL SHOCK.
- 15.2.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 15.2.7 LIQUID CRISTAL IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
 WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC.
 WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 15.2.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 15.2.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 15.2.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 15.2.11 REWIRING: NO MORE THAN 3 TIMES.

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