SP	F	C	ΙF	IC	ΔΊ	N C	I.S
VI.	_	v		\cdot	$\boldsymbol{\frown}$ I	 <i>-</i>	v

CUSTOMER . CJP083

SAMPLE CODE . SG24064WRM-EGAI04Q

MASS PRODUCTION CODE . PG24064WRM-EGAI04Q

SAMPLE VERSION . 02

SPECIFICATIONS EDITION . 003

DRAWING NO. (Ver.) : HLMD-PG24064WRM-EGAI04Q (Ver:001)

PACKAGING NO. (Ver.) : JPKG-PG24064WRM-EGAI04Q_002

Customer Approved

Date:

Approved	Checked	Designer
 	劉進	徐明菲

- □ Preliminary specification for design input
- Specification for sample approval

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RECORDS OF REVISION

Date (mm / dd / yyyy)	Ver	Edi	Description	Page	Design by
5/27/2008	01	001	The sample has changed the IC, which was based on the Powertip's MASS PRODUCTION: PG24064WRM-ETIY8Q		張恒
12/23/2008	02	002	The sample has changed the B/L base on the previous sample		楊興華
07/04/2018	02	003	Update Specification (Client required the speicification of HD's edition)	1	徐明菲

Total: 27page



Contents

1. SPECIFICATIONS

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- 1.4 DC Electrical Characteristics
- 1.5 Optical Characteristics
- 1.6 Backlight Characteristics

2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics
- 2.4 Display command
- 2.5 JUMPER

3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

4. RELIABILITY TEST

4.1 Reliability Test Condition

5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

Appendix: 1. LCM Drawing

2. Packing Specification



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	240*64 Dots
LCD Type	STN Blue Positive Transmissive Extended Temp.
Driver Condition	LCD Module: 1/64 Duty, 1/9 Bias
Viewing Direction	6 O'clock
Weight	173 g
Interface	8080-series 8-bits parallel data bus
Other	SAP1024B
(controller / driver IC)	3AI 1024B
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web site :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	180.0 (L) *65.0 (w) * 14.8(H)(Max)	mm
Viewing Area	132.6.0(L) * 39.0(w)	mm
Active Area	127.16(L) * 33.88 (w)	mm
Dot Size	0.49 (L) * 0.49(w)	mm
Dot Pitch	0.53 (L) * 0.53 (w)	mm

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	VLCD	-	0	30	V
Input Voltage	VIN	-	-0.3	VDD+0.3	V
Operating Temperature	Тор	-	-20	70	$^{\circ}\!\mathbb{C}$
Storage Temperature	T _{ST}	-	-30	80	$^{\circ}\!\mathbb{C}$
Storage Humidity	H _D	Ta<60 °C	-	90	%RH



1.4 DC Electrical Characteristics

 V_{DD} =5.0 V \pm 10% $\,^{,}$ V_{SS} = 0V $\,^{,}$ Ta = 25 $^{\circ}\mathrm{C}$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V _{DD}	-	4.5	5.0	5.5	V
"H" Input Voltage	VIH	-	VDD-2.2	-	VDD	V
"L" Input Voltage	VIL	-	0	-	0.8	V
"H" Output Voltage	VoH	-	VDD-0.3	-	VDD	V
"L" Output Voltage	V _{OL}	-	0	-	0.3	V
Cumply Current		V _{DD} =5.0V;V _{OP} =12.7V; Pattern= Full display	-	16.8		A
Supply Current	l _{dd}	V _{DD} =5.0V;V _{OP} =12.7V; Pattern= Horizontal line*1	-	17.0	30	mA
		-20 °℃	12.7	12.9	13.1	
LCM Driver Voltage	Vop*2	25 ℃	12.5	12.7	12.9	V
		70℃	12.3	12.5	12.7	

NOTE: *1 The Maximum current display;

*2 The VOP test point is VDD-VO.





1.5 Optical Characteristics

LCD Panel: 1/64Duty, 1/9Bias, V_{LCD} =12.7V, Ta =25

 $^{\circ}$ C

Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		-	200	-	mo	Note2
response fille	Fall	tf	_	1	150	-	ms	NOIGZ
	Тор	θΥ+		-	-	40		
Viewing angle	Bottom	θΥ-	0>0.0	-	-	40	Deg.	Notes 1
range	Left	θX-	C>2.0	-	C -	45		
	Right θX+			-	-	45	1	
Contrast Ra	tio	С	θ = 0°	2.0	4.2	-	-	Note 3
Average Bright (LCD & BL)		IV		60	72	-	cd/m2	
CIE Color Coordinate		Х	IF=90mA	0.141	0.171	0.201	nm	Note 4
(LCD & BL)*2		у		0.064	0.094	0.124	nm	
Uniformity *1		∆B		70	-	-	%	

Note 4:

1 : △B=B(min) / B(max) * 100%

2: Measurement Condition for Optical Characteristics:

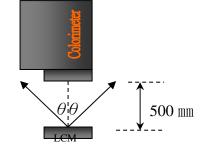
a : Environment: 25°C±5°C / 60±20%R.H ⋅ no wind ⋅ dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

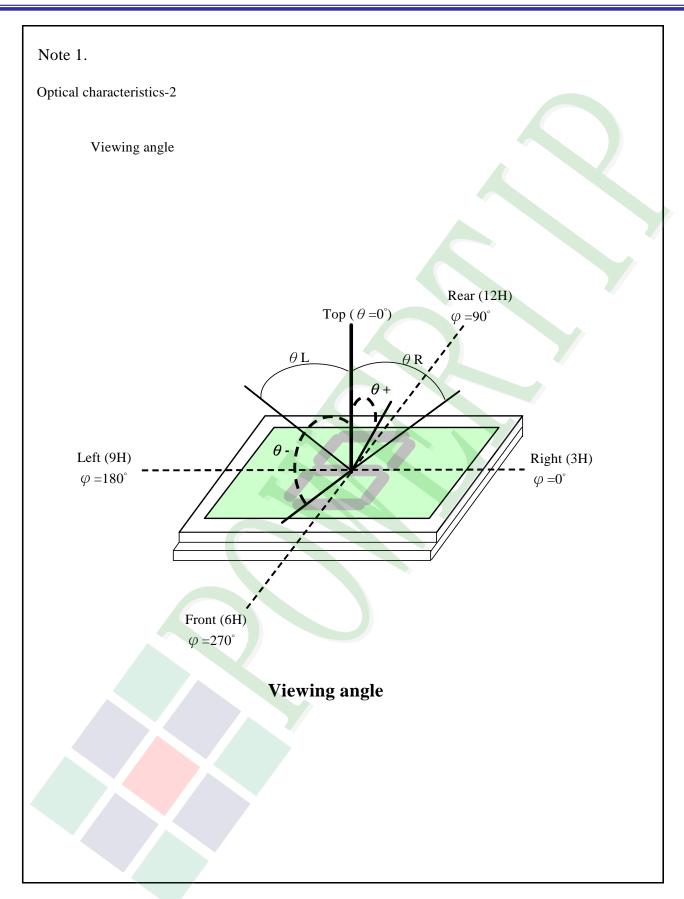
d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%



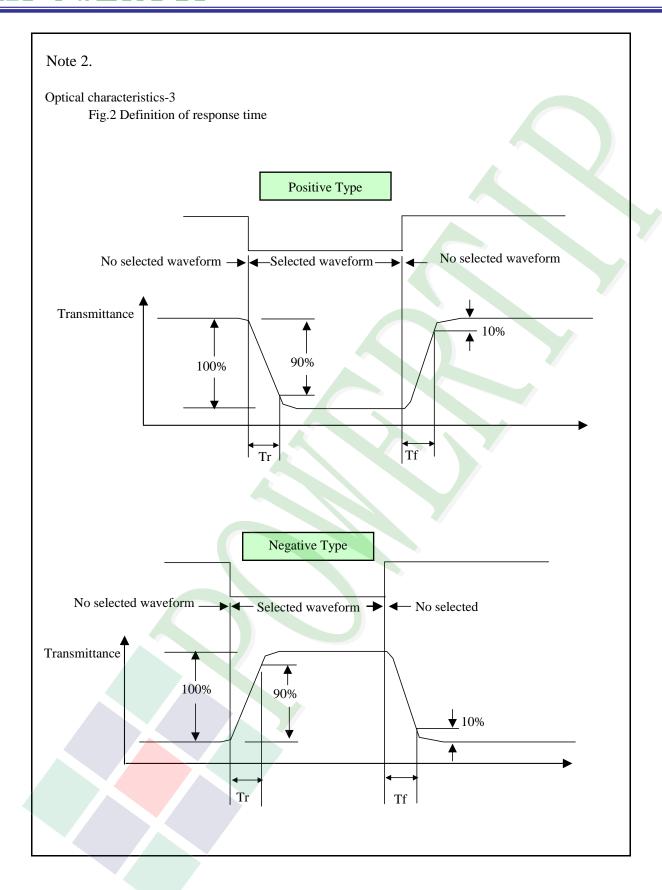


Colorimeter=BM-7 fast











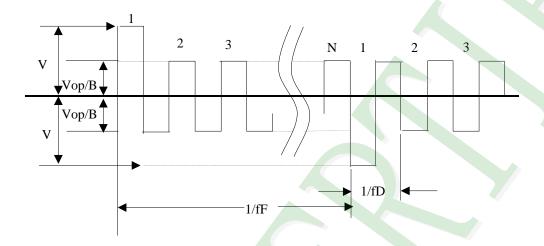
Electrical characteristics-2

[™] 2 Drive waveform

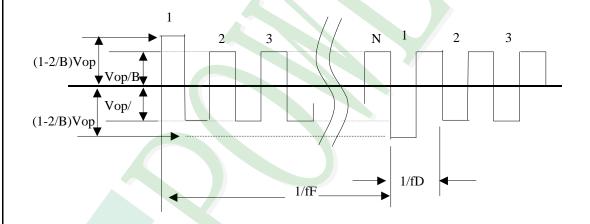
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



(2) Non- Selected wave form

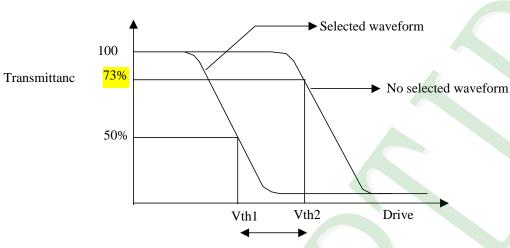


Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period







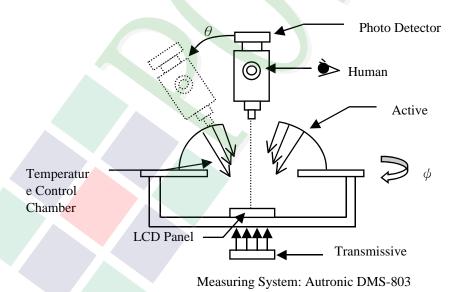
Active voltage range

	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

LCD Module with LED Backlight

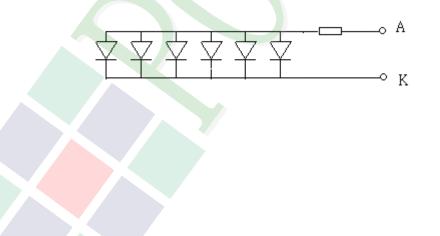
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°℃	-	90	mA
Reverse Voltage	VR	Ta =25°℃	- 🔥	3.0	V
Reverse Current	IR	VR=3.0V	- ^	10	uA
Power Dissipation	PD	Ta =25°ℂ		300	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF			3.6	4.2	V
Average Brightness (without LCD)	IV	IF=90mA	200	300	-	cd/m ²
CIE Color Coordinate	X	1E-00m A	0.264	0.28	0.296	
(Without LCD)	Y	IF=90mA	0.248	0.29	0.305	_
Color			white			

Circuit Diagram:





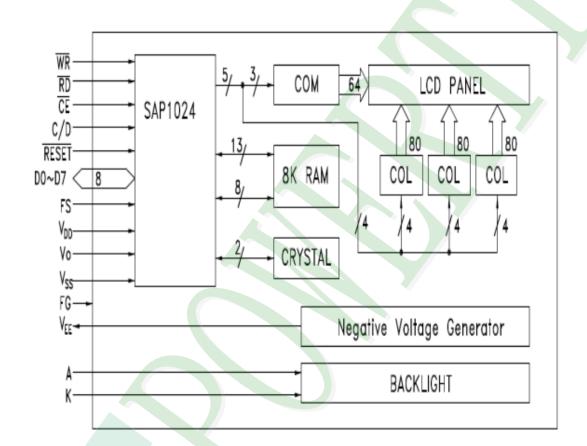
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



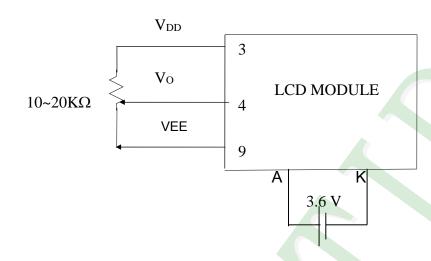


2.2 Interface Pin Description

Pin No.	Symbol	Function
1	FG	Frame ground (connected to metal bezel)
2	Vss	Power Supply (Vss=0)
3	V_{DD}	Power Supply (VDD>VSS)
4	Vo	Operating voltage for LCD
5	WR	Data write (write data to the module at "L")
6	RD	Data read (read data from the module at "L")
7	CE	Chip enable for the module (active at "L")
8	C/ D	WR ="L";C/ D = "H" :command write,C/ D ="L":data write R D ="L";C/ D = "H" :command read, C/ D ="L":data read
9	V _{EE}	Negative voltage output
10	RESET	Controller reset (module reset)
11	DB0	Data bus (DB0=LSB)
12	DB1	Data bus
13	DB2	Data bus
14	DB3	Data bus
15	DB4	Data bus
16	DB5	Data bus
17	DB6	Data bus
18	DB7	Data bus (DB7=MSB)
19	FS	Font select :connect to VDD : 6*8 Dots font connect to VSS : 8*8 Dots font
20	N/A	Not connection



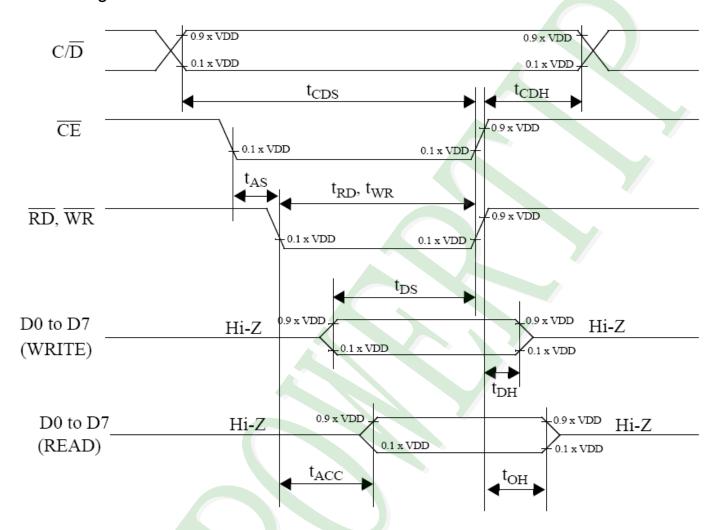
Contrast Adjust





2.3 Timing Characteristics

Bus Timing



V_{DD} =5.0V±0.5V, V_{SS} =0V, Ta=25

 $^{\circ}\! C$

symbol	parameter	MIN.	MAX.	test conditions	Unit
tcps	C/D set-up time	100			ns
t _{CDH}	C/D hold time	10			ns
t _{RD} , t _{WR}	RD, WR pulse width	80			ns
t _{AS}	Address set-up time	0			ns
t _{AH}	Address hold time	0			ns
t _{DS}	Data set-up time	80			ns
t _{DH}	Data hold time	40		,	ns
tacc	Access time		150		ns
tон	Output hold time	10	50		ns



2.4 Display command

Commands and Registers.

COMMAND	CODE	OPERAND 1	OPERAND 2	FUNCTION
	0010 0001	X address	Y address	Set cursor pointer
Register Setting	0010 0010	Data	00H	Set offset register
Setting	0010 0100	Low address	High address	Set address pointer
	0100 0000	Low address	High address	Set text home address
Set Control	0100 0001	Columns	00H	Set text area
Word	0100 0010	Low address	High address	Set graphic home address
	0100 0011	Columns	00H	Set graphic area
	1000 ×000			OR mode
	1000 x001			EXOR mode
	1000 x011			AND mode
Mode Set	1000 x100			Text Attribute mode
	1000 0xxx			Internal CG ROM mode
	1000 1xxx			External CG RAM mode
	1001 0000			Display OFF.
	1001 xx10			Cursor ON, blink OFF.
	1001 xx11			Cursor ON, blink ON.
Display mode	1001 01xx			Text ON, graphic OFF.
	1001 10xx			Text OFF, graphic ON.
	1001 10xx			Text ON, graphic ON.
	1010 0000			Selec one-line cursor.
	1010 0000			Select two-line cursor.
	1010 0001			Select three-line cursor.
Cursor	1010 0010			Select full-e-line cursor.
Pattern				
Select	1010 0100			Select five-line cursor.
	1010 0101			Select six-line cursor.
	1010 0110			Selec seven-line cursor.
	1010 0111			Select eight-line cursor.
Data Auto	1011 0000			Select Data Auto Write
Read/Write	1011 0001			Select Data Auto Read
	1011 0010			Reset Auto Read/Write
COMMAND	CODE	OPERAND 1	OPERAND 2	FUNCTION
	1100 0000	Data		Data Write and increment Address Pointer
	1100 0001	2 .		Data Read and increment Address Pointer
Data READ / WRITE	1100 0010	Data		Data Write and decrement Address Pointer.
WKITE	1100 0011	Data		Data Read and decrement Address Pointer Data Write and Keep Address Pointer
	1100 0100	Data		Data Read and Keep Address Pointer
Screen Peek	1110 0000			Screen peek
Screen Copy	1110 1000			Screen copy
	1111 0xxxx			Bit Reset
	1111 1xxxx			Bit Set
	1111 x000			Bit 0
	1111 x001			Bit 1
Bit Set/Reset	1111 x010			Bit 2
Dit Seurceset	1111 x011			Bit 3
	1111 x100			Bit 4
	1111 ×101			Bit 5
	1111 x110			Bit 6
	1111 x111			Bit 7

2.5 JUMPER

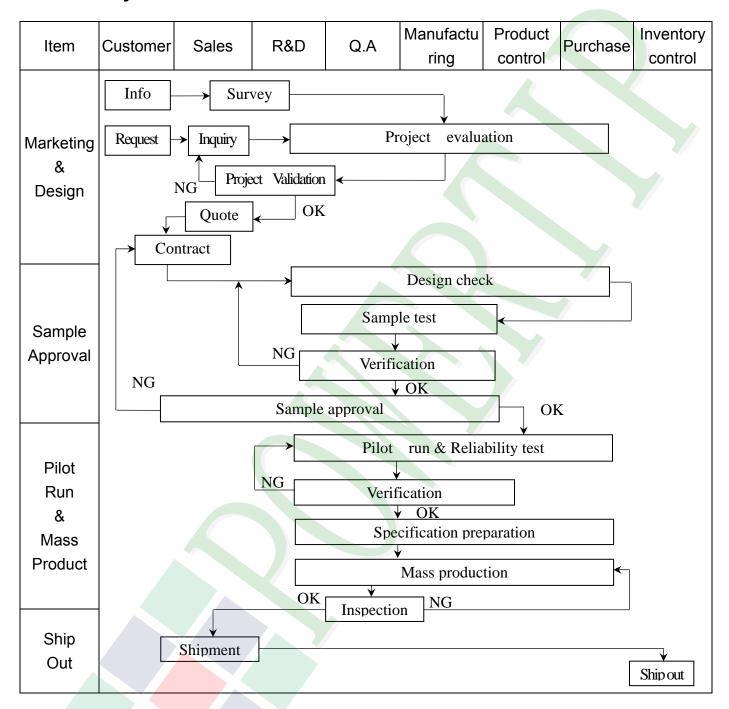
JEA/JEK/JP2(2.3)/JA2/JF1/JR/JM5/JF4: SHORT;

OTHERS: OPEN

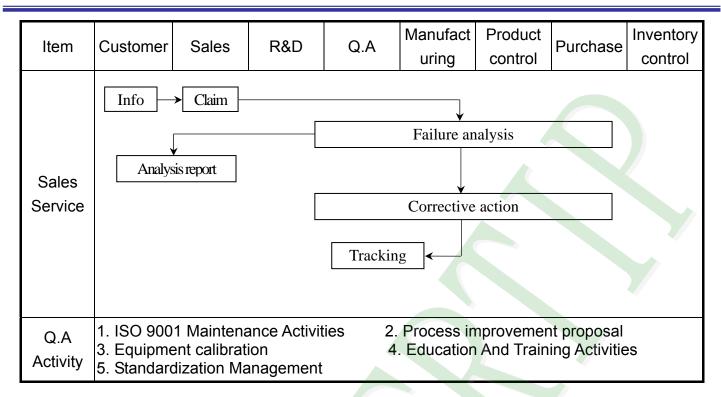


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

- ◆Scope: The document shall be applied to LCD Module for Monotype and Color STN (Ver. 03).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge · MIL-STD · Powertip Tester · Sample
- ◆Defect Level: Major Defect AQL: 0.4 ; Minor Defect: AQL: 1.5.
- ◆OUT Going Defect Level: Sampling.
- ◆Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). Standard of inspection : (Unit : mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area. (Fig. 2)

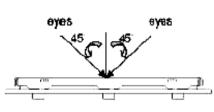


Fig.1

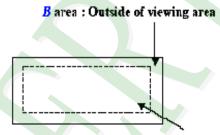


Fig. 2 A area: viewing area

◆ Specification:

NO	Item	Criterion	level
		1, 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
04	Electrical Testing	4. 2 No function or no display.	Major
		4, 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major



(Ver 03)

	cuication For Mone	otype and Color STN:			•	(Yer. 03)	
NO	Item		Criteri	on		level	
	Black or white dot v scratch v contamination	 5. 1 Round type: 5. 1. 1 display only: • White and black spots on display ≤ 0. 30 mm, no more than 4 white or black spots present. • Densely spaced: NO more than two spots or lines within 3 mm. 					
		5. 1. 2 Non-display :					
	Round type	Dimension		Acceptance (Q'ty)	Minor	
	Round type	(diameter : Φ)		A area	B area		
	→ _X ← _→	Φ ≤ 0. 1	10 Acc	ept no dense			
05	Y	$0.10 < \Phi \leq 0.$	20	3	Ignoro		
"	<u> </u>	$0.20 < \Phi \leq 0.$	30	2	Ignore		
	$\Phi = (x+y)/2$	Total quantity		4			
		5. 1. 3 Line type:					
	Line type	Dimension		Acceptance (Q'ty)			
		Length (L) Widt	th (W)	A area	B area		
	~ /¥w		$W \leq 0.03$	Accept no dens	e		
	→ L +		$W \le 0.05$ $V \le 0.075$	4	Ignore		
			V > 0. 075	As ro	und type		
		Dimension		Acceptance	T		
		(diameter: Φ)		A area	B area		
		Φ ≤ 0.20		ccept no dense	-	Minor	
06	Polarizer	$0.20 < \Phi \leq 0.50$		3		MILIOI	
	Bubble	$0.50 < \Phi \leq 1.00$	'	2	Ignore		
		Φ > 1.0	0	0			
		Total quantity		4			
			•				



	fication For Mone	otype and Color STN:	(1	er. 03)
NO	Item	Criterion		Level
		Z: The thickness of crack W:	The width of crack. terminal length LCD side length	
		7. 1 General glass chip: 7. 1. 1 Chip on panel surface and crack	between panels:	
07	The crack of glass	SP Z Z X X X X Seal width Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z	SP [NG]	Minor
		X Y	Z	
		≦ a Crack can't enter viewing area	≦1/2 t	
		≤ a Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



◆Specification For Monotype and Color STN: (Yer. 03) NO Item Criterion Level Symbols: Y: The width of crack. X: The length of crack Z: The thickness of crack W: terminal length t: The thickness of glass a: LCD side length 7. 1. 2 Corner crack: Х Y Z Crack can't enter Z ≤ 1/2 t ≤1/**5** a viewing area Crack can't exceed the 1/2 t < Z≤1/5 a half of SP width. The crack of 07 Minor glass 7.2 Protrusion over terminal: 7. 2. 1 Chip on electrode pad: X Y Z ≤ t ≤ a ≤ 1/2 W Front Neglect Back



◆Specification For Monotype and Color STN: (Yer. 03)

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
		7.2.2 Non-conductive portion:	
		Y X X	
		Y Z N	
07	The crack of glass	$\begin{array}{c cccc} X & Y & Z \\ \hline \leq 1/3 & \mathbf{a} & \leq \mathbf{W} & \leq \mathbf{t} \end{array}$	Minor
		⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.	
		7. 2. 3 Glass remain :	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$	



◆Specification For Monotype and Color STN: (Yer. 03)

NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION				
1	High Temperature Storage Test	Keep in 80 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
2	Low Temperature Storage Test	Keep in-30 ±2°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)				
4	Temperature Cycling Storage Test	$-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow 80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ $(30\text{mins}) (5\text{mins}) (5\text{mins})$ 20 Cycle Surrounding temperature, then storage at normal condition 4hrs.				
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15°C ~35°C 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least				
6	Vibration Test (Packaged)	1 sec) (Tolerance if the output voltage indication : ±5%) 1. Sine wave 10 ~ 55 Hz frequency (1 min/sweep) 2. The amplitude of vibration :1.5 mm 3. Each direction (X · Y · Z) duration for 2 Hrs				
7	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm)				



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ±5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

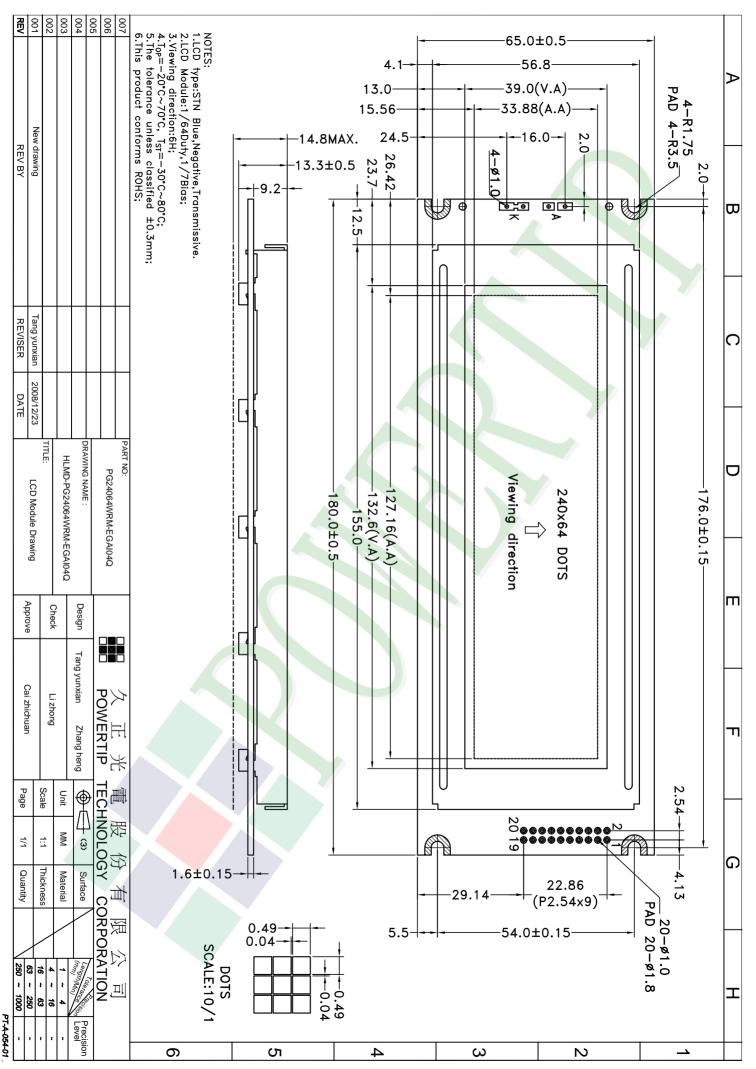
5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security



systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





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Ver.001

Documents NO. JPKG-PG24064WRM-EGAI04Q

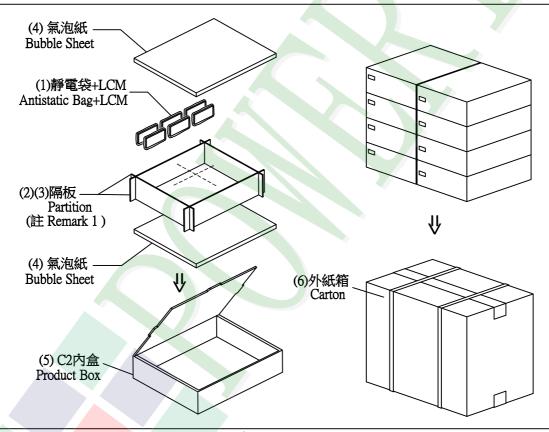
LCM包裝規格書 LCM Packaging Specifications

Approve	Check	Contact	
Ryan	Terry	Air	

1.包裝材料規格表 (Packaging Material): (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PG24064WRM-EGAI04Q	180.0 X 65.0 X 14.8	0.174	88	15.312
2	靜電袋(1)Antistatic Bag	BAG250100ARABA	250 X 100	0.0025	88	0.22
3	A2-1隔板(2)A2-1 Partition	BX29500072BZBA	295 X 72 X 3.0	0.0109	104	1.1336
4	B2-1隔板(3)B2-1 Partition	BX24500072BZBA	245 X 72 X 3.0	0.0094	32	0.3008
5	氣泡紙(4)Bubble Sheet	BAG280240BWABA	280 X 240	0.006	16	0.096
6	C2内盒(5)Product Box	BX31025580AABA	310 X 255 X 86	0.16	8	1.28
7	外紙箱(6)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8						
9						

- 2. 整箱總重量 (Total LCD Weight in carton): 19.17 Kg±10% 取小數2位
- 3.單箱數量規格表 (Packaging Specifications and Quantity):
 - (1)Quantity Of Spacer: A2-1隔板 X 13 , B2-1隔板 X 4
- (2) Total LCM quantity in carton: quantity per box 11 x no of boxes 8 = 88



特記事項(REMARK)

- 1. LCM排放示意圖(前後間隔不放置):
- 1. LCM placed as figure showing:

 (First and last slot should be empty)

