POWERTIP TECH. CORP.

SPECIFICATIONS

CUSTOMER	:	
SAMPLE CODE (Ver.)	:	
MASS PRODUCTION CODE (Ver.)	:	**PE12864WRF-018HY1Q(Ver.0)**
DRAWING NO. (Ver.)	:	**PE-04011-005 (Ver.0)**

Customer Approved

Date:

Approved	QC Confirmed	Designer
		

■ Approval For Specifications Only.

* This specification is subject to change without notice.

Please contact Powertip or it's representative before designing your product based on this specification.

□ Approval For Specifications and Sample.

POWERTIP TECH. CORP.

Headquarters:

No.8, 6th Road, Taichung Industrial Park,
Taichung, Taiwan
台中市 407 工業區六路 8 號

TEL: 886-4-2355-8168
FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw
Http://www.powertip.com.tw

RECORDS OF REVISION

Date	Rev.	Description	Note	Design by
2006/7/21	**0**	PE12864WRF-018HY1Qis the ROHS compliant part number based on Powertip's standard PE12864WRF-018-HY1	-	

Total：27 Page

Contents

Note：For detailed information please refer to IC data sheet：NOVATEK---NT7532H-BDT

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	128 * 64 Dots
LCD Type	FSTN, White Positive, Transflective Extended temp
Driver Condition	LCD Module :1/65 Duty, 1/9 Bias
Viewing Direction	6 O'clock
Backlight	LED B/L
Weight	12 g
Interface	8 bits parallel data input
Other(controller/driver IC)	NT7532H-BDT
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web side : http://www.powertip.com.tw/news/LatestNews.asp

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	80.5 (W) ×45.0 (H) × 5.3 Max (T)	mm
Viewing Area	60.0 (W) * 32.6 (L)	mm
Active Area	55.01(W) * 27.49 (L)	mm
Dot Size	0.4 (W) × 0.4 (H)	mm
Dot Pitch	0.43 (W) × 0.43 (H)	mm

Note：For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	-	-0.3	3.6	V
LCD Driver Supply Voltage	V_0 -V_{SS}	-	-0.3	14	V
Input Voltage	V_{IN}	-	-0.3	V_{DD}+0.3	V
Operating Temperature	T_{OP}	-	-20	70	°C
Storage Temperature.	T_{ST}	-	-30	80	°C
Storage Humidity	H_D	Ta < 40 °C	-	90	%RH

1.4 DC Electrical Characteristics

V_{DD} = 2.85 V ± 0.3V, V_{SS} = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	2.55	2.85	3.15	V
"H" Input Voltage	V_{IH}	-	0.8 V_{DD}	-	V_{DD}	V
"L" Input Voltage	V_{IL}	-	V_{SS}	-	0.2 V_{DD}	V
"H" Output Voltage	V_{OH}	-	0.8 V_{DD}	-	V_{DD}	V
"L" Output Voltage	V_{OL}	-	V_{SS}	-	0.2 V_{DD}	V
Supply Current	I_{DD}	V_{DD} = 2.85 V	-	0.5	1.5	mA
LCM Driver Voltage	V_{OP}	V0–V_{SS} (-20°C)	-	-	-	V
		V0–V_{SS} (25°C)	7.7	8.0	8.3	
		V0–V_{SS} (70°C)	-	-	-	

1.5 Optical Characteristics

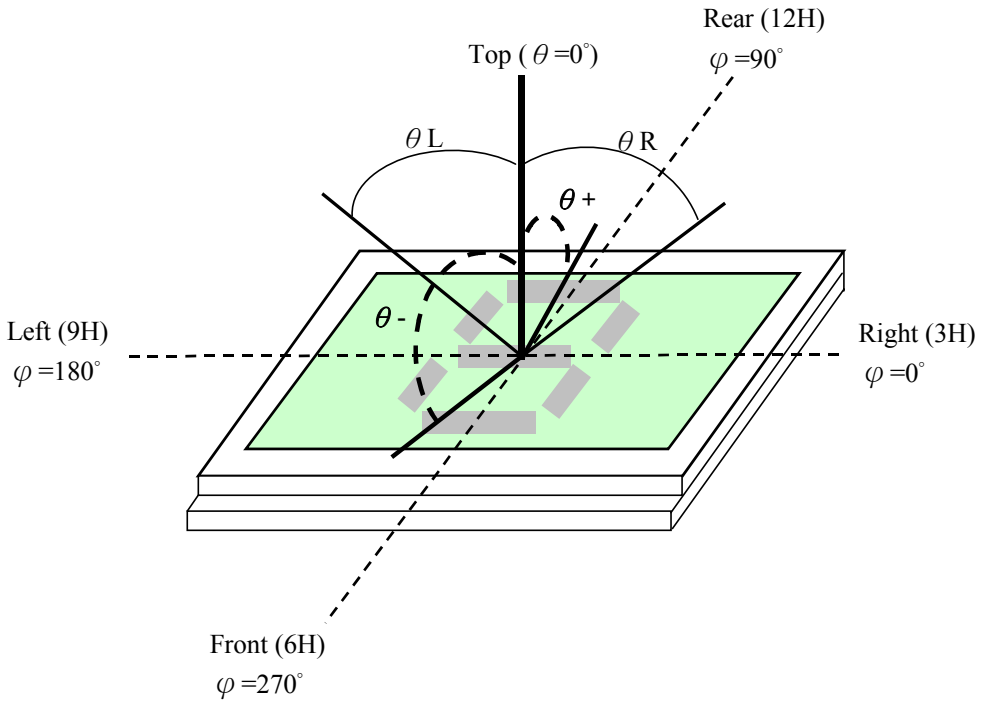
LCD Panel: 1/65 Duty, 1/9 Bias, V_{LCD} = 8.23 V, Ta = 25°C

Item	Symbol	Conditions	Min.	Typ.	Max.	Reference
View Angle	θ	C≥2.0,∅=270°	-40°	-	-	Notes 1 & 2
Contrast Ratio	CR	θ= -5°, ∅=270°	6	8	-	Note 3
Response Time(rise)	Tr	θ= -5°, ∅=270°	-	110 ms	165 ms	Note 4
Response Time(fall)	Tf	θ= -5°, ∅=270°	-	260 ms	390 ms	Note 4

Note 1.

Optical characteristics-2

Viewing angle

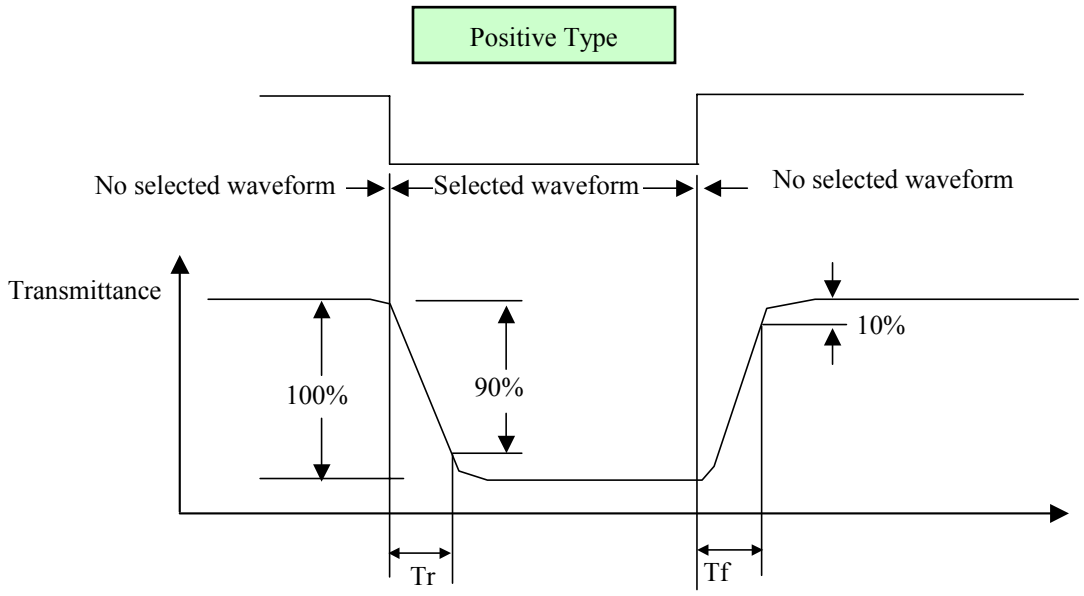


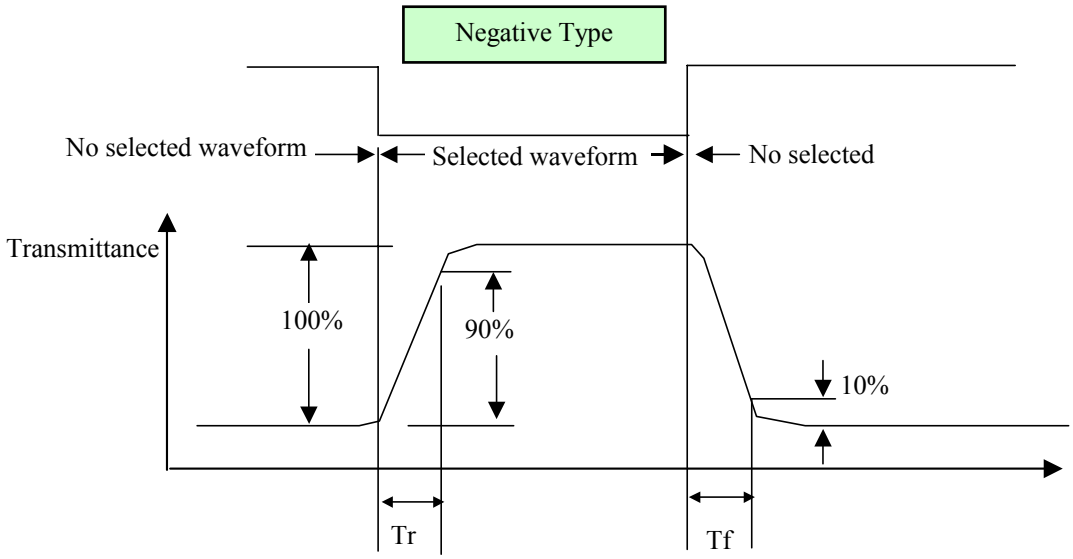
Viewing angle

Note 2.

Optical characteristics-3

Fig.2 Definition of response time





Electrical characteristics-2

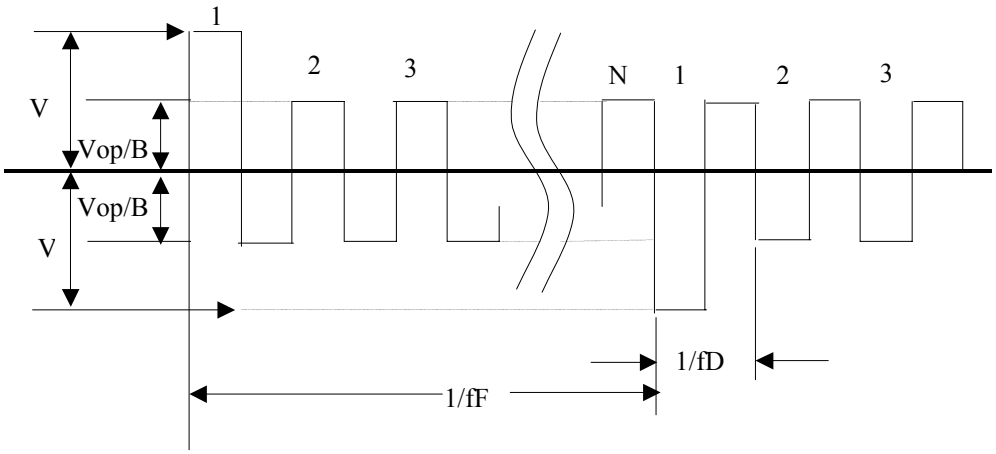
※2 Drive waveform

Vop: Drive voltage fF: Frame frequency

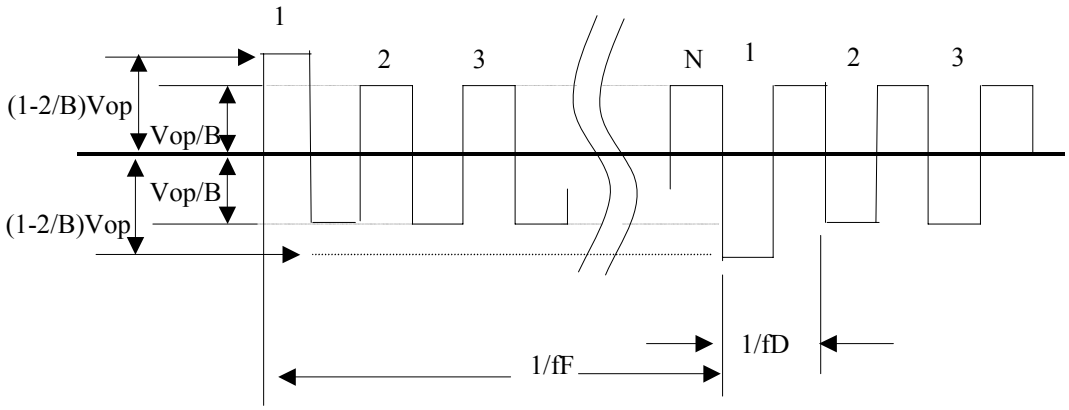
1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



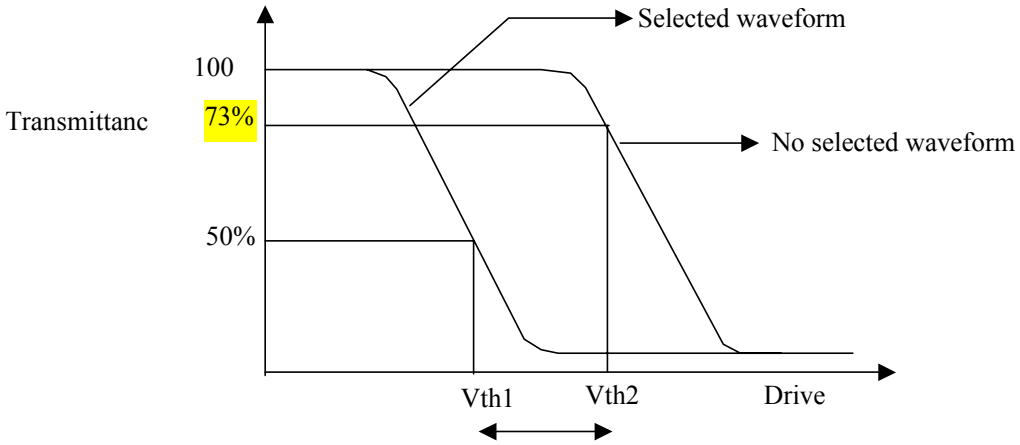
(2) Non- Selected waveform



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period

Note 3. : Definition of Vth

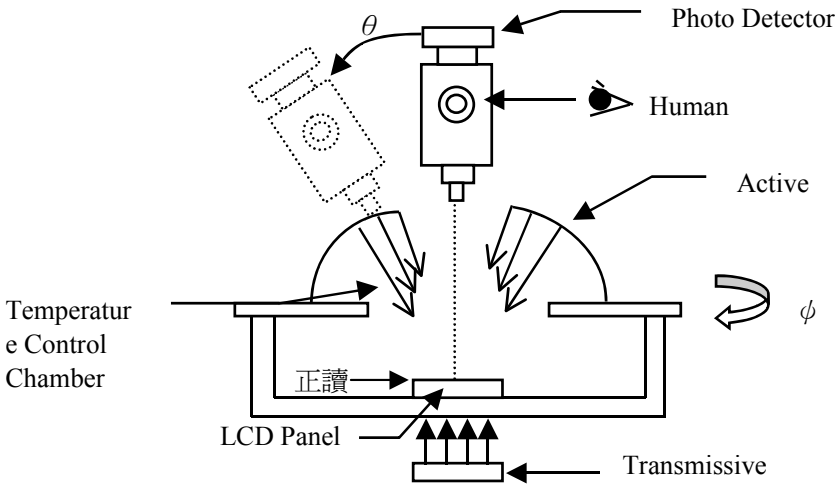


Active voltage range

	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System



Measuring System: Autronic DMS-803

1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Forward Current	IF	Ta = 25 °C	-	160	mA
Reverse Voltage	VR	Ta = 25 °C	-	1	V
Power Dissipation	PO	Ta = 25 °C	-	0.56	W
Operating Temperature	T_{OP}	-	-30	70	℃
Storage Temperature	T_{ST}	-	-40	80	℃
Solder Temp. for 3 Second	-	-	260		℃

Electrical / Optical Characteristics

Ta =25℃

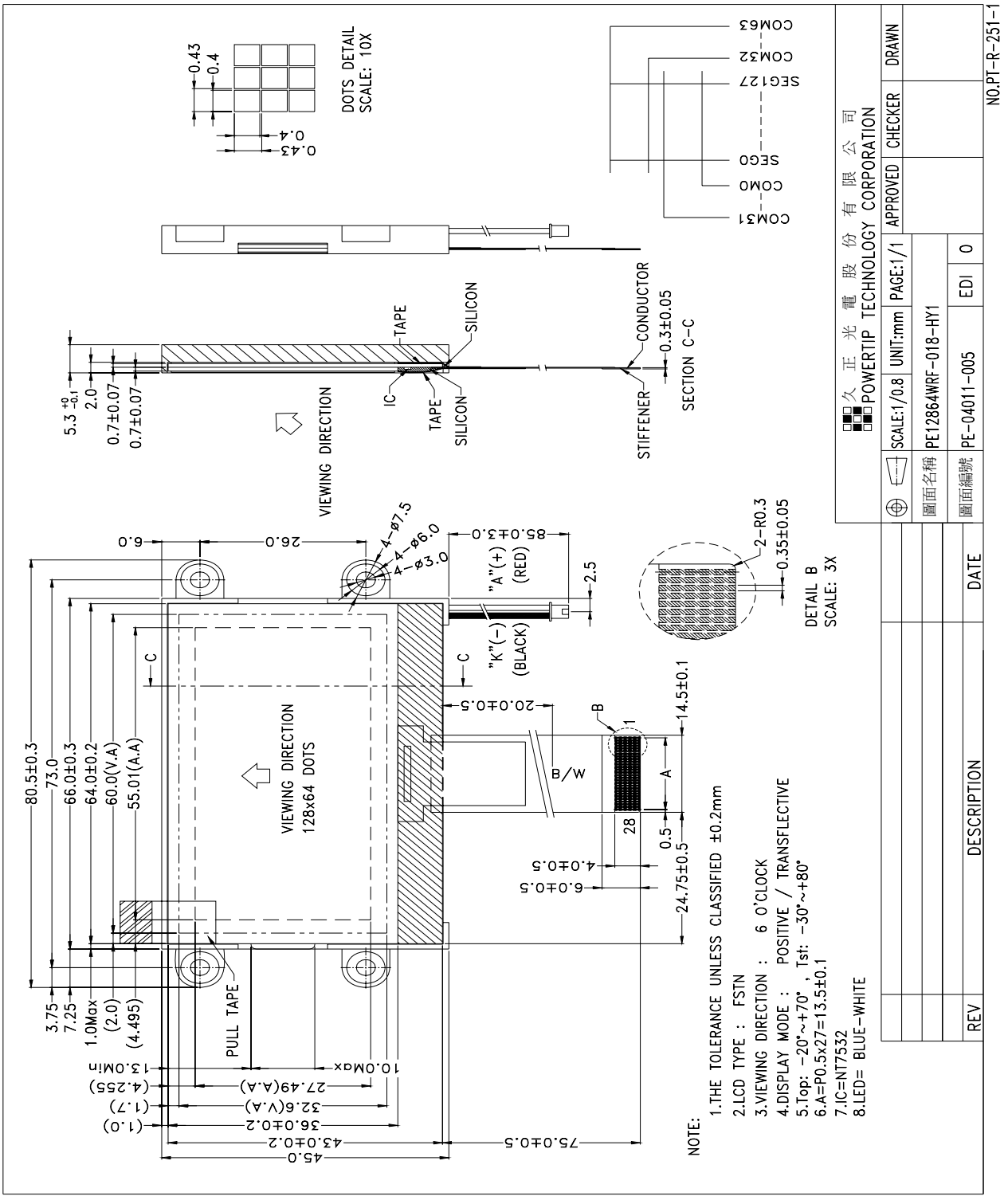
Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF = 40mA	-	3.5	-	V
Reverse Current	IR	VR= 0.8 V	-	-	30	mA
Average Brightness (without LCD) *1	IV	IF = 40 mA	96	128	-	cd/m^2
CIE Color Coordinate (with LCD) *1	X	IF = 40mA	0.17	-	0.22	-
	Y		0.1-2	-	0.18	-
Color	White					

*1 This vaule will be changed while mass production.

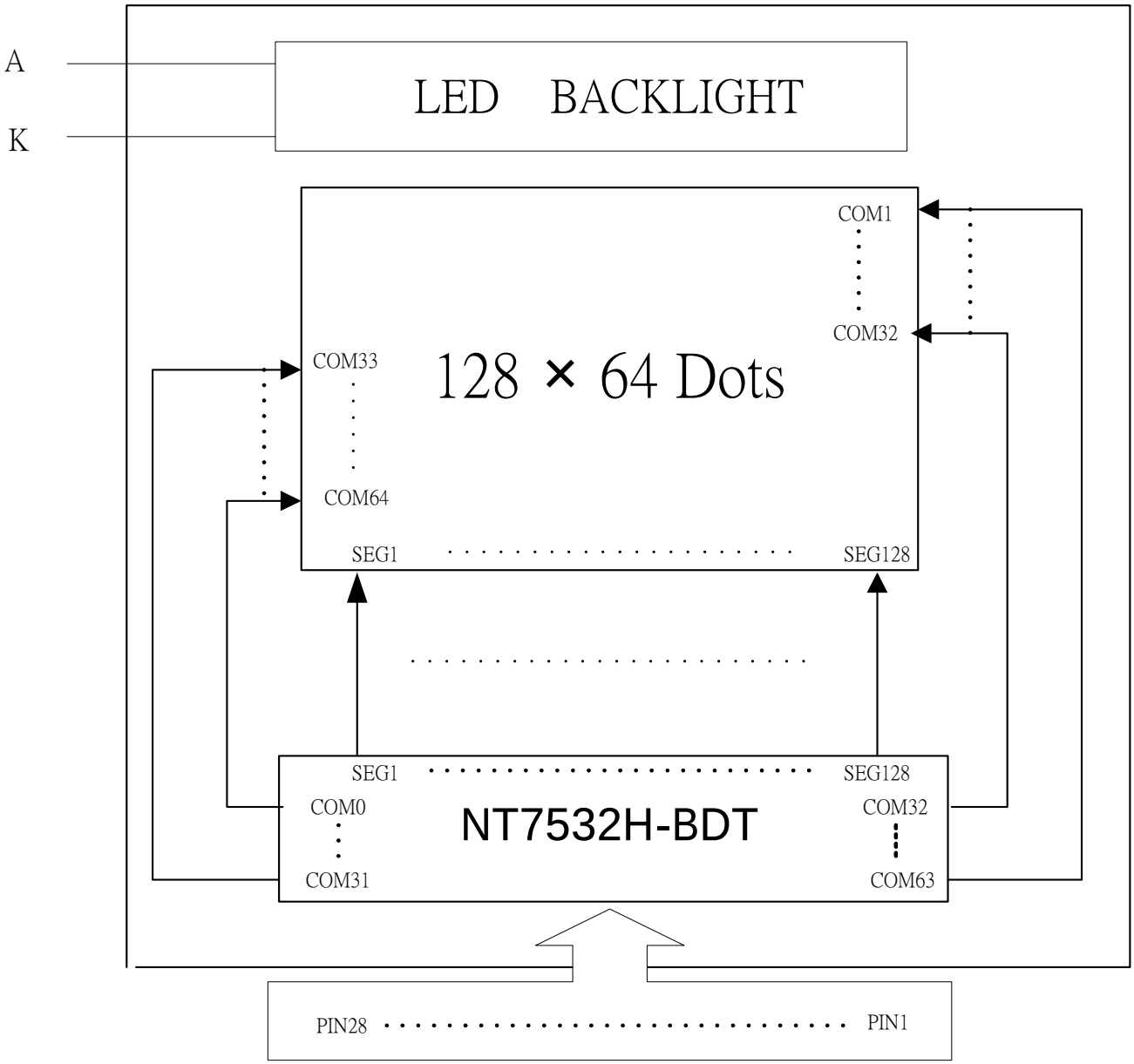
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram



2.1.2 Block Diagram



Prese refer interface pin description for detail

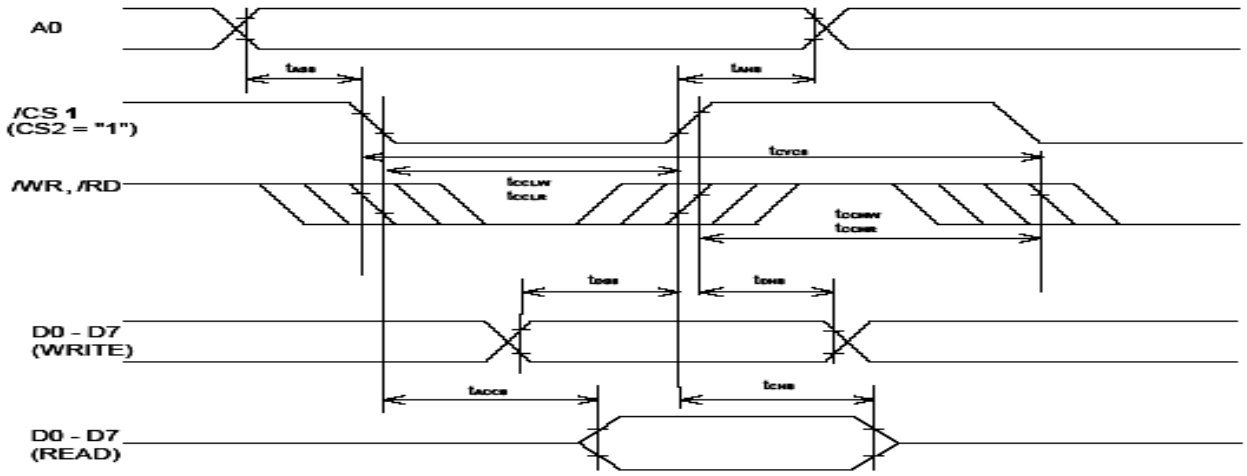
2.2 Interface Pin Description

Pin No.	Symbol	Function
1	CS1	This is the chip select signal. When CS1 = "L" then the chip select becomes active, and data/command I/O is enabled.
2	/RES	/RES is set to "L", the settings are initialized. The /RES signal level performs the reset operation.
3	A0	This is connect to the least significant bit of the normal MPU address bus,and it determines whether the data bits are data or a command. A0 = "H": Indicates that DB0 to DB7 are display data. A0 = "L": Indicates that DB0 to DB7 are control data.
4	/WR	• When connected to an 8080 MPU, this is LOW active. This terminal connects to the 8080 MPU /WR signal. The signals on the data bus are latched at the rising edge of the/ WR signal.
5	/RD	• When connected to an 8080 MPU, this is LOW active. This pin is connected to the /RD signal of the 8080 MPU, and the NT7532 series data bus is in an output status when this signal is "L".
6	DB0	This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus. When the serial interface is selected (P/S = "L"), DB7 serves as the serial data input terminal (SI) and DB6 serves as the serial clock input terminal (SCL). At the same time, DB0 to DB5 are set to high impedance. When the chip select is inactive, DB0 to DB7 are set to high impedance
7	DB1	
8	DB2	
9	DB3	
10	DB4	
11	DB5	
12	DB6	
13	DB7	
14	V_{DD}	Power Supply (V_{DD}=2.85V)
15	V_{SS}	Power Supply (V_{SS}=0)
16	VOUT	DC/DC voltage converter output
17	CAP3+	Capacitor 3+ pad for internal DC/DC voltage converter
18	CAP1-	Capacitor 1- pad for internal DC/DC voltage converter
19	CAP1+	Capacitor 1+ pad for internal DC/DC voltage converter

20	CAP2+	Capacitor 2+ pad for internal DC/DC voltage converter
21	CAP2-	Capacitor 2- pad for internal DC/DC voltage converter
22	V1	LCD driver supply voltages The voltage determined by LCD cell is cell impedance-converted by a resistive driver or an operational amplifier for application.Voltages should be according the following relationship; V0 ≧ V1 ≧ V2 ≧ V3 ≧ V4 ≧ VSS When the internal power circuit is active, these voltages are generated as following table according to the state of LCD Bias command.
23	V2	
24	V3	
25	V4	
26	V0	
27	LCD_ID	no connection
28	IRS	This terminal selects the resistors for the V0 voltage level adjustment. IRS = "H": Use the internal resistors. IRS = "L": Do not use the internal resistors. The V0 voltage level is regulated by an external resistive Voltage divider attached to the VR terminal.This pin is Enabled only when the master operation mode is selected It is fixed to either "H" or "L" when the slave operationmode Is selected

A	+	Power supply LED backlight anode input(+)
K	-	Power supply LED backlight cathode input(-)

2.3 Timing Characteristics



(V_{DD} = 2.7 to 3.0 V)

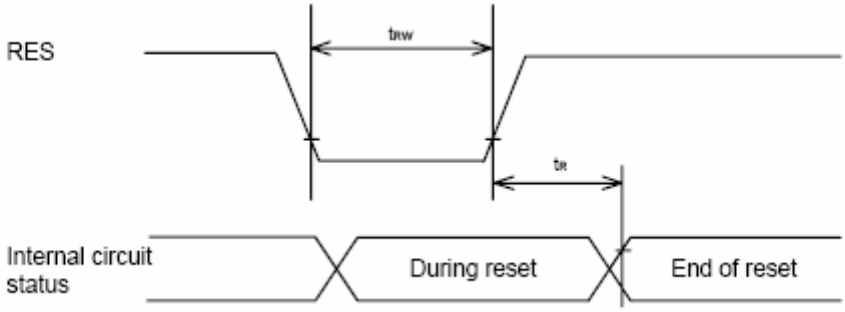
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Address hold time	t_{AH8}	0			ns	
Address setup time	t_{AS8}	0				
System cycle time	t_{CYC8}	300				
Control L pulse width (/WR)	t_{CCLW}	90				
Control L pulse width (/RD)	t_{CCLR}	120				
Control L pulse width (/WR)	t_{CCHW}	120				
Control L pulse width (/RD)	t_{CCHR}	60				
Data setup time	t_{DS8}	40				
Data hold time	t_{DH8}	15				
/RD access time	t_{ACC8}			140		C_L = 100pf
Output disable time	t_{CH8}	10		100		C_L = 100pf

*1. The input signal rise time and fall time (tr,tf) is specified at 15ns or less. When the system cycle time is extremely fast,$(tr+tf) \leqq (t_{CYC8}-t_{CCLW}-t_{CCHW})$ for $(tr+tf) \leqq (t_{CYC8}-t_{CCLR}-t_{CCHR})$ are specified.

*2. All timing is specified using 20% and 80% of V_{DD} as the reference.

*3. t_{CCLW} and t_{CCLR} are specified as the overlap between CS1 being “L” and /WR and /RD being at the “L”level.

Reset Timing



(V_{DD} = 2.7 to 3.0 V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Reset time	t_R			1.0	us	
Reset low pulse width	t_{RW}	1.0				

2.4 Display Command

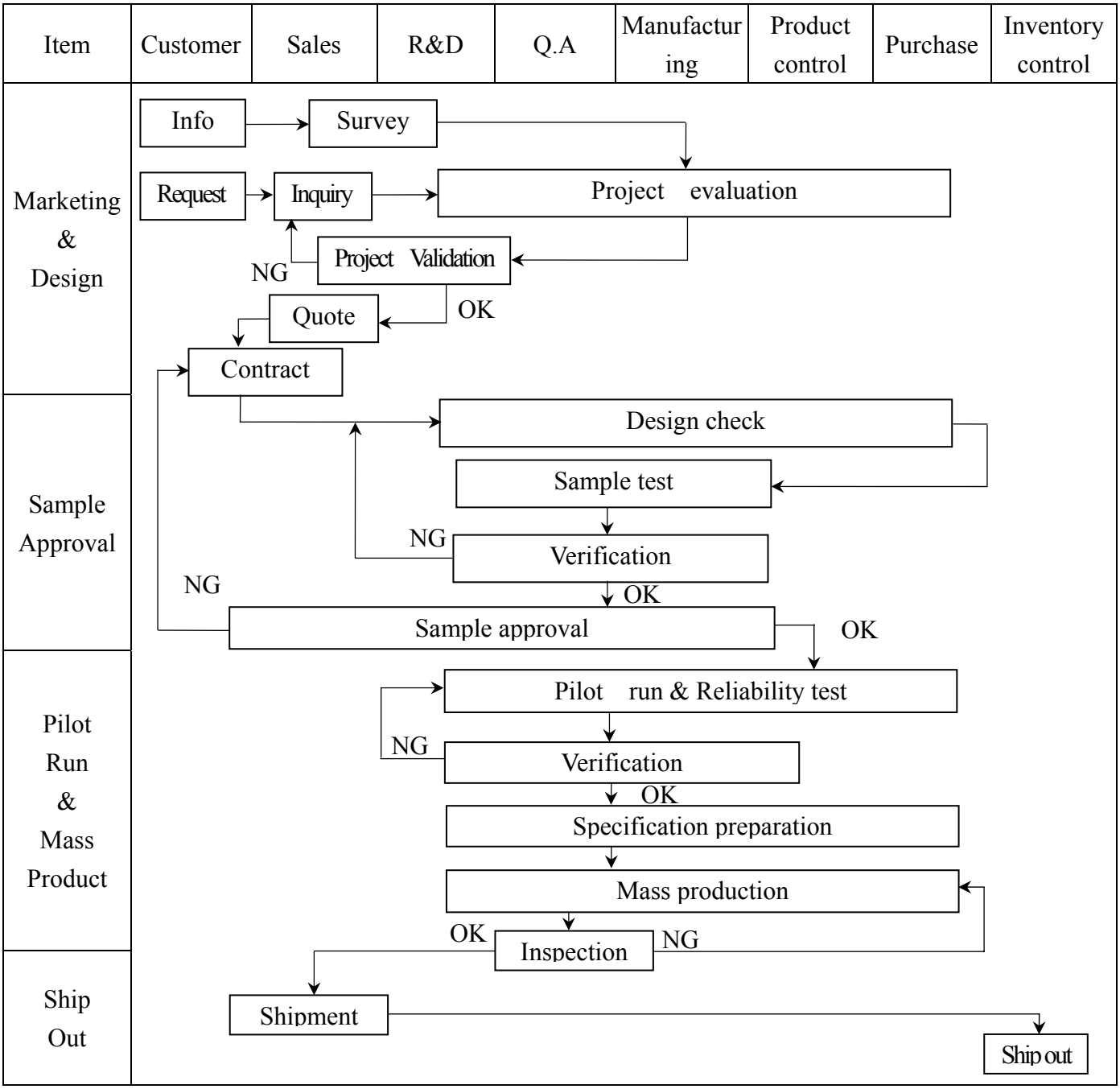
Command	Command Code											Function
	A0	$\overline{RD}$	$\overline{WR}$	D7	D6	D5	D4	D3	D2	D2	D0	
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0	LCD display ON/OFF 0:OFF, 1:ON
											1	
(2) Display start line set	0	1	0	0	1	Display start address						Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	1	Page address				Sets the display RAM page address
(4) Column address set upper bit	0	1	0	0	0	0	1	Most significant column address				Sets the most significant 4 bits of the display RAM column address.
Column address set lower bit	0	1	0	0	0	0	0	Least significant column address				Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1	Status				0	0	0	0	Reads the status data
(6) Display data write	1	1	0	Write data								Writes to the display RAM
(7) Display data read	1	0	1	Read data								Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
											1	
(9)Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0	Sets the LCD display RAM normal/reverse 0: normal, 1: reverse
											1	
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0	Display all points 0: normal display, 1: all points ON
											1	
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0	Sets the LCD drive voltage bias ratio 0: 1/9, 1:1/7
											1	
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset

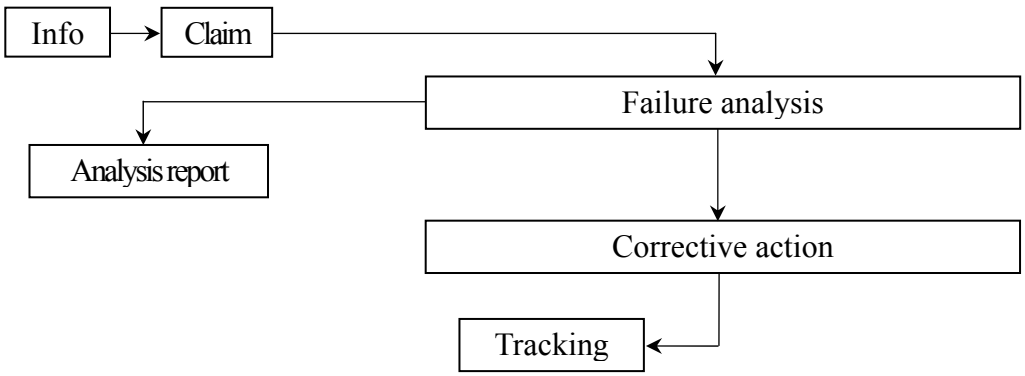
(15) Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 0: normal direction, 1: reverse direction
								1				
(16) Power control set	0	1	0	0	0	1	0	1	Operating mode			Select internal power supply operating mode
(17) V5 voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Resistor ratio			Select internal resistor ratio (Rb/Ra) mode
(18) Electronic volume mode set	0	1	0	1	0	0	0	0	0	0	1	
Electronic volume register set	0	1	0	*	*	Electronic volume value						Set the V5 output voltage electronic volume register.
(19) Static indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0	0: OFF 1: ON
											1	
Static indicator register set	1	0	1	*	*	*	*	*	*	Mode		Set the flashing mode
(20) Power saver												Display OFF and display all points ON compound command
(21) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(22) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

(Note) *: disabled data

3. QUALITY ASSURANCE SYSTEM

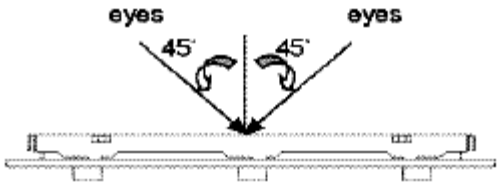
3.1 Quality Assurance Flow Chart

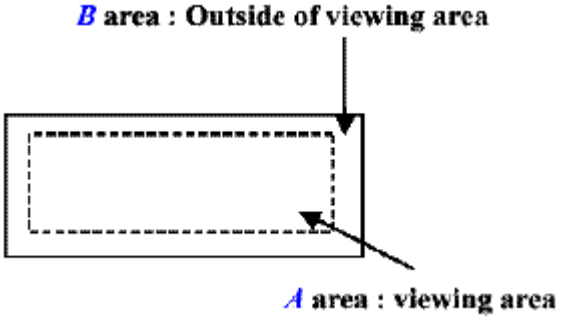


Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service								
Q.A Activity	1. ISO 9001 Maintenance Activities 2. Process improvement proposal 3. Equipment calibration 4. Education And Training Activities 5. Standardization Management							

3.2 Inspection Specification

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ .
◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
◆Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5 .
◆OUT Going Defect Level : Sampling .
◆Manner of appearance test :
(1). The test be under 40W×2 fluorescent light ’ and distance of view must be at 30 cm.
(2). The test direction is base on about around 45° of vertical line. (Fig. 1)
(3). Definition of area . (Fig. 2)

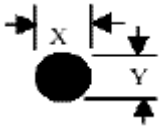


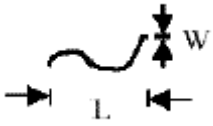


◆ Specification:

NO	Item	Criterion	level
01	Product condition	1.1 The part number is inconsistent with work order of Production.	Major
		1.2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2.1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
04	Electrical Testing	4.1 Missing line character、dot and icon.	Major
		4.2 No function or no display.	Major
		4.3 Output data is error.	Major
		4.4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major
05	Black or white dot、scratch、contamination Round type	5.1 Round type: 5.1.1 display only : • White and black spots on display ≦ 0.25mm, no more than Four white or black spots present. • Densely spaced : NO more than two spots or lines within 3mm	Minor

◆Specification :

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>level</th></tr>
<tr><td>05</td><td>Black or white dot、scratch、contamination
Round type
 

Φ=(x+y)/2
 
</td><td>5.1.2 Nom-display :
<table><tr><td>Dimension (diameter : Φ)</td><td>Acceptance(Q’ty)</td></tr><tr><td>Φ ≦0.10mm</td><td>Accept no dense</td></tr><tr><td>0.10mm＜ Φ ≦0.20mm</td><td>3</td></tr><tr><td>0.20mm＜ Φ ≦0.25mm</td><td>2</td></tr><tr><td>Total</td><td>4</td></tr></table>5.1.3 Line type:
<table><tr><td colspan="2">Dimension (diameter : Φ)</td><td colspan="2">Acceptance (Q’ty)</td></tr><tr><td>Length</td><td>width</td><td>A area</td><td>B area</td></tr><tr><td>---</td><td>w≦0.03mm</td><td>Accept no dense</td><td>Don’t count</td></tr><tr><td>L≦3.0mm</td><td>0.03mm＜ Φ ≦0.05mm</td><td rowspan="2">4</td><td>Don’t count</td></tr><tr><td>L≦2.5mm</td><td>0.05mm＜ Φ ≦0.075mm</td><td>Don’t count</td></tr><tr><td>---</td><td>w＞0.075mm</td><td colspan="2">As round type</td></tr></table></td><td>Minor</td></tr>
<tr><td>06</td><td>Polarizer
Bubble</td><td><table><tr><td rowspan="2">Dimension (diameter : Φ)</td><td colspan="2">Acceptance(Q’ty)</td></tr><tr><td>A area</td><td>B area</td></tr><tr><td>Φ ≦0.20mm</td><td>Accept no dense</td><td>Don’t count</td></tr><tr><td>0.20mm＜ Φ ≦0.50mm</td><td>3</td><td>Don’t count</td></tr><tr><td>0.50mm＜ Φ ≦1.00mm</td><td>2</td><td>Don’t count</td></tr><tr><td>Φ ＞1.00mm</td><td>0</td><td>Don’t count</td></tr><tr><td>Total quantity</td><td>4</td><td>Don’t count</td></tr></table></td><td>Minor</td></tr>
<tr><td>07</td><td>The crack of glass</td><td>● Glass Crack:
7.1 Crack on the circuit of electrode terminal :
 

<table><tr><td></td><td>X</td><td>Y</td><td>Z</td></tr><tr><td>Front</td><td>X≦1/5 a</td><td>Y≦ 1/2 D</td><td>Z≦ t</td></tr><tr><td>Back</td><td colspan="3">Neglect</td></tr></table></td><td>Minor</td></tr>
</table>

◆Specification :

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>07</td><td>The crack of glass

X: The length of Crack

Y: The width of crack

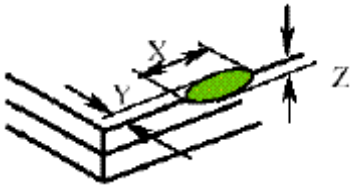
Z: The thickness of crack

D: terminal length

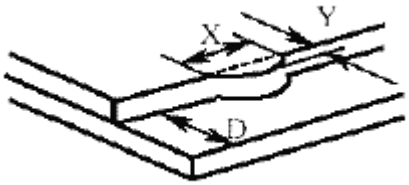
T: The thickness of glass

A : The length of glass</td><td>● Glass Crack:

7.2 General glass crack and corner edge:

7.2.1
 

<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>Neglect</td><td>Out A area</td><td>Neglect</td></tr></table>
7.2.2
 

<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>Neglect</td><td>Out A area</td><td>Neglect</td></tr></table></td><td>Minor</td></tr>
<tr><td></td><td></td><td>7.3 Glass remain:
 

<table><tr><th>X</th><th>Y</th></tr><tr><td>Neglect</td><td>≦ 1/3 d</td></tr></table></td><td>Minor</td></tr>
</table>

◆Specification :

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>Level</th></tr>
<tr><td>07</td><td>The crack of glass

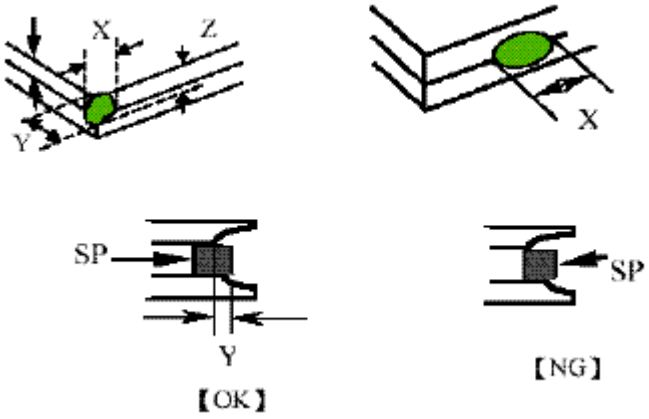
X: The length of Crack

Y: The width of crack

Z: The thickness of crack

D: terminal length

T: The thickness of glass

A : The length of glass</td><td>7.4 Corner crack and medial crack:
 

<table>
<tr><th>X</th><th>Y</th><th>Z</th></tr>
<tr><td>≦1/5a</td><td>Crack can’t enter viewing area</td><td>≦1/2t</td></tr>
<tr><td>≦1/5a</td><td>Crack can’t exceed the half of width of SP width of SP</td><td>1/2t<Z≦2t</td></tr>
</table></td><td>Minor</td></tr>
<tr><td>08</td><td>Backlight elements</td><td>8.1 Backlight can’t work normally.</td><td>Major</td></tr>
<tr><td></td><td></td><td>8.2 Backlight doesn’t light or color is wrong.</td><td>Major</td></tr>
<tr><td></td><td></td><td>8.3 Illumination source flickers when lit.</td><td>Major</td></tr>
<tr><td>09</td><td>General appearance</td><td>9.1 pin type must match type in specification sheet</td><td>Major</td></tr>
<tr><td></td><td></td><td>9.2 No short circuits in components on PCB or FPC</td><td>Major</td></tr>
<tr><td></td><td></td><td>9.3Product packaging must the same as specified on packaging specification sheet.</td><td>Major</td></tr>
<tr><td></td><td></td><td>9.4 The folding and peeled off in polarizer are not acceptable</td><td>Major</td></tr>
<tr><td></td><td></td><td>9.5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≦1.5mm</td><td>Major</td></tr>
</table>

4. RELIABILITY TEST

4.1 Reliability Test Condition

<table>
<tr><th>NO.</th><th>TEST ITEM</th><th>TEST CONDITION</th></tr>
<tr><td>1</td><td>High Temperature Storage Test</td><td>Keep in 80 ±2℃ 96 hrs
Surrounding temperature, then storage at normal condition 4hrs</td></tr>
<tr><td>2</td><td>Low Temperature Storage Test</td><td>Keep in -30 ±2℃ 96 hrs
Surrounding temperature, then storage at normal condition 4hrs</td></tr>
<tr><td>3</td><td>High Humidity Storage</td><td>Keep in +60℃/90%RH duration for 96 hrs
Surrounding temperature, then storage at normal condition 4hrs
(Excluding the polarizer)Or
Keep in +40℃/90%RH duration for 96 hrs
Surrounding temperature, then storage at normal condition 4hrs</td></tr>
<tr><td>4</td><td>Vibration Test</td><td>1. Sine wave 10～55HZ frequency (1 min)
2. The amplitude of vibration :1.5 mm
3. Each direction (XYZ) duration for 2 Hrs</td></tr>
<tr><td rowspan="2">5</td><td rowspan="2">ESD Test</td><td>Air Discharge:
Apply 6 KV with 5 times
Discharge foreach polarity +/-
Contact Discharge:
Apply 250V with 5 times
discharge foreach polarity +/-</td></tr>
<tr><td>1. Temperature ambinace:15℃～35℃
2. Humidity relative:30%～60%
3. Energy Storage Capacitance(Cs+Cd):150pF±10%
4. Discharge Resistance(Rd):330Ω±10%
5. Discharge, mode of operation:
Single Discharge (time between successive discharges at least 1 s)
(Tolerance lf the output voltage indication: ±5%)</td></tr>
<tr><td>6</td><td>Temperature Cycling Test</td><td>-20℃ → 25℃ → 70℃ → 25℃
(30mins) (5mins) (30mins) (5mins)
10 Cycle
Surrounding temperature, then storage at normal condition 4hrs</td></tr>
<tr><td>7</td><td>Vibration Test (Packaged)</td><td>1. Sine wave 10～55HZ frequency (1 min)
2. The amplitude of vibration :1.5 mm
3. Each direction (XYZ) duration for 2 Hrs</td></tr>
<tr><td>8</td><td>Drop Test (Packaged)</td><td><table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>0 ~ 45.4</td><td>122</td></tr><tr><td>45.4 ~ 90.8</td><td>76</td></tr><tr><td>90.8 ~ 454</td><td>61</td></tr><tr><td>Over 454</td><td>46</td></tr></table>Drop direction :※3 comer /1 edges /6 sides etch 1times</td></tr>
</table>

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

5.2.1 Avoid any strong mechanical shock which can break the glass.

5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.

5.2.3 Do not remove the panel or frame from the module.

5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.

5.2.6 Do not touch the display area with bare hands , this will stain the display area.

5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.2.8 To control temperature and time of soldering is 320±10℃and 3-5 sec.

5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

5.3.1 Store the panel or module in a dark place where the temperature is 25℃ ±5℃ and the humidity is below 65% RH.

5.3.2 Do not place the module near organics solvents or corrosive gases.

5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company’s specification as a part for use in your company’s general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

6. PACKING Specification

LCM Model	PE12864WRF-018HY1Q
Drawing NO.	DPK-06830

LCM包裝規格書
LCM Packaging Specifications

Approve	Check	Contact
DATE	初版	版次Ver
06'12'05	06'12'05	0

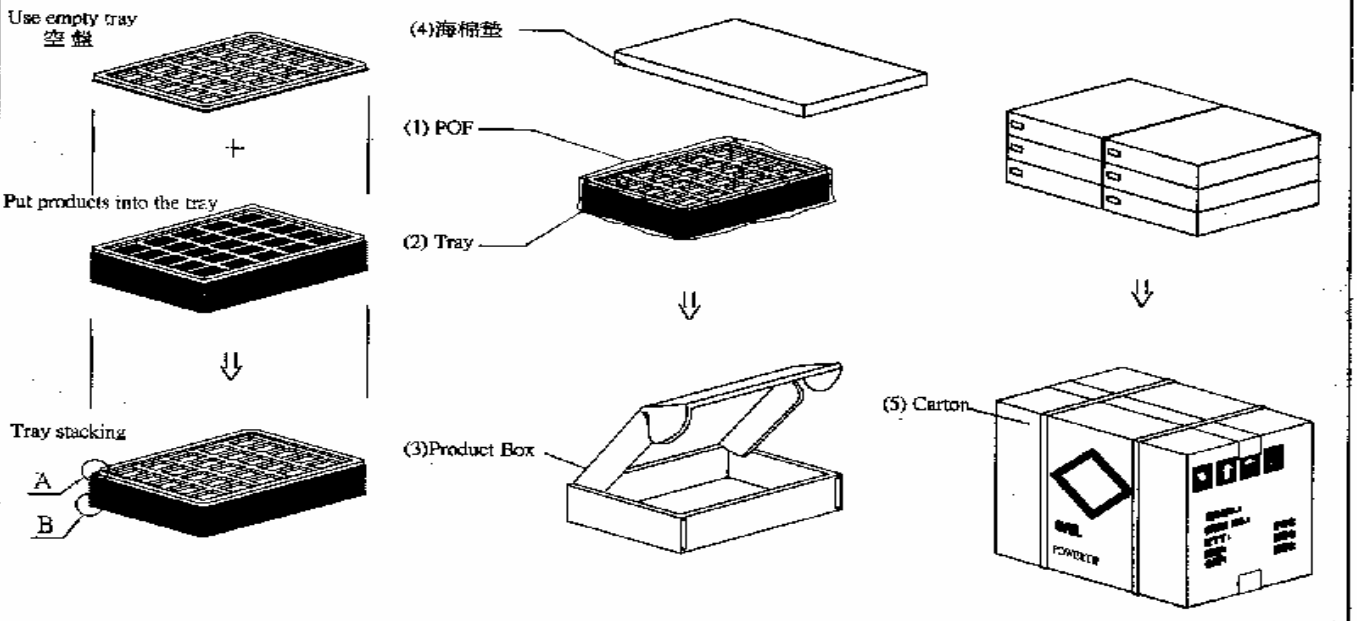
1.包裝材料規格表 (Packaging Material) : (per carton)

No.	Item	Model	Dimensions (mm)	Quantity
1	成品 (LCM)	PE12864WRF-018HY1Q	(64.5*38.6)	288
2	多層薄膜(1)POF	OTFILM0BA03ABA	19"*350*0.015	6
3	TRAY 盤 (2)	TY12806410TZBA	295*245*16.8	54
4	內盒(3)Product Box	BX31025510AABA	310*255*105	6
5	海棉墊(4)	OTFOAM00006ABA	290*240*10	12
6	外紙箱(5)Carton	BX52732536CCBA	527*325*360	1
7				
8				
9				

2.單箱數量規格表(Packaging Specifications and Quantity) :

(1)LCM quantity per box : no per tray 6 x no per tray 8 = 48

(2)Total LCM quantity in carton : quantity per box 48 x no of boxes 6 = 288



特 記 事 項 (REMARK)

1. Label Specifications :

MODEL:
LOT NO:
QUANTITY:
CHECK:

POWERTIP TECH. CORP.