

	SPECIFIC	CATIONS				
CUSTOMER	:	PTC				
SAMPLE CODE	:	SH480272T005-IAB05				
MASS PRODUCTION CODE	:	PH480272T00	5-IAB05			
SAMPLE VERSION	:	01				
SPECIFICATIONS EDITION	:	003				
DRAWING NO. (Ver.)	:	JLMD- PH480	272T005-IAB05_00	2		
PACKAGING NO. (Ver.)	:	JPKG-PH480	272T005-IAB05_00	1		
	Customer		Date:	POWERTIP 2018.01.31 JS RD APPROVED		
Approved	Che	cked	Designer			
閆偉	劉	徐明菲				
Preliminary specificationSpecification for sample a		t				
Р	OWERTIP T	ECH. CORP.				
Headquarters: No.8, 6 th Road, Taichung Industrial Park, Taichung, Taiwan 台中市 407 工業區六路 8 號		TEL: 886-4-2355-81 FAX: 886-4-2355-81				



History of Version

Date	Ver.	Edi.	Description	Page	Design by
11/28/2017	01	001	New Drawing	-	徐明菲
11/30/2017	01	002	Modify Specification(Modify LCM Drawing & 1.6 Backlight Characteristics)	Appendix 8	徐明菲
01/25/2018	01	003	New Sample	-	徐明菲
				1	otal: 32 Page



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Note: For detailed information please refer to IC data sheet:Sitronix--- ST7257



1.1 Features

Item	Standard Value			
Display Type	480 * 3 (RGB) * 272 Dots			
LCD Type	Normally white TN, Transmissive Type			
Screen size(inch)	4.3"(Diagonal)			
Viewing Direction	6 O'clock			
Color configuration	R,G, B vertical stripe			
Backlight	White LED B/L			
Display Interface	Digital 24-bits RGB			
Driver IC	ST7257			
	THIS PRODUCT CONFORMS THE ROHS OF PTC			
ROHS	Detail information please refer website :			
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/			

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 4.09(H)	mm

Touch panel

Item	Standard Value	Unit
Active Area	96.7 (W) * 55.5 (L)	mm

LCD panel

Item	Standard Value	Unit
Active Area	95.04 (W) * 53.86 (L)	mm
Pixel Size	0.198 (W) * 0.198 (H)	mm

Note : For detailed information please refer to LCM drawing.



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDD	GND=0	-0.3	+4.6	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	T _{ST}	-	-30	+80	°C
Storage Humidity	HD	Ta ≦ 60 °C	-	90	%RH

1.4 DC Electrical Characteristics

Module				GND	= 0V, Ta = 2	25°C
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
	VDD	-	3.0	3.3	3.6	V
Power supply	VGH		12	15	16	V
	VGL		-12	-10	-7	V
"H" Input Voltage	VIH		0.7*VDD	-	VDD	V
"L" Input Voltage	VIL	-	GND	-	0.3* GND	V
"H" Output Voltage	VOH	-	VDD-0.4	-	VDD	V
"L" Output Voltage	VOL	-	GND	-	GND +0.4	V
Supply Current	IDD	VDD=3.3V	-	30	45	mA



1.5 Optical Characteristics

TFT LCD Panel

VDD =3.3V, Ta=25°C

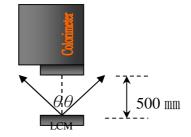
							•	
ltem		Symbol	Condition	Min.	Тур.	Max.	unit	
Response tim	ne	Tr + Tf	-	-	26	39	ms	Note2
	Тор	θY+		-	60	-		
	Bottom	θY-	CD > 10	-	60	-	Dea	Note 4
Viewing angle	Left	θX-	CR ≥ 10	-	60	-	Deg.	Note4
	Right	θX+		-	60	-		
Contrast ratio	0	CR		500	600	-	-	-
	\//bita	Х		0.26	0.31	0.36		
	White	Y		0.29	0.34	0.39		
	Ded	Х		0.55	0.60	0.65		
Color of CIE Coordinate	Red	Y	IF=20mA	0.31	0.36	0.41		Note1
(B/L & LCD & TP)	Croop	Х		0.30	0.35	0.40	-	NOLET
	Green	Y		0.53	0.58	0.63		
	Blue	Х		0.10	0.15	0.20		
	Diue	Y		0.04	0.09	0.14		
Average Brightr	ness							
Pattern=white dis	splay	IV	IF=20mA	180	260	-	cd/m²	Note1
(B/L & LCD & 1	ΓP)							
Uniformity		∆B	IF=20mA	70	-	-	%	Note1

Note1:

 $1 : \triangle B=B(min) / B(max) \times 100\%$

- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 mm \rightarrow (0= 0°)
 - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01 , Average Brightness ± 4%





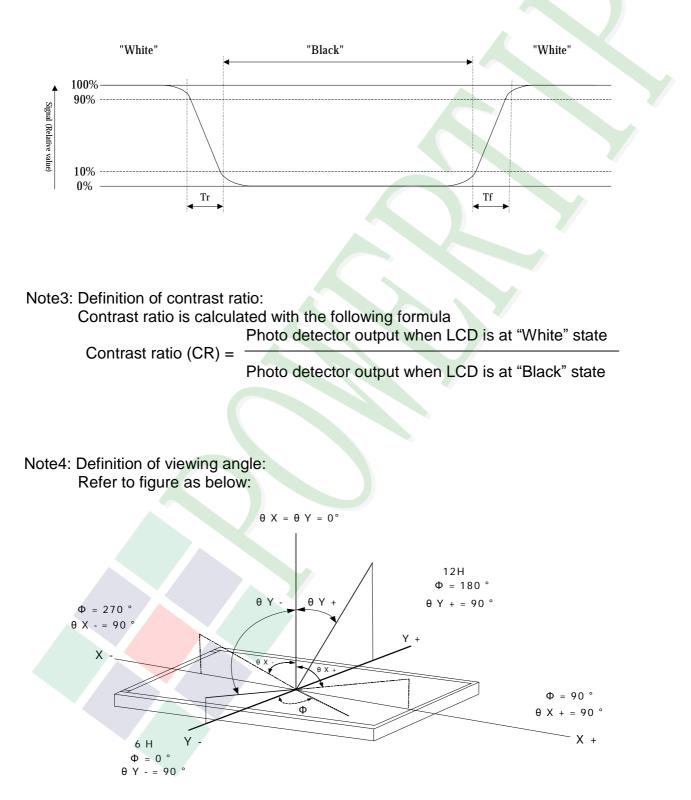
Colorimeter=BM-7 fast



Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:





1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current (Each LED)	IF	Ta =25℃	-	30	mA
LED Reverse Voltage (Each LED)	VR	Ta =25℃	-	5	V
Power Dissipation	PD	Ta =25℃	-	100	mW

Electrical / Optical Characteristics

•						
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		18.2	22.8	24.5	V
Average Brightness (Without LCD)	IV	IF=20mA	4500	5400	-	cd/m ²
CIE Color Coordinate	Х		0.26	0.29	0.32	
(Without LCD)	Y		0.26	0.29	0.32	-
Color			White			

Internal Circuit

PIN(K) PIN(A) ∽ Ď Ê

Other Description

Item	Conditions	Description
Life Time*1	Ta =25℃ IF= 20mA	50,000 hrs

*1 : The "LED life time" is defined as the module brightness decrease to 50% original

brightness at Ta=25°C and IL=20mA. The LED lifetime could be decreased if operating IL is lager than 20 mA.



1.7 Touch Panel Characteristics

1.7.1 Optical Characteristics

Item	Specification
1.Transparency	(80%) Min

1.7.2 Mechanical Characteristic

Item	Specification
1.Input Method	Finger or stylus pen
2.Hardness of surface	3H -pressure 500g of ,45deg.
3.FPC peeling strength	500gf min(Peeling upward by 90°)
4.Activation Force	50gf(Typical 20gf) less individual point with stylus pen(R0.8mm)
	Activation force guarantee area:5.0mm inside of Active Area.
5.Linearity Force	100gf less input with stylus pen(R0.8mm)
	Linearity force guarantee area:3.0mm inside of Active Area.

1.7.3 Electrical Characteristics

Item	Specification
1.Rated Voltage	DC 5V(DC 7V Max)
2.Resistance Between	Direction X (Glass side): 350Ω~ 1240Ω
Terminals.	Direction Y (Film side): 160Ω~ 640Ω
3.Insulation Resistance	20 M Ω or more (DC 25 V 1min)
4.Linearity	 ≤1.5%. Linearity(%)= ΔV/ (EV-SV) *100. ΔV: The difference between the ideal voltage and measured voltage on the each measuring line. SV: Voltage of starting Points. EV: Voltage of Ending Points.
5.Bouncing	<10ms (Tip R 3.75mm, hardness 10°~20°, silicon rubber ,500gf operation : 40 mm/sec)



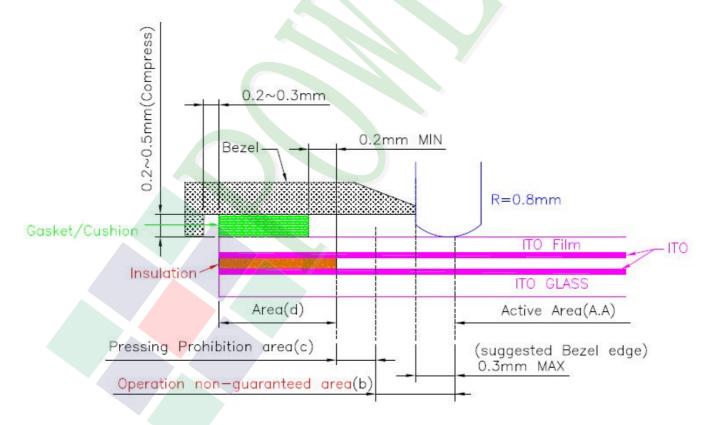
1.7.4 Reliability Characteristic

Item	Specification
1.Hitting Durability	1,000,000times min.(Tip R 8mm&R0.8mm)
2.Pen Sliding Durability	100,000 times min(Tip R0.8mm).
3.Impact Resistance	No damage when ψ 9mm steel ball is dropped on the surface from 30 cm height at 1 time.
4.Flexible pattern Bending	Bending 3 times by bending radius R1.0 mm
Resistance	
5.Flexible Pattern Insert/Pull	
Out Resistance	5times at least .



1.7.5 Touch Panel Design/Handing Guide

- (1) Keep the gap, for example 0.2 to 0.3mm, between bezel edge and T/P edge.
 The reason is to avoid the bezel edge from contacting T/P surface that may cause "short" with bottom layer
- (2) Insertion a cushion material is recommended.
- (3) The cushion material should be limited on the busbar insulation paste area. If it is over the transparent insulation paste area, a "short" may be occurred.
- (4) Do not to use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- (5) Never expand the T/P top layer (PET Film) like a balloon by internal air pressure. The life of the T/P will extremely decreasing.
- (6) Top layer, PET, dimension is changing base on environment temperature and humidity. Please avoid a stress from housing bezel to top layer, because it may cause "waving".
- (7) The input to the Touch Panel sometimes distorts touch panel itself.
- (8)To use the stylus pen or fingernail sliding at the edge of the housing is prohibited. It would cause the cracking of the ITO coating and damage the touch panel. It also request not to press this area while assembling
- (9) Purpose: In order to prevent accidental use and performance deterioration, please keep the following precautions.



In order to prevent unusual performance degradation and malfunction of a touch panel, please carry out the set case designing and a touch panel assembling method after surely considering the definition of each area illustrated in above figure.



Area(a) : Active area

The active area is guaranteed the position data detectable precision, operation force and other operations. it is strongly recommended to place the operation button or menu keys within the active area. Due to structure, the active area is less durable at the edge or close to the edge.

Area(b) : Operation non-guaranteed area

This area does not guarantee a touch panel operation and its function. When this area is pressed, touch panel shows degradation of its performance and durability such as a pen sliding durability becomes about one-tenth compared with the active area (area-(a) as guaranteed area) and its operation force requires about double. About 0.5 mm outside from a boundary of the active area corresponds to this area.

Area(c) : Pressing prohibition area

The area which forbids pressing, because an excessive load is applied to a transparent electrode (ITO) and a serious damage is given to a touch panel function by pressing. About 0.5 mm outside from Operation non-guaranteed area.

Area(d) : Non-Active area The area does not activate even if pressed.



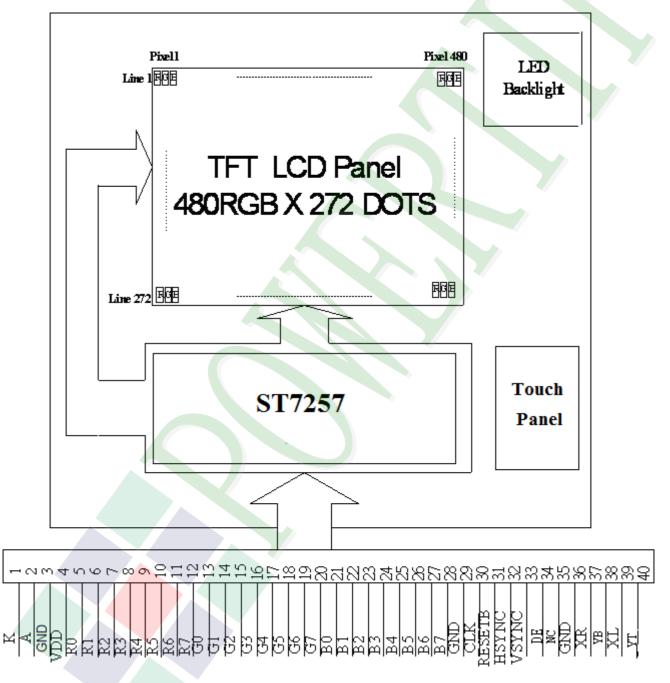
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	А	Power supply for LED Backlight anode input
3	GND	Ground
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7

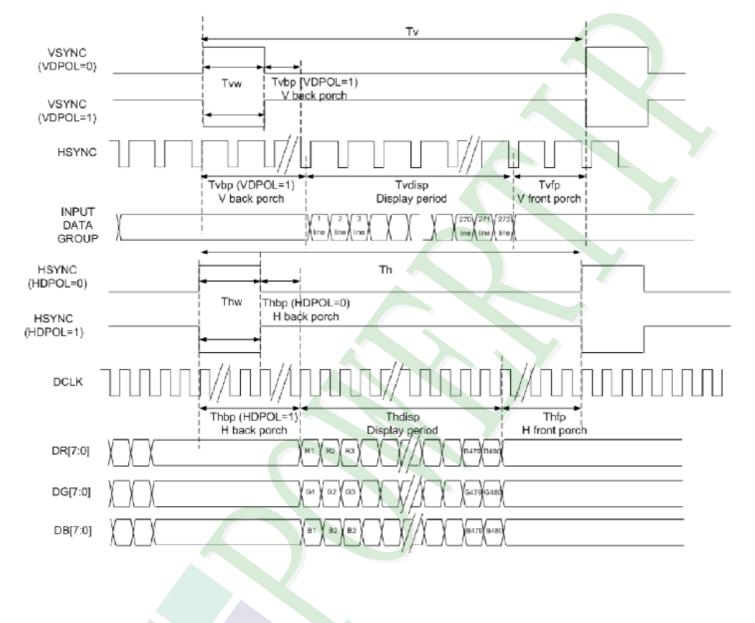


Pin No.	Symbol	Function
21	B0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	B3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	DISP	Display control / standby mode selection "High" : Normal display
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input
35	N.C	Not Connect.
36	GND	Ground
37	XR	TOUCH PANEL RIGHT
38	YB	TOUCH PANEL DOWN
39	XL	TOUCH PANEL LEFT
40	YT	TOUCH PANEL TOP



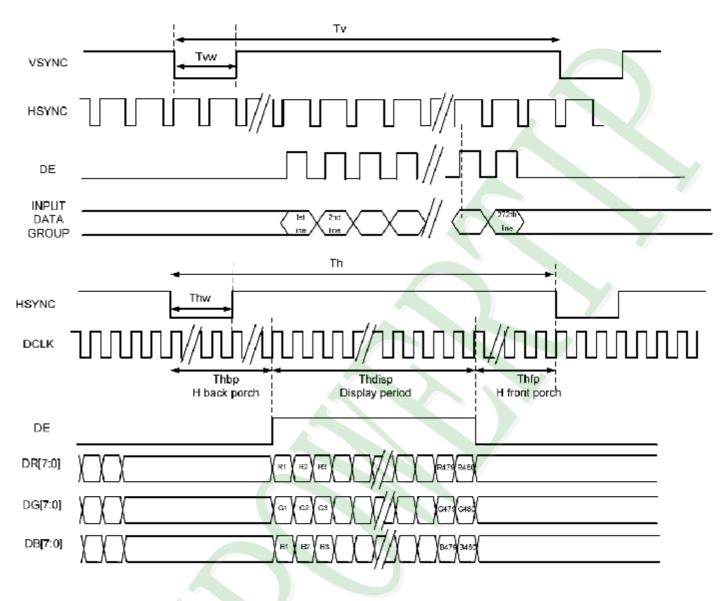
2.3 Timing Characteristics

2.3.1 SYNC Mode



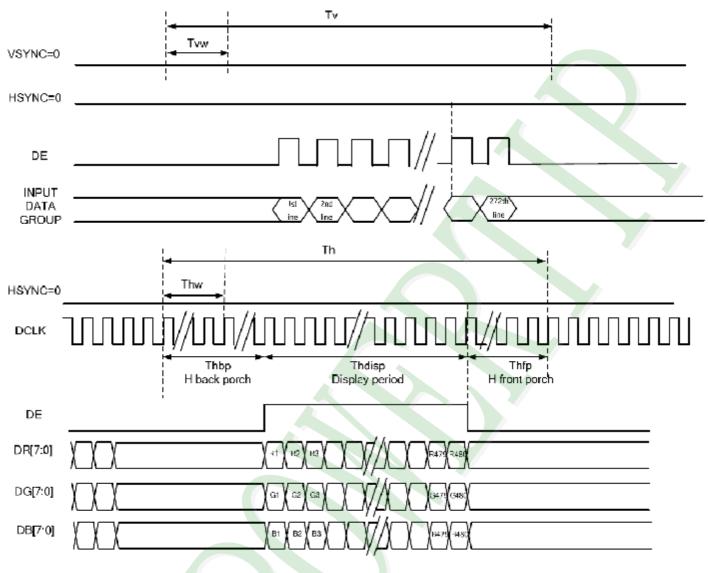


2.3.2 SYNC-DE Mode





2.3.3 DE Mode





2.3.4 Parallel 24-bit RGB Input Timing Table

	480RGB X 272 Resolution Timing Table							
	Item	Symbol	Min.	Тур.	Max.	Unit	Remark	
DCLK Free	luency	Fclk	8	9	12	MHz		
DCLK Peri	od	Tclk	83	111	125	ns		
HSYNC	Period Time	Th	485	531	598			
	Display Period	Thdisp		480		DCLK		
	Back Porch	Thbp	3	43	43	DCLK	By H_Blanking setting	
	Front Porch	Thfp	2	8	75	DCLK		
	Pulse Width	Thw	2	4	75	DCLK		
VSYNC	Period Time	Τv	276	292	321	н		
	Display Period	Tvdisp		272		Н		
	Back Porch	Tvbp	2	12	12	H	By V_Blanking setting	
	Front Porch	Tvfp	2	8	37	Н		
	Pulse Width	Tvw	2	4	37	H	/	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

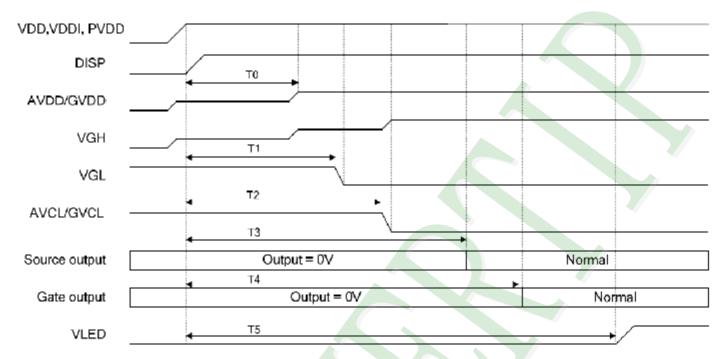
		480RGB X 2	40 Reso	olution	Timing 1	Tabl e	
	ltem	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLK Free	quency	Fclk	8	9	12	MHz	
DCLK Peri	od	Tçik	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	75	DCLK	
VSYNC	Period Time	Τv	244	260	321	Н	
	Display Period	Tvdisp		240		Н	
	Back Porch	Tvbp	2	12	12	Н	By V_Blanking setting
	Front Porch	Tvfp	2	8	37	н	
	Pulse Width	Tvw	2	4	37	Н	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.



2.3.5 Power Sequence

POWER ON

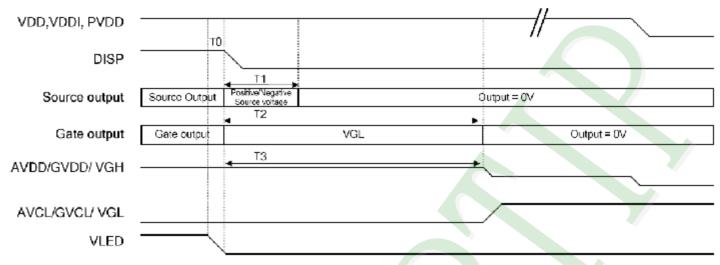


Symbol	Description	Min. Time	Unit
T0	DISP="High" to AVDD/GVDD voltage stability	40	ms
T1	DISP="High" to VGL voltage stability	50	ms
T2	DISP="High" to AVCL/GVCL stability	70	ms
Т3	DISP="High" to Source output	100	ms
T4	DISP="High" to Gate output	110	ms
Т5	Black Turn on	130	ms





POWER OFF

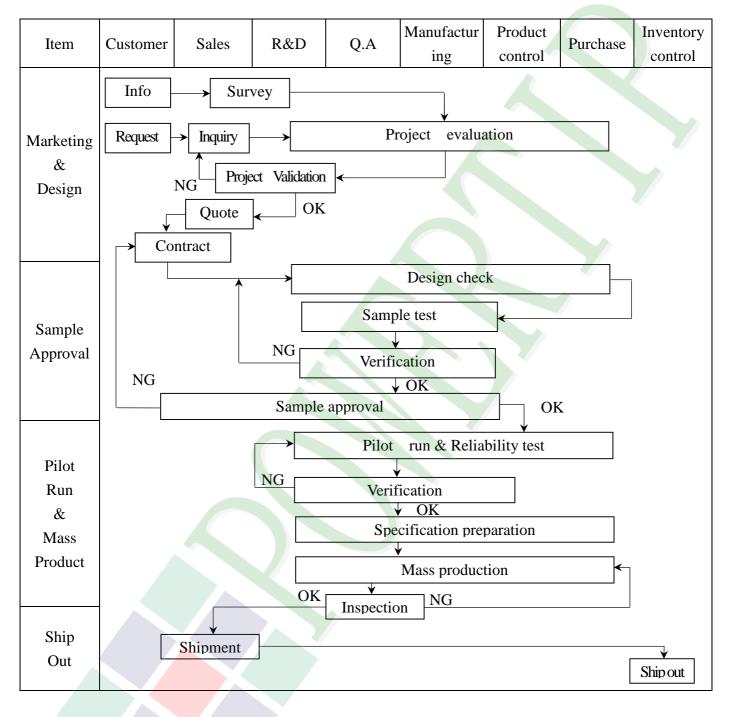


Symbol	Description	Min. Time	Unit
T0	Backlight turn off to DISP="Low"	5	ms
T1	DISP="Low" to Source output disable	20	ms
T2	DISP="Low" to Gate output disable	50	ms
Т3	DISP="Low" to Gate output disable	50	ms



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



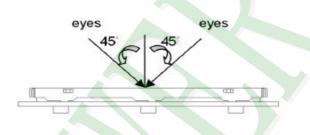


Item	Customer	Sales	R&D	Q.A	Manufactu ring	Product control	Purchase	Inventory control
Sales Service	Info Analys	→ Claim sis report	[Trackin	Failure an Corrective			
Q.A Activity	 ISO 9001 Equipment Standardi 		n		ocess improv Education An			

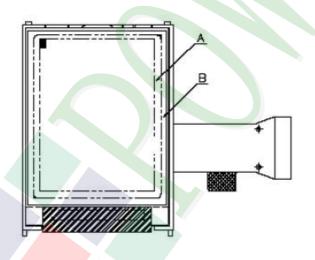
POWERTIP

3.2. Inspection Specification

- Scope : The document shall be applied to TFT-LCD Module for 3. 5" ~15" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ♦OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test:
 - a. Manner of appearance test :
 - (1). The test best be under 20W×2 fluorescent light , and distance of view must be at 30 cm.
 - (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



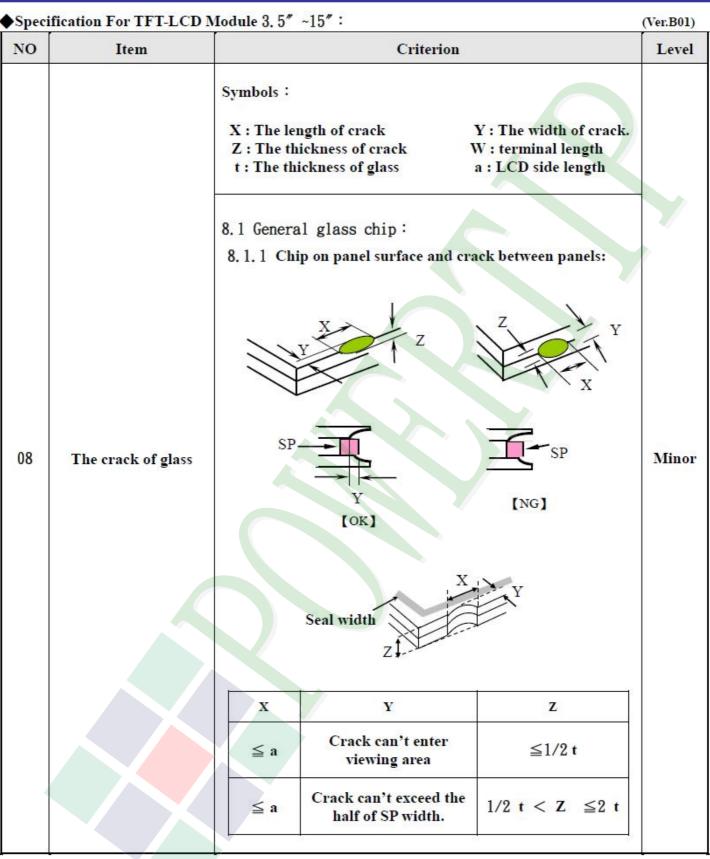
◆Specification For TFT-LCD Module 3. 5" ~15":

NO	Item	Criterion					
NO	Item		Level				
		1. 1The part number is inconsistent with work order of production.					
01	Product condition	1. 2 Mixed product types.	Major				
		1. 3 Assembled in inverse direction.	Major				
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major				
03	Outline dimension	3.1 Product dimension and structure must conform to structur diagram.	e Major				
		4.1 Missing line character and icon.	Major				
04		4. 2 No function or no display.					
	Electrical Testing	4. 3 Display malfunction.					
		4.4 LCD viewing angle defect.					
		4.5 Current consumption exceeds product specifications.					
		4. 6 Mura can not be seen through 5% ND filter. (Mura : Under the normal examination angle of view,the picture has the non-uniform phenomenon.)					
	Č.		0				
		Item Acceptance (Q'ty)					
		$\begin{array}{c c} \text{Bright Dot} & \leq 4 \end{array}$					
	Dot defect	Dot Dark Dot ≦ 5					
		Defect Joint Dot ≦ 3					
05	(Bright dot ` Dark dot)	Total ≤ 7	Minor				
	On -display	5.1 Inspection pattern : full white , full black , Red , Green and					
		blue screens.					
		5. 2 It is defined as dot defect if defect area $>1/2$ dot.					
		5. 3 The distance between two dot defect ≥ 5 mm.					
		5.4 Bright dot that can not be seen through 5% ND filter	i -				

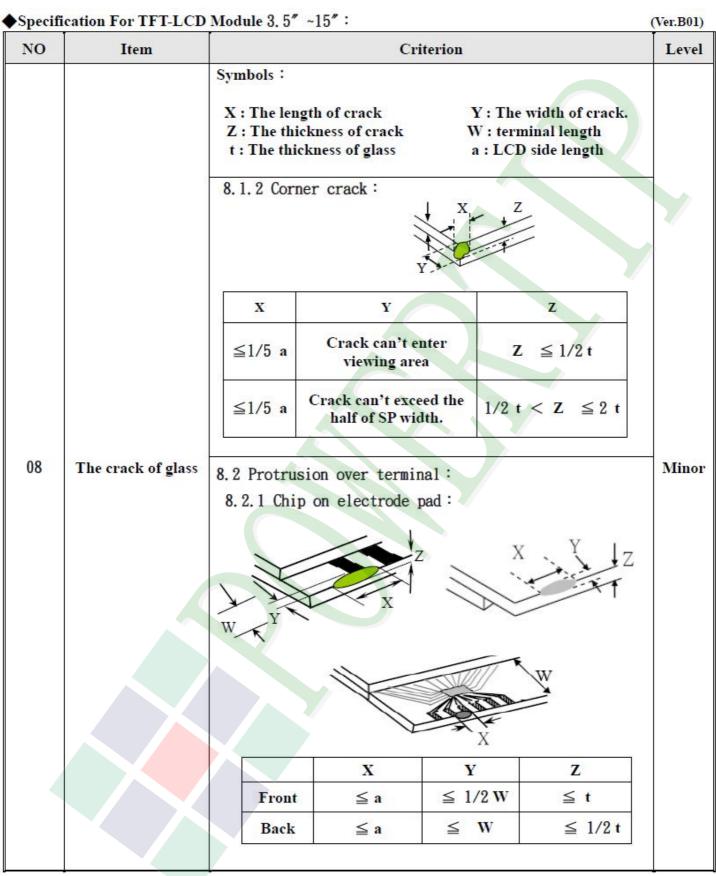


♦ Spee	cification For TF	-LCD Module 3, 5	o‴~15″:					(Ver.B01)	
NO	Item	Criterion						Level	
	Black or white dot \cdot scratch \cdot contamination Round type $\downarrow x \qquad \downarrow \\ \hline y \\ \hline \\ \Phi = (x+y)/2$ Line type $\downarrow L \qquad \downarrow W$	6. 1 Round type (Non-display or display) :Dimension (diameter : Φ)Acceptance (Q'ty)A areaB area $\Phi \leq 0.25$ Ignore $0.25 < \Phi \leq 0.50$ 5 $\Phi > 0.50$ 0Total56. 2 Line type(Non-display or display) :							
06		module size	Length (L) 		idth (W) $W \leq 0.03$	Acceptance A area Ignore	e (Q'ty) B area	Minor	
		3.5" to less 9"	L ≦10.0 L ≦5.0	34,012 3,57	$\frac{\langle W \leq 0.05}{\langle W \leq 0.10}$ $W > 0.10$	4 2 As round type	Ignore		
				Total		5			
		→ _L •–	9" to 15"	 L ≦10.0	0.05	$\frac{W \leq 0.05}{\langle W \leq 0.10}$	Ignore 5 As round	Ignore	
						type 5	-gnore		
						I			
		Dimension	(diameter :	Φ)	Accepta A area	nce (Q'ty) B ar	ea		
	Polarizer Bubble		$\Phi \leq 0.25$ $0.25 < \Phi \leq 0.50$		Ignore		Minor		
07		0.25 <			4	Ignore			
		$0.50 < \Phi \leq 0.80$			1				
			$\Phi > 0.80$		0				
			[otal		5				











◆Specification For TFT-LCD Module 3. 5″~15″:

) Itom Cuitanian	(Ver.B01)
	Leve
D Item Criterion Symbols : X: The length of crack. Y: The width of crack. Z: The thickness of glass a: LCD side length 8. 2. 2 Non-conductive portion : V V V V V X Y X V X V X V X V X Y X X X Y X <th>Leve</th>	Leve



◆Specification For TFT-LCD Module 3. 5" ~15":

NO Item		Criterion		
09	Backlight elements	9. 1 Backlight can't work normally.		
		9. 2 Backlight doesn't light or color is wrong.	Majoi	
		9. 3 Illumination source flickers when lit.	Majoi	
	General appearance	10. 1 Pin type ` quantity ` dimension must match type in structure diagram.	Majoi	
		10. 2 No short circuits in components on PCB or FPC .		
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Majo	
10		10. 4 Product packaging must the same as specified on packaging specification sheet.		
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor	
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Mino	



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

CST ITEM Temperature orage Test Temperature orage Test Temperature / h Humidity orage Test rature Cycling orage Test	Surroundin Keep in -30 Surroundin Keep in +60 Surroundin Surroundin Air Dischar Apply 15 K	D ±2°C 240 hrs g temperature, then sto ±2°C 240 hrs g temperature, then sto °C / 90% R.H duration g temperature, then sto $-30°C \rightarrow +25°C -$ (30mins) (5mins) ↓ 10 C g temperature, then sto	brage at normal condition 4h → $+80^{\circ}C \rightarrow +25^{\circ}C$ (30mins) (5mins)	Irs.
orage Test Temperature orage Test Temperature / h Humidity orage Test	Surroundin Keep in -30 Surroundin Keep in +60 Surroundin Surroundin Air Dischar Apply 15 K	g temperature, then sto $\pm 2^{\circ}$ C 240 hrs g temperature, then sto $^{\circ}$ C / 90% R.H duration g temperature, then sto -30° C $\rightarrow +25^{\circ}$ C - (30mins) (5mins) 4 10 C g temperature, then sto ge:	prage at normal condition 4 a for 240 hrs prage at normal condition 4 $\Rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ (30mins) (5mins) Cycle prage at normal condition 4	Irs.
orage Test Femperature / h Humidity orage Test rature Cycling	Surroundin Keep in +60 Surroundin Surroundin Air Dischar Apply 15 K	g temperature, then sto $^{\circ}$ C / 90% R.H duration g temperature, then sto -30° C \rightarrow +25 $^{\circ}$ C - (30mins) (5mins) 10 C g temperature, then sto ge:	th for 240 hrs prage at normal condition 44 $\Rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ (30mins) (5mins) Cycle prage at normal condition 44	ırs.
h Humidity orage Test rature Cycling	Surrounding Surrounding Air Dischar Apply 15 K	g temperature, then sto $-30^{\circ}C \rightarrow +25^{\circ}C -$ (30 mins) (5 mins) 10 C g temperature, then sto ge:	brage at normal condition 4 $\Rightarrow +80^{\circ}C \rightarrow +25^{\circ}C$ (30mins) (5mins) Cycle brage at normal condition 4	
• 0	Surrounding Air Discharg Apply 15 KV	(30mins) (5mins) 10 C g temperature, then sto ge:	(30mins) (5mins) Cycle brage at normal condition 44	Irs.
	Apply 15 K		Contact Discharge:	
ESD Test	 Humidit Energy S Discharg Discharg Single Disc 	or each polarity +/- ature ambiance : 15°C - y relative : 30%~60% Storage Capacitance(Ca ge Resistance(Rd) : 330 ge, mode of operation :	s+Cd) : 150 pF±10% Ω±10% successive discharges at leas	+/-
ration Test Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 			
Prop Test Packaged)	Drop Direct	0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454	122 76 61 46	
Pa Dr	rop Test	ation Test ackaged) 2. The amp 3. Each di cop Test ackaged)	ation Test ackaged)2. The amplitude of vibration :1.!3. Each direction (X \ Y \ Z) dur90 Test ackaged)90.8 ~ 454 0ver 454	ation Test ackaged)2. The amplitude of vibration :1.5 mm 3. Each direction (X \ Y \ Z) duration for 2 HrsPacking Weight (Kg)Drop Height (cm)0 ~ 45.412245.4 ~ 90.87690.8 ~ 45461



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

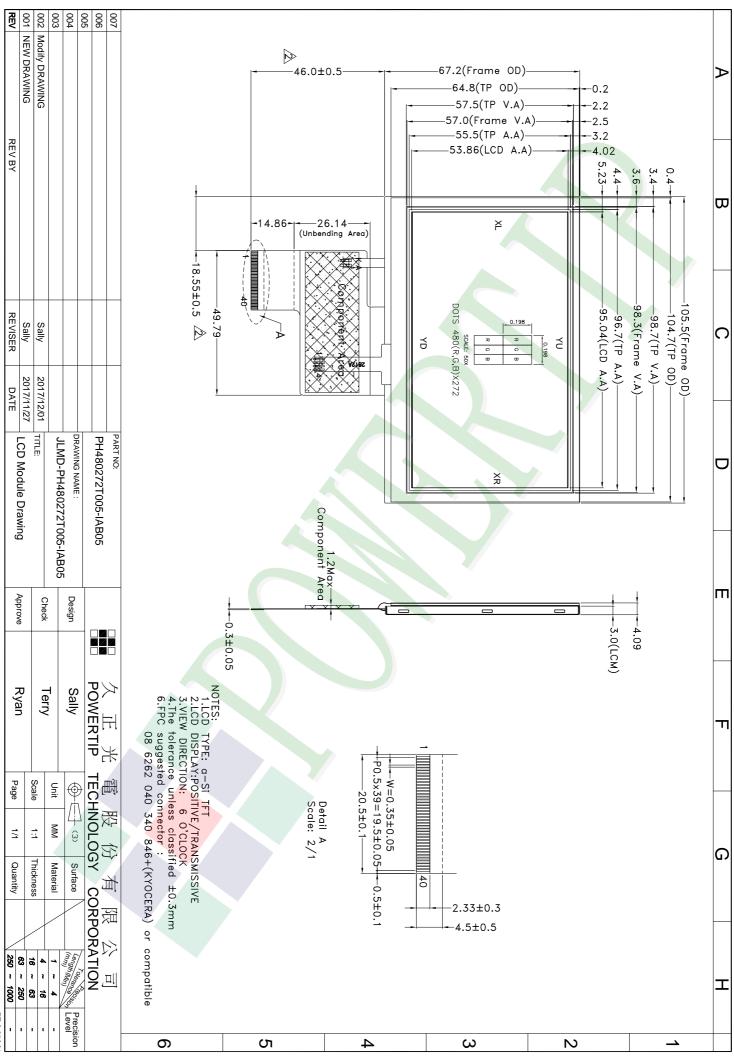
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

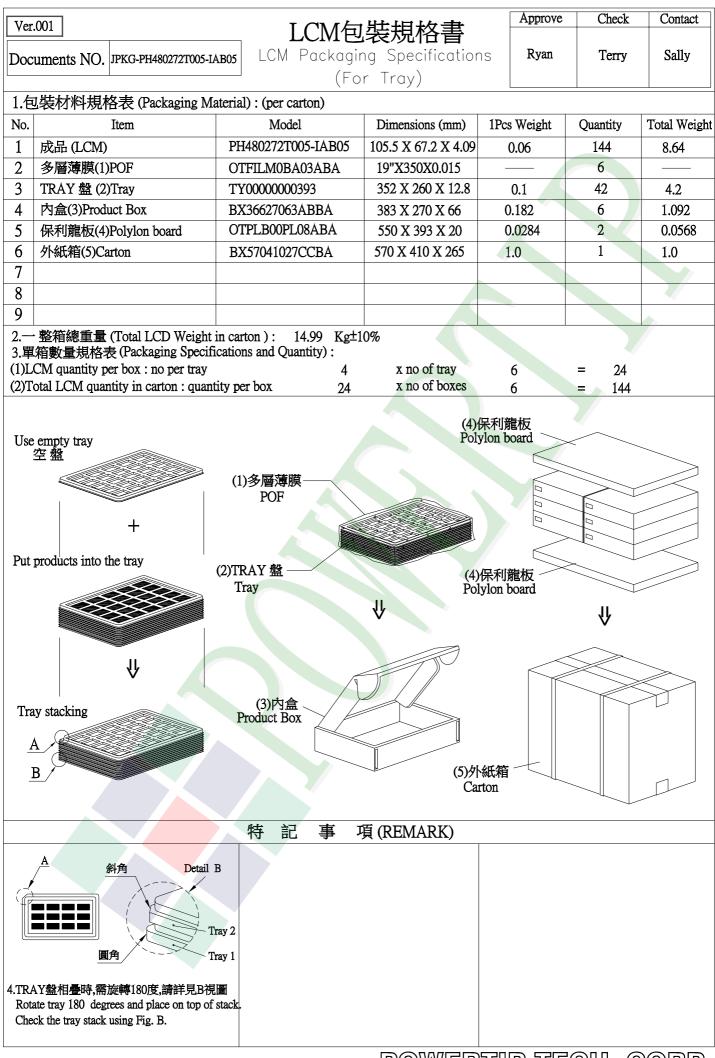
The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



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